

# Trilithium: Efficient and Universally Composable Distributed ML-DSA Signing

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**Abstract.** In this paper, we present Trilithium: a protocol for distributed key generation and signing compliant with FIPS 204 (ML-DSA). Our protocol allows two parties, “server” and “phone” with assistance of correlated randomness provider (CRP) to produce a standard ML-DSA signature. We prove our protocol to be secure against a malicious server or phone in the universal composability (UC) model, introducing some novel techniques to argue the security of two-party secure computation protocols with active security against one party, but only active privacy against the other. We provide an implementation of our protocol in Rust and benchmark it, showing the practicality of the protocol.

**Keywords:** ML-DSA · Crystals-Dilithium · distributed signing · MPC · Universal Composability · threshold signatures

## 1 Introduction

In summer of 2022, National Institute of Standards and Technology (NIST) announced a selection of algorithms to become future cryptographic standards<sup>3</sup>. For key establishment algorithm (KEM) category Crystals-Kyber (ML-KEM) [8] was selected; and for digital signatures – Crystals-Dilithium (ML-DSA) [35], Falcon (FN-DSA) [39] and SPHINCS<sup>+</sup> (SLH-DSA) [5] were chosen. In August 2024, NIST published a set of finalized standards, FIPS 203-205<sup>4</sup>. All selected schemes are considered to be post-quantum secure (also known as *quantum-safe*) i.e. resilient from both classical and quantum adversarial attacks<sup>5</sup>.

Parallel to the initiative on post-quantum cryptography, NIST published a draft of future call for multi-party threshold schemes [64]. The primary aim of this call is to construct advanced specifications for threshold schemes, including for NIST standardized and non-standardized cryptographic primitives. On top

<sup>3</sup> <https://csrc.nist.gov/Projects/post-quantum-cryptography/selected-algorithms-2022>

<sup>4</sup> <https://www.nist.gov/news-events/news/2024/08/nist-releases-first-3-finalized-post-quantum-encryption-standards>

<sup>5</sup> <https://www.etsi.org/technologies/quantum-safe-cryptography>

of the interest from standardization agencies, distributed signatures are already being used in real-life applications such as digital signing services <sup>6</sup>, distributed ledgers <sup>7</sup> and identity wallets <sup>8 9</sup>.

Over the past few years, substantial number of works have studied the topic of distributed signatures (both  $t$ -out-of- $n$  and  $n$ -out-of- $n$ ). For quantum-vulnerable schemes there exist efficient and practical distributed signing protocols: for RSA [43, 24, 11, 10, 19], ECDSA [41, 55, 30, 15, 29, 16, 13, 71, 53, 31, 49], probabilistic [62, 34, 60, 47, 54] and deterministic [61, 40, 37, 48] Schnorr signature schemes. There is also a fair amount of post-quantum distributed signature schemes [46, 17, 3, 22, 33], especially in the area of lattice-based protocols [26, 9, 38, 1, 42, 68, 44]. While many of them can be considered efficient, none of them are interoperable with new standards, except Cozzo and Smart [20] protocols. Still, their approach does not provide active security and directly could not be applied in real-life applications without substantial modifications.

Two-party protocols are an important use-case for threshold signing, having established a firm foothold in ecosystems with formalized frameworks of certification and assurance. Smart-ID <sup>10</sup> is a deployment of two-party RSA signatures, used by millions of residents of several European countries, and certified as a Qualified Signature Creation Device (QSCD) <sup>11</sup>. It is deployed as a central server, keeping one share of every user’s signing key, while the second share is managed by that user’s smartphone. It is interoperable with the rest of the ecosystem — the signatures created with the Smart-ID service are indistinguishable from any other RSA signatures and can be verified by anyone.

With the transition to quantum-safe primitives, currently deployed threshold schemes have to be replaced. While there are several pathways towards interoperability, certification-reliant ecosystems demand the use of standardized cryptosystems. As ML-DSA appears to be on its way to becoming the *default* postquantum-secure signature scheme <sup>12</sup>, we ask: *Can an efficient and actively secure distributed protocol for ML-DSA be constructed?* In this paper, we give an affirmative answer to this question.

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<sup>6</sup> <https://www.smart-id.com/>

<sup>7</sup> <https://github.com/coinbase/cb-mpc>

<sup>8</sup> <https://irma.app/docs/getting-started/>

<sup>9</sup> <https://github.com/cleverbase/scal3>

<sup>10</sup> <https://www.smart-id.com/>

<sup>11</sup> <https://www.smart-id.com/e-service-providers/smart-id-as-a-qscd/>

<sup>12</sup> <https://www.nist.gov/news-events/news/2024/08/nist-releases-first-3-finalized-post-quantum-encryption-standards>

*Our Contributions* <sup>13</sup>

1. We propose *Trilithium* <sup>14</sup>, a protocol for distributed ML-DSA key generation and signing, and prove it secure in the universal composability (UC) model.
  - We give a framework for actively *private* multiparty computation protocols in the UC model.
2. We provide an implementation and benchmarks of *Trilithium*.

**Related work**

While there exist numerous constructions of lattice-based threshold signature schemes, as referenced above, our techniques are rather unrelated to them. Our protocol is an instance of classical secret-sharing [51] based secure two-party / multi-party computation [72, 18, 4, 27], with proofs given in the universal composability model [12, 14].

While the execution of the ML-DSA key generation algorithm on top of a 2PC / MPC protocol set is relatively straightforward, if one can create *small* private values (which may be reduced to the conversion of private values between different moduli of sharing [63, 21]), the signature creation is trickier. Besides the generation of small private values (same as key generation), it has two main parts: high bits computation, and rejection sampling. The latter is mostly inequality checking, for which a number of MPC protocols have been proposed [57, 2, 74]; we are adapting [2] for our purposes. The high bits computation mostly consists of division (with public divisor) together with truncation / rounding of the result. Protocols for secure division have been proposed [7, 70, 69], but they are not much related to our techniques. Our division protocol is highly specific to the used divisor.

Our actively secure two-party protocols have subprotocols that provide active security against one of the parties, but only active *privacy* against the other party. This consideration of privacy as a separate goal is inspired by [6, 65]. Similar considerations are present in certain offline precomputation protocols of MPC protocol sets [36], but our methods of composition appear to be novel.

**2 Preliminaries****2.1 Notation**

Let  $a \stackrel{\$}{\leftarrow} A$  denote a uniformly random sampling of an element  $a$  from the set  $A$ . Symbol  $\perp$  is used to indicate a failure or rejection.  $\mathbb{Z}$  denotes the set of integers,  $\mathbb{N}$  the set of non-negative integers,  $\mathbb{Z}_q$  the set of integers modulo  $q$ . The set  $\{0, 1, \dots, n - 1\}$  is denoted by  $[n]$ . Let  $R_q$  denote the ring of polynomials

<sup>13</sup> Some results of this work were presented in [50]

<sup>14</sup> Since Crystals-Dilithium (ML-DSA submission name to NIST PQC competition) was inspired by Star Trek series, we decided to call our protocol Trilithium, a compound from Star Trek Generations

$\mathbb{Z}_q[X]/(X^n + 1)$ , where  $q$  is a prime number and  $n \in \mathbb{N}$ . We denote scalar values (including polynomials) by italic letters, vectors by bold lowercase letters, and matrices by bold uppercase letters.

For  $\alpha \in \mathbb{N}$  and  $x \in \mathbb{Z}$ , let  $x' = x \bmod^\pm \alpha$  denote centered modulo reduction, where  $x' \equiv x \pmod{\alpha}$  and either  $-\frac{\alpha}{2} < x' \leq \frac{\alpha}{2}$  (if  $\alpha$  is even) or  $-\frac{\alpha-1}{2} \leq x' \leq \frac{\alpha-1}{2}$  (if  $\alpha$  is odd).

For an element  $x \in \mathbb{Z}_q$ , its infinity norm is defined as  $\|x\|_\infty = |x \bmod^\pm q|$ , where  $|x|$  denotes the absolute value of the element. For an element  $p \in R_q$ , its infinity norm is defined as  $\|p\|_\infty = \max_i \|p_i\|_\infty$ . For a vector  $\mathbf{v}$ , it is  $\|\mathbf{v}\|_\infty = \max_i \|v_i\|_\infty$ .

Let  $v \in [B]$ . The *characteristic vector* of  $v$  is a vector  $\text{cv}(v) = (t_0, t_1, \dots, t_{B-1})$ , where  $t_v = 1$  and  $t_i = 0$  for all  $i \neq v$ . The length of  $\text{cv}(v)$ , i.e. the upper bound  $B$  will be clear from the context.

## 2.2 ML-DSA (Module-Lattice-based Digital Signature Algorithm)

Module-Lattice-Based Digital Signature Algorithm (ML-DSA) is a quantum-safe digital signature scheme. ML-DSA follows Fiat-Shamir with Aborts [56] paradigm and its security relies on hardness of MLWE, MSIS and SelfTargetMSIS problems.

Ducas et al. [35, Fig. 1] present a “template” signature scheme, which is later improved to Crystals-Dilithium and ML-DSA by significantly shortening the public key and adding some *hints* to signatures. The integrity properties of the “template” scheme are no worse than those of ML-DSA. The public key is shortened by simply cutting off some of its bits. Given a public key and a signature of the “template” scheme, one can compute the *hints* for ML-DSA, resulting in a valid ML-DSA signature. Hence it is sufficient to give a two-party protocol for key generation and signing in the “template” scheme; both parties locally store the public key of the “template” scheme resulting from the key generation protocol, publish it without the to-be-cut-off bits, and locally add the hints to the signatures produced by the signing protocol. In the following, let us describe the “template” scheme; see [35] and FIPS 204 [59] for the description of the full scheme.

The parameters of the “template” scheme are a prime number  $q$ , data sizes  $k, \ell, n, \tau$ , and value sizes  $\gamma_1, \gamma_2, \eta$ . Also define  $\alpha = 2\gamma_2$  and  $\beta = \eta \cdot \tau$ . The values must such that  $\alpha$  divides  $(q - 1)$ . We use the ring  $R_q = \mathbb{Z}_q[X]/(X^n + 1)$ . Let  $S_m \subseteq R_q$  [resp.  $\tilde{S}_m \subseteq R_q$ ] for  $m \in \mathbb{N}$  denote the set of polynomials with all coefficients between  $-m$  [resp.  $-m + 1$ ] and  $m$ . Let  $B_\tau \subseteq S_1$  denote the set of polynomials with exactly  $\tau$  non-zero coefficients.

Given a value  $w \in \mathbb{Z}_q = \{0, \dots, q - 1\}$ , we define  $\text{HighBits}(w) \in \mathbb{Z}_q$  as 0 if  $w' = q - 1$ , and  $\lfloor w'/\alpha \rfloor$  otherwise, where  $w' = (w + \alpha/2 - 1) \bmod q$ . This definition is equivalent to [35, Fig. 3]. Also equivalent is defining  $\text{LowBits}(w) := (w - \alpha \cdot \text{HighBits}(w)) \bmod q$ . One commonly uses superscript  $(\cdot)^H$  and  $(\cdot)^L$  for high and low bits. The definitions of high and low bits are extended from  $\mathbb{Z}_q$  to  $R_q$  coefficient-wise, and to vectors of polynomials component-wise.

```

Output: Keypair  $pk, sk$ 
 $\mathbf{A} \xleftarrow{\$} R_q^{k \times \ell};$  // Generated from a seed
 $(\mathbf{s}_1, \mathbf{s}_2) \xleftarrow{\$} S_\eta^\ell \times S_\eta^k$ 
 $\mathbf{t} \leftarrow \mathbf{A} \cdot \mathbf{s}_1 + \mathbf{s}_2$ 
return  $pk = (\mathbf{A}, \mathbf{t}), sk = (\mathbf{s}_1, \mathbf{s}_2)$ 

```

**Algorithm 1:** Key generation in “template” scheme

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Input: Keypair  $(\mathbf{A}, \mathbf{t}; \mathbf{s}_1, \mathbf{s}_2)$ , message digest  $\mu$ 
Output: Signature  $\sigma = (\mathbf{z}, c)$ , or  $\perp$ 
 $\mathbf{y} \xleftarrow{\$} \tilde{S}_{\gamma_1}^\ell;$  // Follows [59], not [35]
 $\mathbf{w} \leftarrow \mathbf{A} \cdot \mathbf{y}$ 
 $c \in B_\tau \leftarrow H(\mu, \mathbf{w}^H)$ 
 $\mathbf{z} \leftarrow \mathbf{y} + c \cdot \mathbf{s}_1$ 
 $\mathbf{r}_0 \leftarrow \text{LowBits}(\mathbf{w} - c \cdot \mathbf{s}_2)$ 
if  $\|\mathbf{z}\|_\infty < \gamma_1 - \beta$  and  $\|\mathbf{r}_0\|_\infty < \gamma_2 - \beta$  then
| return  $(\mathbf{z}, c)$ 
else
| return  $\perp$ 

```

**Algorithm 2:** Signing attempt in “template” scheme: SignAtt

The signature scheme employs “hashing into a ball”. It defines a hash function  $H$ , based on SHAKE [58], whose output is uniformly distributed in  $B_\tau$ . See [35, 59] for its precise description.

The key generation algorithm of the “simplified” scheme [35, Fig. 1] is given in Alg. 1. The algorithm for a single signing *attempt* is given in Alg. 2. If Alg. 2 returns  $\perp$ , then the signer is expected to rerun it, with newly selected  $\mathbf{y}$ . The ML-DSA specification has picked the parameters so, that the average number of reruns is 4 to 5. In this paper, we give a two-party protocol for Alg. 2. This does not reduce the security of our protocols — the frequency of retries is independent of the private key.

Our protocols also make use of the verification algorithm of the template scheme. The verification consists of making sure that  $\|\mathbf{z}\|_\infty < \gamma_1 - \beta$ , computing  $\mathbf{x}_0 \leftarrow \mathbf{A} \cdot \mathbf{z} - c \cdot \mathbf{t}$ , and checking that  $c = H(\mu, \mathbf{x}_0^H)$ . Note that  $\mathbf{x}_0 = \mathbf{w} - c \cdot \mathbf{s}_2$ .

Let us recall the values of the most important parameters in the three parameter sets of ML-DSA [59]:  $q = 2^{23} - 2^{13} + 1$ ,  $\alpha \in \{190464, 523776\}$ ,  $\eta \in \{2, 4\}$ ,  $\gamma_1 \in \{2^{17}, 2^{19}\}$ . We depend on certain properties of these values to make our protocols efficient.

### 2.3 Module Short Integer Solution

While the unforgeability of ML-DSA, and hence also the property of our protocols to constitute a secure threshold signature scheme is based on certain lattice-based hardness assumptions, we also “independently” use one of these assumptions to show that an adversarial party cannot cheat. Let us state it here. Given parameters  $q, n, m, \eta \in \mathbb{N}$ , the MSIS assumption [52] states that for a uniformly sampled  $\mathbf{A} \xleftarrow{\$} R_q^{n \times m}$  it is infeasible to find a *non-zero* vector  $\mathbf{x} \in R_q^{m+n}$ , such that  $[\mathbf{A}|\mathbf{I}] \cdot \mathbf{x} = \mathbf{0} \pmod{q}$  and  $\|\mathbf{x}\|_\infty \leq \eta$ .

### 2.4 Secure Multiparty Computation

Our protocol, Trilithium, is an instance of secret-sharing based secure multiparty computation (MPC) protocols, where the intermediate values have been additively shared (often modulo “the ML-DSA modulus”  $q$ ) between two parties: the *Phone* and the *Server*. In these protocols, a private value  $v \in \mathbb{Z}_q$  is stored as  $\llbracket v \rrbracket = (\llbracket v \rrbracket_{\mathbf{P}}, \llbracket v \rrbracket_{\mathbf{S}})$ , where  $v = \llbracket v \rrbracket_{\mathbf{P}} + \llbracket v \rrbracket_{\mathbf{S}}$ , Phone knows the random value  $\llbracket v \rrbracket_{\mathbf{P}} \in \mathbb{Z}_q$  and Server knows the value (also random)  $\llbracket v \rrbracket_{\mathbf{S}} \in \mathbb{Z}_q$ . We may write  $\llbracket v \rrbracket^{(q)}$  to emphasize that sharing is done modulo  $q$ .

There exist protocols for arithmetic operations with additively shared values. While linear (including affine) operations can be performed locally by Phone and Server, others require communication between parties. Such non-linear operations may benefit from some *correlated random values (CR)* distributed before the protocol start. For certain operations, e.g. multiplication, there exist protocols for generating CR [45]. Alternatively, some deployments may warrant the use of a trusted party, the *Correlated Randomness Provider (CRP)* to generate and distribute CR. In this paper, we have chosen the CRP approach.

Storing  $v$  as  $\llbracket v \rrbracket$  does not prevent a malicious party from tampering with its value by changing its share. It can be avoided with the help of (homomorphic) “*message authentication codes*” (MACs) [4, 27]. In this paper, we use so-called *BeDOZa-style MACs* [4], where the Phone and the Server have random private MAC keys  $\Delta_{\mathbf{P}}, \Delta_{\mathbf{S}}$ , respectively, and the value  $v$  is stored as  $\langle\langle v \rangle\rangle = (\llbracket v \rrbracket, \llbracket M_{\mathbf{S}} \rrbracket, \llbracket M_{\mathbf{P}} \rrbracket)$ , where  $M_{\mathbf{S}} = \llbracket v \rrbracket_{\mathbf{P}} \cdot \Delta_{\mathbf{S}}$  and  $M_{\mathbf{P}} = \llbracket v \rrbracket_{\mathbf{S}} \cdot \Delta_{\mathbf{P}}$ . We call  $M_{\mathbf{S}}$  “*Server’s MAC on  $\llbracket v \rrbracket$* ” and  $M_{\mathbf{P}}$  “*Phone’s MAC on  $\llbracket v \rrbracket$* ”. Affine operations with MAC-ed values  $\langle\langle v \rangle\rangle$  are still possible without communication.

Whenever the Phone  $\mathbf{P}$  sends its share  $\llbracket v \rrbracket_{\mathbf{P}}$  to a Server  $\mathbf{S}$ , it must accompany it with  $\llbracket M_{\mathbf{S}} \rrbracket_{\mathbf{P}}$ .  $\mathbf{S}$  is able to verify the correctness of the MAC by checking whether  $\llbracket v \rrbracket_{\mathbf{P}} \cdot \Delta_{\mathbf{S}} = \llbracket M_{\mathbf{S}} \rrbracket_{\mathbf{S}} + \llbracket M_{\mathbf{S}} \rrbracket_{\mathbf{P}}$ . If  $\mathbf{P}$  tries to change  $\llbracket v \rrbracket_{\mathbf{P}}$ , then its MAC share has to be correspondingly changed, and this task is as hard as guessing the random  $\Delta_{\mathbf{S}}$ . The probability of guessing is  $1/q$ ; and if this value is too large for given  $q$ , then several independent MAC keys  $\Delta$  and MACs can be used. Independent MAC keys  $\Delta^{(q_i)}$  are also used if the protocol makes use of several different moduli  $q_i$ . Instead of shares of the MACs, their digests can be communicated, hence MACs do not noticeably contribute to the online complexity of protocols.

While Trilithium two-party protocol (including an honest CRP) is secure against a malicious  $\mathbf{P}$  or a malicious  $\mathbf{S}$ , it includes subprotocols that by their

- On input  $(\text{corrupt}, \text{sid}, X)$  from  $\mathcal{S}$  before the initialization, mark that party  $X$  is corrupted and send  $(\text{corrupted}, \text{sid})$  to  $X$ .
- On input  $(\text{keygen}, \text{sid})$  from both  $\mathcal{P}$  and  $\mathcal{S}$ , use Alg. 1 to generate a new keypair  $(\mathbf{A}, \mathbf{t}; \mathbf{s}_1, \mathbf{s}_2)$ . Send  $(\text{pkey}, \text{sid}, \mathbf{A}, \mathbf{t})$  to  $\mathcal{S}$ . If neither party is corrupted, then receive  $(\text{proceed}, \text{sid})$  from the adversary, otherwise receive  $(\text{update}, \text{sid}, \mathbf{A}', \mathbf{t}')$  from the adversary. Send  $(\text{pkey}, \text{sid}, \mathbf{A}, \mathbf{t})$  to the honest party(s) in  $\mathcal{Z}$ , and  $(\text{pkey}, \text{sid}, \mathbf{A}', \mathbf{t}')$  to the corrupted party (if any). Accept no further  $(\text{keygen})$ -commands.
- On input  $(\text{sign}, \text{sid}, \mu)$  from both the  $\mathcal{P}$  and  $\mathcal{S}$ , compute  $\sigma \leftarrow \text{SignAtt}(\mathbf{A}, \mathbf{t}; \mathbf{s}_1, \mathbf{s}_2; \mu)$ . Send  $(\text{signature}, \text{sid}, \mu, \sigma)$  to  $\mathcal{S}$ . If neither party is corrupted, then receive  $(\text{proceed}, \text{sid})$  from the adversary, otherwise receive  $(\text{update}, \text{sid}, \sigma')$  from the adversary. Send  $(\text{signature}, \text{sid}, \sigma)$  to the honest party(s) in  $\mathcal{Z}$ , and  $(\text{signature}, \text{sid}, \sigma')$  to the the corrupted party (if any).

**Functionality 1:**  $\overline{\mathcal{F}}_{\text{ML-DSA}}$ : ideal functionality for the 2-party ML-DSA

own provide security only against a malicious  $\mathcal{P}$  and a semi-honest  $\mathcal{S}$ , while still providing *privacy* against a malicious  $\mathcal{S}$ . Here “*privacy*” means that nothing is learned from the *messages* exchanged by the protocol. However, a malicious  $\mathcal{S}$  may be able to change the outputs of such protocols, as well as learn something from its public outputs. In the following, we say that such (sub-)protocols are “*AP-secure*”, while we call protocols with active security against both parties “*AA-secure*”. This is made formal in Sec. 4. In our AP-secure protocols, we often use the representation  $\langle v \rangle = (\llbracket v \rrbracket, \llbracket M_{\mathcal{S}} \rrbracket)$  for private values.

Let us introduce notation to explicitly refer to the components of  $\langle v \rangle = (\llbracket v \rrbracket, \llbracket M_{\mathcal{S}} \rrbracket, \llbracket M_{\mathcal{P}} \rrbracket)$ , where  $M_{\mathcal{S}}$  and  $M_{\mathcal{P}}$  are Server’s and Phone’s MACs on  $\llbracket v \rrbracket$ . Denote the three components of  $\langle v \rangle$  by  $\mathcal{V}\langle v \rangle$ ,  $\mathcal{M}_{\mathcal{S}}\langle v \rangle$ , and  $\mathcal{M}_{\mathcal{P}}\langle v \rangle$  respectively. We may also write e.g.  $\mathcal{M}_{\mathcal{P}}\langle v \rangle_{\mathcal{S}}$  for  $\llbracket M_{\mathcal{P}} \rrbracket_{\mathcal{S}}$ . The notation  $\mathcal{V}\langle v \rangle$  and  $\mathcal{M}_{\mathcal{S}}\langle v \rangle$  has similar meaning. We will use this notation in the security proofs, in order to avoid introducing separate notations for the components of secret-shared values.

## 2.5 Universal Composability

We prove the security of our protocols in the *Universal Composability (UC)* framework [12] (our notation is more similar to the functionally equivalent *reactive simulatability* framework [66]). Our real system  $\Pi_{\text{ML-DSA}}$  and ideal functionality  $\overline{\mathcal{F}}_{\text{ML-DSA}}$  (given in

Func. 1) expose an interface to the environment  $\mathcal{Z}$  that corresponds to two parties, called  $\mathcal{P}$ (hone) and  $\mathcal{S}$ (erver) being able to run a single instance of key generation, and any number of signings. We will construct the protocol  $\Pi_{\text{ML-DSA}}$  and show that it *securely implements*  $\overline{\mathcal{F}}_{\text{ML-DSA}}$ , meaning that for each adversary  $\mathcal{A}$  against it, there exists an adversary  $\mathcal{S}$ , such that no environment  $\mathcal{Z}$  can distinguish whether it is running in the collection  $\mathcal{Z} \parallel \Pi_{\text{ML-DSA}} \parallel \mathcal{A}$  or  $\mathcal{Z} \parallel \overline{\mathcal{F}}_{\text{ML-DSA}} \parallel \mathcal{S}$ . We demonstrate the existence of  $\mathcal{S}$  by constructing a *simulator*  $\text{Sim}$  and showing that  $\mathcal{S} = \text{Sim} \parallel \mathcal{A}$  is a suitable choice. This is shown by arguing that no  $\mathcal{Z} \parallel \mathcal{A}$  can distinguish  $\Pi_{\text{ML-DSA}}$  from  $\overline{\mathcal{F}}_{\text{ML-DSA}} \parallel \text{Sim}$ .

In our proof, we make heavy use of composability: if an ideal functionality  $\mathcal{F}'$  is a component in the protocol  $\Pi$  that securely implements the ideal functionality  $\mathcal{F}$ , and if  $\Pi'$  is a secure implementation of  $\mathcal{F}'$ , then  $\Pi$ , where  $\mathcal{F}'$  is replaced with  $\Pi'$ , also securely implements  $\mathcal{F}$ . We say that  $\Pi$  securely implements  $\mathcal{F}$  in  $\mathcal{F}'$ -hybrid model. In particular, the CRP will be modeled as a set of ideal functionalities.

A protocol  $\Pi$  is hence implemented by a system consisting of several executing components. In our setting, this system always contains two machines,  $\mathcal{M}_P$  and  $\mathcal{M}_S$ , executing the protocol logic on behalf of  $P$  and  $S$ . These machines expose an interface to the environment  $\mathcal{Z}$ . Besides these two machines, the system may also contain a number of *ideal components*  $\mathcal{F}_i$ . These components may correspond to subprotocols, or to the generation of CR. In practice, the former are replaced with their secure implementations, while the latter are collected to another party — the CRP.

The machines  $\mathcal{M}_P$  and  $\mathcal{M}_S$  take commands from the environment, send messages to each other, issue commands to the ideal components in  $\Pi$ , receive answers from them, and give answers back to the environment. One of  $\mathcal{M}_P$  and  $\mathcal{M}_S$  may receive the `corrupt`-command from the adversary at the beginning of the execution. In this case, this machine  $\mathcal{M}$  hands all control over to the adversary: it sends to the adversary all commands / messages / answers it receives from the environment, the other machine or the ideal components, and it will send out only the answers / messages / commands as told by the adversary. Note that the latter includes the answer that  $\mathcal{M}$  sends to the environment.

### 3 Trilithium: Server-Supported ML-DSA Signature Scheme

In this section, we define Trilithium and describe our subprotocols, using the notation typical for MPC protocols. Our descriptions are bottom-up, starting with small operations and finishing with the protocols for key generation and signing. We give informal correctness and security arguments together with the descriptions; formal security arguments are given in the next section.

In our protocols, we make use of the protocol `randbit()` that takes no inputs and returns  $\langle\langle b \rangle\rangle^{(q)}$  for a random  $b \in \{0, 1\}$  and a modulus  $q$  that will be clear from the context. We use the bit conversion protocol `convbit( $\langle\langle b \rangle\rangle^{(2)}$ )` that takes a bit  $b$  shared modulo 2, and returns  $\langle\langle b \rangle\rangle^{(q)}$  — the same bit shared modulo  $q$ . Obviously, we also make use of protocols for adding and multiplying shared values, and for generating public random values modulo some prime number  $Q$  (called `pubrand()`). All these protocols are standard, and described e.g. in [73]. We use the CRP for any correlated randomness that these protocols require.

#### 3.1 Declassification

Let us start with the *declassification* protocol `declassify` given in Alg. 3. As discussed in Sec. 2.4, it involves the checking of MACs, using the MAC keys  $\Delta_P^{(q)}$



**Input:**  $\langle\langle v \rangle\rangle = (\llbracket v \rrbracket, \llbracket M_S \rrbracket, \llbracket M_P \rrbracket)$ , where  $v \in \mathbb{Z}_q$ ,  $M_S = \llbracket v \rrbracket_P \cdot \Delta_S$  and  $M_P = \llbracket v \rrbracket_S \cdot \Delta_P$

**Output:**  $v$

- 1 **S**  $\rightarrow$  **P**:  $\llbracket v \rrbracket_S$  and  $\llbracket M_P \rrbracket_S$
- 2 **P**: **stop** if  $\llbracket v \rrbracket_S \cdot \Delta_P - \llbracket M_P \rrbracket_P \neq \llbracket M_P \rrbracket_S$
- 3 **P**  $\rightarrow$  **S**:  $\llbracket v \rrbracket_P$  and  $\llbracket M_S \rrbracket_P$
- 4 **S**: **stop** if  $\llbracket v \rrbracket_P \cdot \Delta_S - \llbracket M_S \rrbracket_S \neq \llbracket M_S \rrbracket_P$
- 5 **P** and **S**: **return**  $\llbracket v \rrbracket_P + \llbracket v \rrbracket_S$

**Algorithm 3:** Declassification protocol: declassify

**Par:**  $Q$  — a small prime number

**Output:**  $\langle\langle v \rangle\rangle^{(q)}$ ,  $v \stackrel{\$}{\leftarrow} \{0, \dots, Q-1\}$

**CR:**  $\langle\langle t \rangle\rangle^{(q)}$ , s.t.  $|t| = Q$ ,  $t$  is a one-hot 0-1 vector

- 1  $r \leftarrow \text{pubrand}() \in \mathbb{Z}_Q$
- 2 **return**  $\sum_{i=0}^{Q-1} i \cdot \langle\langle t_{(i+r) \bmod Q} \rangle\rangle$

**Algorithm 4:** Generation of a small random value:  $\Pi_{\text{gen\_small}}$

and  $\Delta_S^{(q)}$  for the modulus  $q$ . The inputs to the declassification protocol are the shares  $\llbracket v \rrbracket$  of the value  $v$ , and the shares of Server’s MAC  $M_S$  and Phone’s MAC  $M_P$  for this sharing  $\llbracket v \rrbracket$ .

For a small modulus  $q$ , the shares of MACs would make up the lion’s share of the communication. In our actual implementations, the shares of MACs are actually not sent. Instead, the hash of all MAC shares to be sent in the current round is computed, and only this hash value is sent to the other party. The other party can compute the same hash from the values it holds, and verify that it equals the one it received.

We see that Alg. 3 is asymmetric: **P** learns the value  $v$  first. This will later be reflected in our security proofs.

Alg. 3 is also a good description of the declassification of  $\langle\langle v \rangle\rangle$ , if we leave out the checks related to **P**’s MAC. We get the AP-declassification by removing  $M_P$  and line 2 from Alg. 3.

### 3.2 Randomness generation

The protocol  $\text{gen\_small}$ , parametrized by a small prime number  $Q$  and given in Alg. 4, takes no inputs and returns a uniform random value  $\langle\langle v \rangle\rangle^{(q)}$ , such that  $0 \leq v < Q$ . The correlated randomness used by this algorithm is a private 0-1 vector, where the position of the only value “1” is randomly selected by the CRP, and unknown to **P** and **S**.

In the rest of Sec. 3 we distinguish between parameters (fixed at compile-time) and inputs (known only at run-time) to a protocol. We write  $f[c](a)$  to denote the invocation of a protocol  $f$  with parameter  $c$  and input  $a$ . We may omit the parameters if they’re clear from the context. The correlated randomness **CR** used by the protocols is assumed to be uniformly distributed, subject to the listed constraints.

**Par:** ML-DSA parameters  $k, \ell, \eta$   
**Input:** The matrix  $\mathbf{A}$  of the public key  
**Output:** The private key  $\langle\langle \mathbf{s}_1 \rangle\rangle, \langle\langle \mathbf{s}_2 \rangle\rangle$

```

1 foreach  $i \in [k + \ell], t \in [256]$  do
2   if  $\eta = 2$  then
3      $\langle\langle r_{i,t} \rangle\rangle \leftarrow \text{gen\_small}[5]() - 2$ 
4   else //  $\eta = 4$ 
5      $\langle\langle r_{i,t} \rangle\rangle \leftarrow 3 \cdot \text{gen\_small}[3]() + \text{gen\_small}[3]() - 4$ 
6  $\langle\langle \mathbf{s}_1 \rangle\rangle \leftarrow (\sum_{t=0}^{255} r_{i-1,t} X^t)_{i=1}^{\ell}$ 
7  $\langle\langle \mathbf{s}_2 \rangle\rangle \leftarrow (\sum_{t=0}^{255} r_{i+\ell-1,t} X^t)_{i=1}^k$ 
8 return  $\langle\langle \mathbf{s}_1 \rangle\rangle, \langle\langle \mathbf{s}_2 \rangle\rangle$ 

```

**Algorithm 5:** ML-DSA private key generation: keygen

### 3.3 Private key generation

Generation of private small random values is all that is needed for ML-DSA key generation. The protocol `keygen` is given in Alg. 5. It generates the necessary number of random values and arranges them as coefficients of polynomials (with  $X$  denoting the formal variable). Here and in the following, we do not make explicit whether polynomials are represented coefficient-wise or in the Number Theoretic Transform (NTT) representation. Note that the conversion between the two is a linear operation. Our implementation converts wherever reasonable. In particular, the multiplications of polynomials are done using NTT.

It would perhaps be natural to have Alg. 5 also contain public key generation, starting from the generation of a random matrix  $\mathbf{A}$ , and finishing with the (linear) computation of  $\langle\langle \mathbf{t} \rangle\rangle \leftarrow \mathbf{A} \cdot \langle\langle \mathbf{s}_1 \rangle\rangle + \langle\langle \mathbf{s}_2 \rangle\rangle$  and its declassification. For the uniformity of our security arguments (Sec. 4), these steps are not included here. But they are part of  $\Pi_{\text{ML-DSA}}$ .

### 3.4 Zero equality checking protocol

A necessary sub-protocol in distributed signing is the equality check for values shared modulo  $q$ . This is equivalent to checking whether a value is 0: the input to `zero_check`, given in Alg. 6 is a value  $\langle\langle v \rangle\rangle$ , and the output is  $\langle\langle b \rangle\rangle$ , where  $b = 1$  iff  $v = 0$ . While protocols for this task have been proposed before [23], our protocol is simplified by the guarantee  $v < B$  for a small upper bound  $B$  parametrizing the `zero_check` protocol. With this guarantee, we can keep the size of the correlated randomness proportional to  $B$ , not to the much larger modulus  $q$ .

Let us show that if  $v < B$  then Alg. 6 indeed produces the correct result. We have denoted  $a = \lfloor q/B \rfloor$ . The vector  $\mathbf{t}$  is constructed so that the bit  $t_{\lfloor m/a \rfloor}$  is equal to 1, while all other bits are equal to 0. There are two cases.

**Case 1:**  $v = 0$ . Then  $d = m$ . The protocol outputs the secret-shared bit  $t_{\lfloor m/a \rfloor}$ . By the construction of the vector  $\mathbf{t}$ , this bit is equal to 1.

**Case 2:**  $v \neq 0$ . The protocol outputs the secret-shared bit  $t_{\lfloor d/a \rfloor}$ . If we consider all values as integers (i.e. elements of  $\mathbb{Z}$ , not elements of  $\mathbb{Z}_q$ ), then we can write

**Par:**  $B \in \mathbb{N}$ . Denote  $a = \lfloor q/B \rfloor$   
**Input:**  $\langle\langle v \rangle\rangle^{(q)}$ , where  $v < B$   
**Output:**  $\langle\langle b \rangle\rangle^{(q)}$ ,  $b \in \{0, 1\}$ ,  $b = 1 \Leftrightarrow v = 0$   
**CR:**  $\langle\langle m \rangle\rangle^{(q)}$ .  $\langle\langle \mathbf{t} \rangle\rangle^{(q)}$ , s.t.  $|\mathbf{t}| = B + 1$ ,  $\mathbf{t} = \text{cv}(\lfloor m/a \rfloor)$   
1  $d \leftarrow \text{declassify}(\langle\langle m \rangle\rangle + a \cdot \langle\langle v \rangle\rangle)$   
2 **return**  $\langle\langle t_{\lfloor d/a \rfloor} \rangle\rangle$

**Algorithm 6:** Checking whether a value is 0: `zero_check`

$\lfloor \frac{d}{a} \rfloor = \lfloor \frac{(m+av) \bmod q}{a} \rfloor$ . We have  $m < q$  and  $av \leq \frac{q}{B} \cdot v < \frac{q}{B} \cdot B = q$ . Hence  $0 \leq m + av < 2q$ . Let us consider two cases.

**Case 2a:**  $m + av < q$ . Then  $\lfloor \frac{d}{a} \rfloor = \lfloor \frac{m+av}{a} \rfloor = \lfloor \frac{m}{a} \rfloor + v \neq \lfloor \frac{m}{a} \rfloor$ . Hence by the construction of the vector  $\mathbf{t}$ , the bit  $t_{\lfloor d/a \rfloor}$  is equal to 0.

**Case 2b:**  $q \leq m + av < 2q$ . Then  $d = m + av - q \leq m + \frac{q}{B} \cdot (B - 1) - q \leq m + (q - a) - q = m - a$ . Hence  $\lfloor \frac{d}{a} \rfloor \leq \lfloor \frac{m-a}{a} \rfloor = \lfloor \frac{m}{a} \rfloor - 1$ . We obtain  $\lfloor d/a \rfloor \neq \lfloor m/a \rfloor$ , hence the bit  $t_{\lfloor d/a \rfloor}$  is equal to 0.

### 3.5 Symmetric reshare to parts

In order to compute the *high bits* of a value  $w \in \mathbb{Z}_q$ , we have to decompose it into high and low bits. Consider a vector  $\mathbf{r} = (r_0, r_1, \dots, r_{n-1}) \in \mathbb{N}^n$ , such that  $R_n \geq 2q$ , where we denote  $R_0 = 1$  and  $R_i = R_{i-1} \cdot r_{i-1}$ . Given a value  $v \in [2q]$ , there exist unique  $v_0, v_1, \dots, v_{n-1}$ , such that  $0 \leq v_i < r_i$  and  $v = \sum_{i=0}^{n-1} R_i \cdot v_i$ . We call  $(v_0, \dots, v_{n-1})$  the *representation of  $v$  in the mixed-radix base  $\mathbf{r}$* , and call the individual values  $v_i$  the *digits* of  $v$  (wrt.  $\mathbf{r}$ ).

Our protocol `symm_rp`, called “*symmetric reshare to parts*” and parametrized by a mixed-radix basis  $\mathbf{r}$ , takes as input a shared value  $\langle\langle w \rangle\rangle$  and returns the secret-shared digits of  $w$  in the representation of their characteristic vectors (CV) (where the length of the CV of the  $i$ -th digit is  $r_i$ ). Actually, the digits may correspond either to  $w$  or to  $w + q$ ; this is chosen randomly by `symm_rp`, given in Alg. 7.

We see that Alg. 7 begins by making public the value  $y = (w - s) \bmod q$ . The loop that follows basically adds the values  $y$  and  $s$ , thus explaining why the result randomly corresponds to either  $w$  or  $w + q$  (but never to  $2q - 1$ ). This addition is digit-wise. To add a digit with the CV  $\mathbf{d}_i$ , and the digit  $z_i$ , we rotate the vector to the right by  $z_i$  positions, thereby increasing the index of the position with “1” on it by  $z_i$ . As the length of  $\mathbf{d}_i$  is  $2r_i$ , this rotation (also with  $z_i + 1$ ) cannot move the element 1 of the vector over its right end. The rotation with  $z_i$  corresponds to no carry coming into the  $i$ -th digit, while the rotation with  $z_i + 1$  corresponds to the incoming carry. The actual carry bit,  $c_i$ , is used to *obviously choose* between the two possibilities. The oblivious choice operation,  $b ? x : y$  is defined to be  $x$  if  $b = 1$ , and  $y$  if  $b = 0$ . It can be computed as  $y + b \cdot (x - y)$ . In Alg. 7 we use it for vector-valued  $x$  and  $y$ ; in our implementation, the CR we use is for scalar-vector multiplication.

During this addition, we have an outgoing carry at the  $i$ -th digit if that digit is at least  $r_i$ , hence we find that carry by summing up the indicator bits of the

**Par:**  $\mathbf{r} = (r_0, \dots, r_{n-1})$ , where  $\prod r_i \geq 2q$   
**Input:**  $\langle\langle w \rangle\rangle^{(a)}$   
**Output:**  $\langle\langle \mathbf{b}_0 \rangle\rangle^{(a)}, \dots, \langle\langle \mathbf{b}_{n-1} \rangle\rangle^{(a)}$ , CV-s of digits of either  $w$  or  $w + q$  (but never of  $2q - 1$ )  
**CR:**  $\langle\langle s \rangle\rangle^{(a)}, \langle\langle \mathbf{d}_0 \rangle\rangle^{(2)}, \dots, \langle\langle \mathbf{d}_{n-1} \rangle\rangle^{(2)}$ : CV-s of digits of  $s$   
**1**  $\forall i$ : append  $\langle\langle \mathbf{d}_i \rangle\rangle$  with  $\langle\langle 0 \rangle\rangle$ -s, s.t. its length becomes  $2r_i$   
**2**  $y \leftarrow \text{declassify}(\langle\langle w \rangle\rangle - \langle\langle s \rangle\rangle)$   
**3**  $(z_0, \dots, z_{n-1}) \leftarrow$  the digits of  $y$   
**4** **for**  $i = 0$  **to**  $n - 1$  **do**  
**5**      $\langle\langle \mathbf{p}'_i \rangle\rangle \leftarrow \text{rotate\_right}(\langle\langle \mathbf{d}_i \rangle\rangle, z_i)$   
**6**      $\langle\langle \mathbf{p}''_i \rangle\rangle \leftarrow \text{rotate\_right}(\langle\langle \mathbf{d}_i \rangle\rangle, z_i + 1)$   
**7**      $\langle\langle \mathbf{p}_i \rangle\rangle \leftarrow$  **if**  $i = 0$  **then**  $\langle\langle \mathbf{p}_0 \rangle\rangle$  **else**  $\langle\langle c_i \rangle\rangle ? \langle\langle \mathbf{p}'_i \rangle\rangle : \langle\langle \mathbf{p}''_i \rangle\rangle$ ;  
**8**      $\langle\langle c_{i+1} \rangle\rangle \leftarrow \sum_{j=r_i}^{2r_i-1} \langle\langle p_{i,j} \rangle\rangle$   
**9**     **foreach**  $i \in [n], j \in [r_i]$  **do**  
**10**      $\langle\langle b_{i,j} \rangle\rangle^{(a)} \leftarrow \text{convbit}(\langle\langle p_{i,j} \rangle\rangle^{(2)} + \langle\langle p_{i,j+r_i} \rangle\rangle^{(2)})$   
**11** **return**  $\langle\langle \mathbf{b}_0 \rangle\rangle, \dots, \langle\langle \mathbf{b}_{n-1} \rangle\rangle$   
**Algorithm 7:** Symmetric reshare to parts: `symm_rp`

upper half of the vector  $\mathbf{p}_i$ . The final value of that digit comes either from the lower (if there was no carry) or the upper (if there was carry) half of the vector  $\mathbf{p}_i$ .

In our implementation, we first propagate all carries as in carry-lookahead adders [32], and only afterwards compute the vectors  $\mathbf{p}_i$ . We use binomial tuples [67] to compute the carries in a single round of communication. In this way, the main loop only requires two communication rounds, instead of  $n - 1$ . The binomial tuples will not become too large, because we use  $n = 5$ .

### 3.6 Distributed HighBits decomposition

The computation of the high bits of a secret-shared value is performed by the `high_bits` protocol, given in Alg. 8. The algorithm begins by shifting  $w$  by  $\alpha/2 - 1$ . Indeed, according to the definition of high bits, we have  $w^H = \lfloor w'/\alpha \rfloor$ , unless  $w' = q - 1$ , in which case  $w^H = 0$ . We continue by splitting  $w'$  (or  $w' + q$ ) into five digits, let us call these digits  $d_0, \dots, d_4$ , their characteristic vectors are  $\mathbf{b}_0, \dots, \mathbf{b}_4$ .

Note that  $r_0 \cdots r_3 = \alpha$ . Hence, if the digits correspond to  $w'$ , then  $\lfloor w'/\alpha \rfloor$  is “basically”  $d_4$ . If the digits correspond to  $w' + q$ , then we have added the digits of  $q$  to the digits of  $w'$  to obtain  $d_0, \dots, d_4$ ; the digits of  $q$  are  $(1, 0, 0, 0, s)$ . To handle both cases, we should return  $d_4$  if  $d_4 < s$ , and return  $d_4 - s$  otherwise. This can be computed from  $\mathbf{b}_4$  by finding its inner product with the constant vector  $(0, 1, \dots, s - 1, 0, 1, 2, \dots)$ .

There are exceptions, though. One of them is  $w' = q - 1$ , in which case the digits are  $(0, 0, 0, 0, s)$ . We see that this is actually not an exception; we would return  $d_4 - s = 0$  as we should. A true exception is that if the digits happen to be  $(0, 0, 0, 0, s')$ , where  $s' > s$ , then we should not return  $s' - s$ , but  $s' - s - 1$ . We

**Par:** ML-DSA parameter  $\alpha = 2 \cdot \gamma_2$ . Let  $s = (q - 1)/\alpha$   
 Let  $\mathbf{r} = (r_0, \dots, r_4) = \begin{cases} (31, 24, 16, 16, 2s + 1), & \text{if } \alpha = 190464 \\ (31, 33, 32, 16, 2s + 1), & \text{if } \alpha = 523776 \end{cases}$

**Input:**  $\langle\langle w \rangle\rangle$

**Output:**  $\langle\langle v \rangle\rangle$ , s.t.  $v = w^H$

- 1  $\langle\langle w' \rangle\rangle \leftarrow \langle\langle w \rangle\rangle + (\alpha/2 - 1)$
- 2  $(\langle\langle \mathbf{b}_0 \rangle\rangle, \dots, \langle\langle \mathbf{b}_4 \rangle\rangle) \leftarrow \text{symm\_rp}[\mathbf{r}](\langle\langle w' \rangle\rangle)$
- 3  $\langle\langle f \rangle\rangle \leftarrow \sum_{i=0}^3 \langle\langle b_{i,0} \rangle\rangle + \sum_{i=s+1}^{2s} \langle\langle b_{4,i} \rangle\rangle$
- 4  $\langle\langle c \rangle\rangle \leftarrow \text{zero\_check}[6](5 - \langle\langle f \rangle\rangle)$
- 5 **return**  $(\sum_{i=0}^{s-1} i \cdot \langle\langle b_{4,i} \rangle\rangle) + (\sum_{i=s}^{2s} (i - s) \cdot \langle\langle b_{4,i} \rangle\rangle) - \langle\langle c \rangle\rangle$

**Algorithm 8:** Finding the high bits of a value: `high_bits`

**Par:** ML-DSA parameters  $k, \ell, \alpha, \gamma_1$

**Input:**  $\mathbf{A}$

- 1 **foreach**  $i \in [\ell]$ ,  $t \in [256]$ ,  $u \in [(\log \gamma_1) + 1]$  **do**
- 2 |  $\langle\langle b_{i,t,u} \rangle\rangle \leftarrow \text{randbit}()$
- 3  $\langle\langle \mathbf{y} \rangle\rangle \leftarrow (\sum_{t=0}^{255} (-\gamma_1 + \sum_{u=0}^{\log \gamma_1} 2^u \cdot \langle\langle b_{i-1,t,u} \rangle\rangle) \cdot X^t)_{i=1}^\ell$
- 4  $\langle\langle \mathbf{w} \rangle\rangle = (\sum_{t=0}^{255} \langle\langle w_{i-1,t} \rangle\rangle \cdot X^t)_{i=1}^k \leftarrow \mathbf{A} \cdot \langle\langle \mathbf{y} \rangle\rangle$
- 5 **foreach**  $i \in [k]$ ,  $t \in [256]$  **do**
- 6 |  $\langle\langle w_{i,t}^H \rangle\rangle \leftarrow \text{high\_bits}(\langle\langle w_{i,t} \rangle\rangle)$
- 7 **return**  $\langle\langle \mathbf{y} \rangle\rangle, \langle\langle \mathbf{w}^H \rangle\rangle$

**Algorithm 9:** Computing the FSwa commitment: `gcom`

check this by adding up the bits indicating this situation into  $f$ , and checking that all these bits are 1 (i.e.  $5 - f$  is zero).

We point out that the feasibility of Alg. 8 very much relies on the *smoothness* of  $\alpha$ : we have managed to represent  $\alpha$  as the product of a few numbers, none of which are too big to have a vector of private values of this length. This smoothness is not implied by the requirements of ML-DSA: while  $q-1$  is required to be divisible by a large power of 2 to enable NTT, the construction of ML-DSA puts no further requirements on the odd prime factors of  $q-1$ .

With the introduced algorithms, the computation of the commitment  $\mathbf{w}^H$  and the secrets corresponding to it is straightforward. We record it in Alg. 9. NTT is used for the multiplication of polynomials. The vector of polynomials  $\langle\langle \mathbf{w}^H \rangle\rangle$  will be declassified outside Alg. 9, the declassification is not included here for the same reason as in Alg. 5.

### 3.7 Arguments to rejection sampling

With  $\mathbf{w}^H$ , we can compute the challenge  $c$  and use it to compute the response  $\langle\langle \mathbf{z} \rangle\rangle = \langle\langle \mathbf{y} \rangle\rangle + c \cdot \langle\langle \mathbf{s}_1 \rangle\rangle$ . Before releasing  $\langle\langle \mathbf{z} \rangle\rangle$ , we have to perform the rejection check on it. We also have to perform the rejection check on  $\langle\langle \mathbf{r}_0 \rangle\rangle = \text{LowBits}(\langle\langle \mathbf{w} \rangle\rangle - c \cdot \langle\langle \mathbf{s}_2 \rangle\rangle)$ . A rejection check makes sure that the value belongs to a segment. The value and the segment can be shifted, such that the segment starts from 0, hence

the rejection check is an inequality check against a constant, for which we have chosen to adapt the efficient two-party protocol (with CRP) by Attrapadung et al. [2]. Their protocol works for rings  $\mathbb{Z}_{2^n}$ , and has only passive security. We have managed to adapt it to rings  $\mathbb{Z}_q$ , and to AP-security. This turns out to be sufficient for an AA-secure ML-DSA protocol, as we explain below.

The computation of  $\langle\langle \mathbf{r}_0 \rangle\rangle$  may be complicated due to the need to find the low bits. Fortunately, it turns out that  $\mathbf{r}_0$  passes the rejection check iff  $\mathbf{x} = \text{LowBits}(\mathbf{w}) - c \cdot \mathbf{s}_2$  passes it. The sharing  $\langle\langle \mathbf{x} \rangle\rangle$  is easy to compute, because  $\langle\langle \mathbf{w}^L \rangle\rangle = \text{LowBits}(\langle\langle \mathbf{w} \rangle\rangle)$  is a linear combination of  $\langle\langle \mathbf{w} \rangle\rangle$  and  $\langle\langle \mathbf{w}^H \rangle\rangle$ .

Let us show that  $\mathbf{r}_0 \leftarrow \text{LowBits}(\mathbf{w} - c \cdot \mathbf{s}_2)$  passes the rejection check iff  $\mathbf{x} = \text{LowBits}(\mathbf{w}) - c \cdot \mathbf{s}_2$  passes it with the same upper bound  $\gamma_2 - \beta$ . By the definition of  $\text{LowBits}$ , a coefficient of an element of  $\mathbf{r}_0$  is between  $-\gamma_2$  and  $\gamma_2$ . The rejection check passes if all coefficients are actually between  $-\gamma_2 + \beta + 1$  and  $\gamma_2 - \beta - 1$ .

A coefficient of an element of  $\mathbf{x}$  may be outside the range  $[-\gamma_2, \gamma_2]$ ; it is between  $-\gamma_2 - \beta$  and  $\gamma_2 + \beta$ . In order to transform  $\mathbf{x}$  to  $\mathbf{r}_0$ , we have to add or subtract  $2\gamma_2$  to all coefficients that are outside the range  $[-\gamma_2, \gamma_2]$ . If some coefficient in  $\mathbf{x}$  is currently outside  $[-\gamma_2, \gamma_2]$ , then the addition or subtraction will turn it to a value that will be rejected by the rejection check. Hence the result of rejection-checking  $\mathbf{r}_0$  will be the same as rejection-checking  $\mathbf{x}$ .

### 3.8 Splitting shares to digits

An important step in [2] is locally splitting a share  $\llbracket v \rrbracket_{\mathbb{P}}$  or  $\llbracket v \rrbracket_{\mathbb{S}}$  into smaller digits, and then thinking of these smaller digits as shares of (smaller) values and working with them. For passive security and rings  $\mathbb{Z}_{2^n}$ , this splitting is basically a non-operation (as long as the basis elements are powers of 2, too), taking certain bits from the shares. With active security and MACs on the shares, we need the digits of the shares to obtain MACs, too. We cannot do the splitting so, that the digits of both Phone's and Server's share get the MACs, but we can give MACs to digits of one party's share. This is done by the protocol `asymm_rp`, given in Alg. 10. Similarly to Alg. 7, it takes a radix basis as a parameter. To simplify this, we let all radices be equal to  $r$ . Let  $\text{split}[r, d](v)$  return the length- $d$  vector of digits of the value  $v$ . Its inverse is  $\text{split}^{-1}[r, d](\mathbf{v}) = \sum_{i=0}^{d-1} r^i \cdot v_i$ .

The input of the Alg. 10 is a value  $\langle v \rangle = (\llbracket v \rrbracket, \llbracket \llbracket v \rrbracket_{\mathbb{P}} \cdot \Delta_{\mathbb{S}} \rrbracket)$ , where we have denoted the second component with  $\llbracket V \rrbracket$ . The correlated randomness contains a random  $m$  known only to  $\mathbb{P}$ , and Server's MACs on both  $m$  and  $\text{split}(m)$ , denoted  $M$  and  $\mathbf{r}$ , respectively. In the end result,  $\text{split}(m)$  will be Phone's share, while Server adjusts its share (before splitting it) by the difference of  $\llbracket v \rrbracket_{\mathbb{P}}$  and  $m$ .

The input and output modulus of Alg. 10 do not have to be the same. In fact, we need the outputs to be shared over a small modulus, in order to input them to the protocols that are analogues to [2]. In Alg. 10, we make explicit, over which modulus each value has been shared. Recall that for different moduli, the Server has different MAC keys  $\Delta_{\mathbb{S}}$ . Also, the radix  $r$  has to be small enough for the additions in  $\llbracket \mathbf{p} \rrbracket_{\mathbb{P}} + \llbracket \mathbf{p} \rrbracket_{\mathbb{S}}$  to not overflow. In our implementation, we use  $Q = 29$ ,  $r = 15$  and  $d = 6$ .

**Par:** output modulus  $Q$ , radix  $r \leq \frac{Q+1}{2}$ , output length  $d$   
**Input:**  $\langle v \rangle^{(q)} = (\llbracket v \rrbracket^{(q)}, \llbracket V \rrbracket^{(q)})$   
**Output:**  $\langle \mathbf{p} \rangle^{(Q)}$ , s.t.  $\text{split}^{-1}(\llbracket \mathbf{p} \rrbracket_{\mathbf{P}}^{(Q)} + \llbracket \mathbf{p} \rrbracket_{\mathbf{S}}^{(Q)}) = v \pmod{q}$   
**CR:**  $m \in \mathbb{Z}_q$  to  $\mathbf{P}$ .  $\llbracket M \rrbracket^{(q)} = \llbracket m \cdot \Delta_{\mathbf{S}}^{(q)} \rrbracket^{(q)}$  and  $\llbracket \mathbf{r} \rrbracket^{(Q)} = \llbracket \text{split}(m) \cdot \Delta_{\mathbf{S}}^{(Q)} \rrbracket^{(Q)}$   
1  $\mathbf{P}$ :  $w \leftarrow \llbracket v \rrbracket_{\mathbf{P}}^{(q)} - m$  and  $M_w \leftarrow \llbracket V \rrbracket_{\mathbf{P}}^{(q)} - \llbracket M \rrbracket_{\mathbf{P}}^{(q)}$   
2  $\mathbf{P} \rightarrow \mathbf{S}$ :  $w, M_w$   
3  $\mathbf{S}$ : **stop** if  $M_w \neq w \cdot \Delta_{\mathbf{S}}^{(q)} - (\llbracket V \rrbracket_{\mathbf{S}}^{(q)} - \llbracket M \rrbracket_{\mathbf{S}}^{(q)})$   
4  $\mathbf{S}$ :  $\langle \mathbf{p} \rangle_{\mathbf{S}}^{(Q)} \leftarrow (\text{split}(\llbracket v \rrbracket_{\mathbf{S}}^{(q)} + w) \bmod q), \llbracket \mathbf{r} \rrbracket_{\mathbf{S}}^{(Q)}$   
5  $\mathbf{P}$ :  $\langle \mathbf{p} \rangle_{\mathbf{P}}^{(Q)} \leftarrow (\text{split}(m), \llbracket \mathbf{r} \rrbracket_{\mathbf{P}}^{(Q)})$   
6 **return**  $\langle \mathbf{p} \rangle^{(Q)}$

**Algorithm 10:** Asymmetric reshare to parts: `asymm_rp`

**Par:** Input modulus  $Q$ , output modulus  $N$   
**Input:**  $\langle v \rangle^{(Q)}$   
**Output:**  $\langle \mathbf{b} \rangle^{(N)}$ , s.t.  $\mathbf{b}$  is the CV of  $v$   
**CR:**  $\langle r \rangle^{(Q)}$  and  $\langle \mathbf{d} \rangle^{(N)}$ , s.t.  $\mathbf{d}$  is the CV of  $r$   
1  $f \leftarrow \text{declassify}(\langle v \rangle^{(Q)} - \langle r \rangle^{(Q)})$   
2  $\forall i \in [Q] : \langle b_i \rangle^{(N)} \leftarrow \langle d_{(i-f) \bmod Q} \rangle^{(N)}$   
3 **return**  $\langle \mathbf{b} \rangle^{(N)}$

**Algorithm 11:** Characteristic vector: `ch_vec`

**Par:** Input modulus  $Q$ , radix  $r \leq \frac{Q+1}{2}$ , output modulus  $N$   
**Input:**  $\langle v \rangle^{(Q)}$   
**Output:**  $\langle b \rangle^{(N)}, \langle c \rangle^{(N)}$ , indicating whether  $v = r - 1$  and  $v \geq r$ , respectively  
1  $\langle \mathbf{w} \rangle \leftarrow \text{ch\_vec}[Q, N](\langle v \rangle)$   
2 **return**  $\langle w_{r-1} \rangle$  and  $\sum_{i=r}^{Q-1} \langle w_i \rangle$

**Algorithm 12:** Short overflow: `short_of`

Having split a private value into digits  $\langle \mathbf{p} \rangle^{(Q)}$ , Attrapadung et al. [2] proceed to convert these digits to characteristic vectors. The protocol for this conversion is straightforward; it is given in Alg 11. The input and output modulus of this protocol may be different, too.

### 3.9 Overflow

The inequality protocol of Attrapadung et al. [2] computes the *overflows* of the to-be-compared private values. Given a vector  $\mathbf{p} = (p_0, \dots, p_{d-1})$ , where  $p_i \leq 2(r-1)$  (such vector could privately be the output of Alg 10), we say that  $\mathbf{p}$  *overflows* if  $\sum_{i=0}^{d-1} r^i \cdot p_i \geq r^d$ . The overflowing of  $\langle \mathbf{p} \rangle$  is computed in two steps, first for a single digit (`short_of`, Alg. 12) and then for the entire vector (`long_of`, Alg. 13).

Attrapadung et al. [2] have given an explanation on how Alg. 13 produces the correct output, let us repeat this explanation here. The input to the long overflow protocol (Alg. 13) is a multi-digit number  $\mathbf{p}$ , where the least significant digit is

**Par:** Length  $d$ ; input, “middle”, and output moduli  $Q, N, M$  satisfying  $N > 2^d$ ;  
radix  $r \leq (Q + 1)/2$   
**Input:**  $\langle \mathbf{p} \rangle^{(Q)}$  of length  $d$   
**Output:** Bit  $\langle z \rangle^{(M)}$ , indicating whether  $\mathbf{p}$  overflows

- 1 **foreach**  $i \in [d]$  **do**
- 2 |  $(\langle b_i \rangle^{(N)}, \langle c_i \rangle^{(N)}) \leftarrow \text{short\_of}[Q, r, N](\langle p_i \rangle)$
- 3  $\langle m \rangle^{(N)} \leftarrow \sum_{i=1}^{d-1} 2^{i-1} \cdot \langle b_i \rangle + \sum_{i=0}^{d-1} 2^i \cdot \langle c_i \rangle$
- 4  $\langle \mathbf{k} \rangle^{(M)} \leftarrow \text{ch\_vec}[N, M](\langle m \rangle)$
- 5 **return**  $\sum_{i=2^{d-1}}^{N-1} \langle k_i \rangle$

**Algorithm 13:** Long overflow: long\_of

indexed as 0 and the most significant digit as  $d - 1$ . With the help of the short overflow protocol, we find for each digit  $p_i$  whether it overflows (indicated by the bit  $c_i$ ) or “almost overflows” (indicated by the bit  $b_i$ ). Here “almost overflowing” means that the overflowing is propagated: if the next lowest digit overflows then this one will overflow as well. The whole number overflows if one of the following cases occurs:

- $c_{d-1} = 1$ , i.e. the most significant digit overflows;
- $b_{d-1} = c_{d-2} = 1$ , i.e. the second most significant digit overflows, and this propagates through the most significant digit;
- $b_{d-1} = b_{d-2} = c_{d-3} = 1$ , i.e. the third most significant digit overflows, and this propagates through the two most significant digits;
- etc.
- $b_{d-1} = b_{d-2} = \dots = b_1 = c_0 = 1$ , i.e. the least significant digit overflows, and this propagates through all digits.

Note that all these cases are mutually exclusive, because each pair of them contains some position  $i$ , where one case has  $c_i$  and the other one has  $b_i$ . Both  $b_i$  and  $c_i$  cannot be 1 at the same time.

Alg. 13 computes the weighted sum  $m$  of all  $b_i$  and  $c_i$ , such that this sum will be at least  $2^{d-1}$  iff one of the above cases is true. We see that for each case given above, the weights given to the bits involved in this case sum up to exactly  $2^{d-1}$ . If some other bits are also set, then the sum can be greater than that.

It is also not difficult to see that if none of the cases hold, then the sum will be less than  $2^{d-1}$ . Indeed, suppose that  $s$  is the greatest index where  $c_s = 1$ , and there exists some  $t \in \{s + 1, \dots, d - 1\}$ , such that  $b_t = 0$ . Then the weighted sum can be at most

$$\sum_{i=0}^s 2^i + \sum_{i=s+1}^{d-1} 2^{i-1} - 2^{t-1} = 2^{s+1} - 1 + 2^{d-1} - 2^s - 2^{t-1} = 2^{d-1} - 1 + 2^s - 2^{t-1},$$

which is less than  $2^{d-1}$ , because  $t - 1 \geq s$ .



**Par:** Length  $d$ ; “middle” ( $\times 2$ ) and output moduli  $Q, N, M$  satisfying  $N > 2^d$ ;  
 radix  $r \leq (Q + 1)/2$

**Input:**  $\langle v \rangle^{(q)}, C \in \mathbb{N}$

**Output:** Bit  $\langle z \rangle^{(M)}$  indicating whether  $v < C$

- 1  $\langle \mathbf{p} \rangle^{(Q)} \leftarrow \text{asymm\_rp}[Q, r, d](\langle v \rangle)$
- 2  $S: w_S \leftarrow \text{split}^{-1}(\llbracket \mathbf{p} \rrbracket_S)$
- 3  $\langle \mathbf{a} \rangle^{(Q)} \leftarrow \langle \mathbf{p} \rangle; \llbracket \mathbf{a} \rrbracket_S := \text{split}(w_S + r^d - q)$
- 4  $\langle \mathbf{b} \rangle^{(Q)} \leftarrow \langle \mathbf{p} \rangle; \llbracket \mathbf{b} \rrbracket_S := \text{split}(((w_S - C) \bmod q) + r^d - q)$
- 5  $\langle c \rangle^{(M)} \leftarrow \langle 0 \rangle; \llbracket c \rrbracket_S := w_S \geq C ? 1 : 0$
- 6  $\langle x \rangle^{(M)} \leftarrow \text{long\_of}[d, Q, N, M, r](\langle \mathbf{a} \rangle)$
- 7  $\langle y \rangle^{(M)} \leftarrow \text{long\_of}[d, Q, N, M, r](\langle \mathbf{b} \rangle)$
- 8 **return**  $1 + \langle x \rangle - \langle y \rangle - \langle c \rangle$

**Algorithm 14:** Inequality of a private value and a constant: `ineq`

### 3.10 Inequality check

The inequality (wrt. a constant) is given in Alg. 14. Similarly to [2], we compute the overflows of the values  $\langle v \rangle$  and  $\langle v \rangle - C$ , where the latter may roll over modulo  $q$ . We are interested in overflows wrt.  $q$ , but Alg. 13 computes them wrt.  $r^d$ , hence we adjust Server’s share of  $v$  by adding  $r^d - q$  to it. This addition happens after we have split  $\langle v \rangle$  to digits. We obtain the shares of digits of  $\langle v \rangle - C$  similarly, doing the adjustments at server’s side after having split  $\langle v \rangle$  into digits and obtained Server’s MACs on them. We use  $N = 67$  and  $M = 71$  as the moduli.

The correctness of Alg. 14 follows through the same arguments as the correctness of Attrapadung et al.’s inequality protocol [2]. Let us give a rehash of these arguments.

We have the number  $v$  shared as  $(\llbracket v \rrbracket_P + \llbracket v \rrbracket_S) \bmod q$ , and the constant  $C$ . Alg. 14 applies the asymmetric reshare to parts to it; if we ignore the plurality of digits, then we can think of this operation as *resharing*  $\llbracket v \rrbracket$ : we change  $\llbracket v \rrbracket_P$  and  $\llbracket v \rrbracket_S$  (now denoted  $w_S$  in Alg. 14), such that they are still the shares of  $v$ . Let us write

- $v_1 := \llbracket v \rrbracket_P$ ;
- $v_2 := \llbracket v \rrbracket_S$ ;
- $v'_2 := (\llbracket v \rrbracket_S - C) \bmod q$ , meaning that either  $v'_2 = v_2 - C$  or  $v'_2 = v_2 + q - C$ .

If we ignore the shift  $r^d - q$ , and the plurality of digits, then we can think that the shares of  $\llbracket \mathbf{a} \rrbracket$  are  $v_1$  and  $v_2$ , while the shares of  $\llbracket \mathbf{b} \rrbracket$  are  $v_1$  and  $v'_2$ . When computing the overflows, we figure out the bit  $x$  indicating whether  $v_1 + v_2 \geq q$ . Similarly, the bit  $y$  indicates whether  $v_1 + v'_2 \geq q$ . We also have the bit  $c$  indicating whether  $v_2 \geq C$ . It turns out that knowing the values of these three bits is sufficient to figure out whether  $v < C$ . Let us consider all eight possibilities for the values of the bits  $x, y, c$ , and figure out the output bit of the inequality check in each case.

**000**  $v_1 + v_2 < q, v_1 + v'_2 < q, v_2 < C$ . In this case,  $v'_2 = v_2 + q - C$ . We have  $C = v_2 - v'_2 + q > v_2 - v'_2 + v_1 + v'_2 = v_1 + v_2$ , i.e. the result should be 1.

- 001**  $v_1 + v_2 < q$ ,  $v_1 + v'_2 < q$ ,  $v_2 \geq C$ . In this case,  $v'_2 = v_2 - C$ . We have  $0 \leq v_1 + v_2 - C < q$ . We have  $C \leq (v_1 + v_2) \bmod q$ , i.e. the result should be 0.
- 1010**  $v_1 + v_2 < q$ ,  $v_1 + v'_2 \geq q$ ,  $v_2 < C$ . In this case,  $v'_2 = v_2 + q - C$ . We have  $C = v_2 - v'_2 + q \leq v_2 - v'_2 + v_1 + v'_2 = v_1 + v_2$ , i.e. the result should be 0.
- 0111**  $v_1 + v_2 < q$ ,  $v_1 + v'_2 \geq q$ ,  $v_2 \geq C$ . In this case,  $v'_2 = v_2 - C$ . This case is impossible, because subtraction means that  $v'_2 \leq v_2$ , but the first two inequalities mean that  $v'_2 > v_2$ .
- 1100**  $v_1 + v_2 \geq q$ ,  $v_1 + v'_2 < q$ ,  $v_2 < C$ . In this case,  $v'_2 = v_2 + q - C > v_2$ , but also  $v'_2 < q - v_1 \leq v_2$ . This case is impossible.
- 1011**  $v_1 + v_2 \geq q$ ,  $v_1 + v'_2 < q$ ,  $v_2 \geq C$ . In this case,  $v'_2 = v_2 - C$ . We have  $C = v_2 - v'_2 > v_2 - q + v_1$ , i.e. the result should be 1.
- 1110**  $v_1 + v_2 \geq q$ ,  $v_1 + v'_2 \geq q$ ,  $v_2 < C$ . In this case,  $v'_2 = v_2 + q - C$ . We have  $C = v_2 - v'_2 + q > v_2 - q + v_1$  (because  $v'_2 < q$  and  $q > v_1$ ), i.e. the result should be 1.
- 1111**  $v_1 + v_2 \geq q$ ,  $v_1 + v'_2 \geq q$ ,  $v_2 \geq C$ . In this case,  $v'_2 = v_2 - C$ . We have  $C = v_2 - v'_2 \leq v_2 - q + v_1$ , i.e. the result should be 0.

We see that in all six cases that are actually possible, the result is  $1 + x - y - c$ .

Alg. 14 allows us to perform rejection checks on  $\langle\langle \mathbf{z} \rangle\rangle$  and  $\langle\langle \mathbf{x} \rangle\rangle$ , as shown in Alg. 15, albeit it provides only privacy, but no correctness against a malicious Server. Moreover, when the result of rejection check is opened, it may give Server some information on  $\mathbf{z}$  and  $\mathbf{x}$ . Rejection check with AP-security is still useful: if the declassified result of rejection check is “OK”, then we first declassify  $\mathbf{z}$  only to the Phone. The Phone verifies the signature  $(\mathbf{z}, c)$  and only on success sends it to the Server. If the Server interfered with the computations and caused the rejection check result to be “OK”, but  $(\mathbf{z}, c)$  does not verify, then the Phone recognizes that the Server is malicious. If the Server caused the rejection check result to be “not OK”, even though it should have been “OK”, then the Server only reduced the amount of information it would have learned: if the Server hadn't tampered with the computations, it would have learned the entire  $\mathbf{z}$  (and also  $\mathbf{x}$ ; it can be computed from the signature and the public key) from the signature. If the Server tampered with the computations of the rejection check in a manner that didn't change its result, then it would have received at least as much information about  $\mathbf{z}$  and  $\mathbf{x}$  also by not tampering. Hence the Server's best tactic is to not tamper.

Alg. 15 invokes the ineq protocol (with parameters  $d, Q, N, M, r$ ), and then computes the long conjunction over the results. We see that it adds up the negations of the bits, and then returns whether the result is 0. Actually, the vector  $\mathbf{v}$  is much longer than the modulus  $M$ , hence the sum may wrap around. The Phone will consider this wraparound as Server's malicious behaviour. We have chosen the modulus  $M = 71$  so, that probability of the sum being at least  $M$  is less than  $2^{-256}$  if both parties behave honestly. Alg. 15 does not declassify its output; the declassification is part of  $\Pi_{\text{ML-DSA}}$ .

**Par:** ML-DSA parameters, and  $d, Q, N, M, r$  (same as in Alg. 14)  
**Input:**  $(\sum_{t=0}^{255} \langle z_{i-1,t} \rangle \cdot X^t)_{i=1}^\ell, (\sum_{t=0}^{255} \langle x_{i-1,t} \rangle \cdot X^t)_{i=1}^k$   
**Output:** A bit indicating whether rejection check is passed

- 1 **foreach**  $i \in [\ell], t \in [256]$  **do**
- 2 |  $\langle v_{256 \cdot i + t} \rangle^{(M)} \leftarrow \text{ineq}[d, Q, N, M, r](\langle z_{i,t} \rangle + \gamma_1 - \beta - 1, 2(\gamma_1 - \beta) - 1)$
- 3 **foreach**  $i \in [k], t \in [256]$  **do**
- 4 |  $\langle v_{256 \cdot (i+\ell) + t} \rangle^{(M)} \leftarrow \text{ineq}[d, Q, N, M, r](\langle x_{i,t} \rangle + \gamma_2 - \beta - 1, 2(\gamma_2 - \beta) - 1)$
- 5  $\langle s \rangle^{(M)} \leftarrow \sum_{i=0}^{\text{length}(\mathbf{v})-1} (1 - \langle v_i \rangle^{(M)})$
- 6  $\langle \mathbf{b} \rangle \leftarrow \text{ch\_vec}[M, 2](\langle s \rangle^{(M)})$
- 7 **return**  $\langle b_0 \rangle$

**Algorithm 15:** Rejection check: `rej_check`

**Par:** ML-DSA parameters, and  $d, Q, N, M, r$   
**Input:**  $\mathbf{A}, \mathbf{t}, \langle \mathbf{s}_1 \rangle, \langle \mathbf{s}_2 \rangle, \mu$   
**Output:** Signature  $(\mathbf{z}, c)$  on  $\mu$ , or  $\perp$

- 1  $\langle \mathbf{y} \rangle, \langle \mathbf{w}^H \rangle \leftarrow \text{gcom}[k, \ell, \alpha, \gamma_1](\mathbf{A})$
- 2  $\langle \mathbf{w}^L \rangle \leftarrow \mathbf{A} \cdot \langle \mathbf{y} \rangle - \alpha \cdot \langle \mathbf{w}^H \rangle$
- 3  $c \leftarrow H(\mu, \text{declassify}(\langle \mathbf{w}^H \rangle))$
- 4  $\langle \mathbf{z} \rangle = (\sum_{t=0}^{255} \langle z_{i-1,t} \rangle \cdot X^t)_{i=1}^\ell \leftarrow \langle \mathbf{y} \rangle + c \cdot \langle \mathbf{s}_1 \rangle$
- 5  $\langle \mathbf{x} \rangle = (\sum_{t=0}^{255} \langle x_{i-1,t} \rangle \cdot X^t)_{i=1}^k \leftarrow \langle \mathbf{w}^L \rangle - c \cdot \langle \mathbf{s}_2 \rangle$
- 6 **if** `declassify(rej_check( $\langle \mathbf{z} \rangle, \langle \mathbf{x} \rangle$ ))` **then**
- 7 |  $\text{S} \rightarrow \text{P}: \llbracket \mathbf{z} \rrbracket_{\text{S}}$
- 8 |  $\text{P}: \mathbf{z} \leftarrow \llbracket \mathbf{z} \rrbracket_{\text{S}} + \llbracket \mathbf{z} \rrbracket_{\text{P}}$
- 9 |  $\text{P}: \text{stop}$  if  `$\neg \text{Verify}((\mathbf{A}, \mathbf{t}), \mu, (\mathbf{z}, c))$`
- 10 |  $\text{P} \rightarrow \text{S}: \mathbf{z}$
- 11 | **return**  $(\mathbf{z}, c)$
- 12 **else return**  $\perp$  ;

**Algorithm 16:** A signing attempt: `sign_trilithium`

### 3.11 Distributed signing

The entire signing attempt is given in Alg. 16.

## 4 Security of components

Each algorithm in Sec. 3 corresponds to a real system  $\Pi$ ; the system  $\Pi_{\text{ML-DSA}}$  is the composition of the systems corresponding to Alg. 5 and Alg. 16. The security analysis becomes more modular when we state the ideal functionalities securely implemented by these systems.

Thus, for each of these algorithms, we will now describe an ideal functionality, a real protocol for running this functionality, and possibly another ideal functionality for generating the correlated randomness. We will then show that the real protocol is a secure implementation of the ideal functionality in the hybrid model that includes the correlated randomness generation, and all functionalities for the sub-protocols invoked by the algorithm. We start with the declassification

- On input  $(\text{corrupt}, \text{sid}, X)$  from  $\mathcal{S}$  before the initialization, mark that  $X$  is corrupted and send  $(\text{corrupted}, \text{sid})$  to  $X$ .
- On input  $(\text{init}, \text{sid}, \Delta_P)$  from  $P$  and  $(\text{init}, \text{sid}, \Delta_S)$  from  $S$ : Store  $\Delta_P$  and  $\Delta_S$ . If party  $X$  is corrupted, then send  $(\text{globkeys}, \Delta_X)$  to the adversary.
- On input  $(\text{declassify}, \text{sid}, \langle\langle v \rangle\rangle_P)$  from  $P$  and  $(\text{declassify}, \text{sid}, \langle\langle v \rangle\rangle_S)$  from  $S$  (where  $\langle\langle v \rangle\rangle_X = (\llbracket v \rrbracket_X, \llbracket M_S \rrbracket_X, \llbracket M_P \rrbracket_X)$ ), proceed as follows:
  1. Notify  $\mathcal{S}$ . If nobody corrupt, send  $(\text{proceed?}, \text{sid})$  to  $\mathcal{S}$ , get back  $(\text{proceed!}, \text{sid})$ . If  $S$  corrupt, send  $(\text{input}, \text{sid}, \langle\langle v \rangle\rangle_S)$  to  $\mathcal{S}$  and get back  $(\text{update}, \text{sid}, \langle\langle v \rangle\rangle_S)$ . If  $P$  corrupt, send  $(\text{input}, \text{sid}, \langle\langle v \rangle\rangle_P, \llbracket v \rrbracket_S, \llbracket M_P \rrbracket_S)$  to  $\mathcal{S}$  and get back  $(\text{update}, \text{sid}, \langle\langle v \rangle\rangle_P)$ . Update the component of  $\langle\langle v \rangle\rangle$  with the value received from  $\mathcal{S}$ .
  2. Compute  $v \leftarrow \llbracket v \rrbracket_P + \llbracket v \rrbracket_S$ . Let the boolean  $b_P$  [resp.  $b_S$ ] indicate whether  $M_P$  [resp.  $M_S$ ] verifies.
  3. Output the result. Proceed as follows:
    - If nobody corrupt: If  $b_P$ , send  $(\text{result}, \text{sid}, v)$  to  $P$ , otherwise send  $(\text{result}, \text{sid}, \perp)$  to  $P$ . If  $b_S \wedge b_P$ , send  $(\text{result}, \text{sid}, v)$  to  $S$ , otherwise send  $(\text{result}, \text{sid}, \perp)$  to  $S$ .
    - If  $P$  corrupt: send  $(\text{result?}, \text{sid})$  to  $\mathcal{S}$ . Receive back  $(\text{result!}, \text{sid}, v', b)$ . Send  $(\text{result}, \text{sid}, v')$  to  $P$ . If  $b \wedge b_S$  then send  $(\text{result}, \text{sid}, v)$  to  $S$ , otherwise send  $(\text{result}, \text{sid}, \perp)$  to  $S$ .
    - If  $S$  corrupt: If  $\neg b_P$ , then send  $(\text{result}, \text{sid}, \perp)$  to  $P$ ,  $S$  and  $\mathcal{S}$ . Otherwise send  $(\text{result}, \text{sid}, v)$  to  $P$  and  $(\text{result?}, \text{sid}, v, b_S)$  to  $\mathcal{S}$ . Get back  $(\text{result}, \text{sid}, v')$  and forward it to  $S$ .

**Functionality 2:**  $\mathcal{F}_{\text{declassify}}^{\text{AA}}$ : ideal functionality for declassification

from Sec. 3.1, then give the generic protocols and precomputation functionalities, as much as these can be given, then go through the algorithms in Sec. 3, and finish with the description of  $\Pi_{\text{ML-DSA}}$  and its security proof in Sec. 5.

#### 4.1 Declassification

**Protocol and ideal functionality for AA security.** The ideal functionality for AA-secure declassification is given in Func. 2. Similarly to Alg. 3, it is asymmetric with respect of the roles of  $P$  and  $S$ . We see that the steps of  $\mathcal{F}_{\text{declassify}}^{\text{AA}}$  are typical for ideal functionalities: it first allows the adversary to adjust the input(s) of corrupted party(-ies), then performs the actual computation (which, in this case consists of recovering  $v$  and checking the MACs), then informs the adversary of the result and allows it to control when the result is sent back and which result is sent back to corrupted parties. In Func. 2 and below, we use  $X$  to denote either  $S$  or  $P$ ; the functionality allows one of the parties to be corrupted.

The description of Alg. 3 can be turned to the description of the real system consisting of machines  $\mathcal{M}_P$  and  $\mathcal{M}_S$ ; this description is given in Prot. 1. The machines  $\mathcal{M}_P$  and  $\mathcal{M}_S$  follow this description when not corrupted, and act as described in Sec. 2.5 when corrupted. To show that Prot. 1 securely implements

- On input (`corrupted`,  $sid$ ) from  $\mathcal{A}$  to  $\mathcal{M}_X$  before the initialization, machine  $\mathcal{M}_X$  sends (`corrupted`,  $sid$ ) to  $X$  and becomes corrupted.
- On input (`init`,  $sid$ ,  $\Delta_X$ ) from  $X$  to  $\mathcal{M}_X$ : store  $\Delta_X$ . If  $\mathcal{M}_X$  is corrupt, send (`globkeys`,  $sid$ ,  $\Delta_X$ ) to  $\mathcal{A}$ .
- On input (`declassify`,  $sid$ ,  $\langle\langle v \rangle\rangle_S$ ) from  $S$  to  $\mathcal{M}_S$ , the machine  $\mathcal{M}_S$  works as follows:
  - Send (`msg1`,  $sid$ ,  $\langle\langle v \rangle\rangle_S$ ,  $\text{mp}\langle\langle v \rangle\rangle_S$ ) to  $\mathcal{M}_P$ .
  - Expect back (`msg2`,  $sid$ ,  $\langle\langle v \rangle\rangle_P$ ,  $\text{ms}\langle\langle v \rangle\rangle_P$ ).
    - \* If the message was (`msg2`,  $sid$ ,  $\perp$ ), then send (`result`,  $sid$ ,  $\perp$ ) to  $S$  and do not proceed further.
  - Recover  $v \leftarrow \langle\langle v \rangle\rangle_P + \langle\langle v \rangle\rangle_S$ .
  - Verify  $S$ 's MAC: check that  $\langle\langle v \rangle\rangle_P \cdot \Delta_S = \text{ms}\langle\langle v \rangle\rangle_P + \text{ms}\langle\langle v \rangle\rangle_S$
  - If the MAC verifies, send (`result`,  $sid$ ,  $v$ ) to  $S$ .
  - If the MAC does not verify, send (`result`,  $sid$ ,  $\perp$ ) to  $S$ .
- On input (`declassify`,  $sid$ ,  $\langle\langle v \rangle\rangle_P$ ) from  $P$  to  $\mathcal{M}_P$ , the machine  $\mathcal{M}_P$  works as follows:
  - Expect (`msg1`,  $\langle\langle v \rangle\rangle_S$ ,  $\text{mp}\langle\langle v \rangle\rangle_S$ ) from  $\mathcal{M}_S$ .
  - Recover  $v \leftarrow \langle\langle v \rangle\rangle_P + \langle\langle v \rangle\rangle_S$ .
  - Verify  $P$ 's MAC: check that  $\langle\langle v \rangle\rangle_S \cdot \Delta_P = \text{mp}\langle\langle v \rangle\rangle_P + \text{mp}\langle\langle v \rangle\rangle_S$
  - If the MAC verifies, send (`msg2`,  $sid$ ,  $\langle\langle v \rangle\rangle_P$ ,  $\text{ms}\langle\langle v \rangle\rangle_P$ ) to  $\mathcal{M}_S$  and (`result`,  $sid$ ,  $v$ ) to  $P$ .
  - If the MAC does not verify, send (`msg2`,  $sid$ ,  $\perp$ ) to  $\mathcal{M}_S$  and (`result`,  $sid$ ,  $\perp$ ) to  $P$ .

**Protocol 1:** AA declassification protocol

Func. 2, we construct the simulator. As the corruptions of parties are static (may only happen before the actual execution starts), our simulator is basically a choice between three simulators: one for the case with not corrupted parties, one for corrupted Phone, and one for corrupted Server. The first case is trivial; the simulator internally executes both  $\mathcal{M}_P$  and  $\mathcal{M}_S$  and relays the scheduling information between  $\mathcal{F}_{\text{ML-DSA}}$  and  $\mathcal{A}$ . Let us describe the construction of the other two simulators.

**Simulator for AA-secure declassification with corrupted Phone.** During initialization, the simulator receives  $\Delta_P$ . During declassification, it receives (`input`,  $sid$ ,  $\langle\langle v \rangle\rangle_P$ ,  $\langle\langle v \rangle\rangle_S$ ,  $\text{mp}\langle\langle v \rangle\rangle_S$ ) from the ideal functionality and tells the adversary that the share  $\langle\langle v \rangle\rangle_P$  is being declassified (as if  $\mathcal{M}_P$  reporting that it got the `declassify`-command from the environment). It creates and sends to  $\mathcal{A}$  the message (`msg1`,  $\langle\langle v \rangle\rangle_S$ ,  $\text{mp}\langle\langle v \rangle\rangle_S$ ), as if  $\mathcal{M}_P$  was reporting to  $\mathcal{A}$  that it received this message from  $\mathcal{M}_S$ .

If the adversary sends back (as the command to  $\mathcal{M}_P$  to send a message to  $\mathcal{M}_S$ ) the message (`msg2`,  $sid$ ,  $\perp$ ), followed by the command to  $\mathcal{M}_P$  to output  $v'$  to  $P$ , then the simulator sends the message (`update`,  $sid$ ,  $\langle\langle v \rangle\rangle_P$ ) back to the ideal functionality, waits to receive (`result?`,  $sid$ ) from the ideal functionality, and sends back (`result!`,  $sid$ ,  $v'$ , `false`) to the ideal functionality.

But if the message (`msg2`,  $sid$ ,  $\langle\langle v \rangle\rangle_P$ ,  $\text{ms}\langle\langle v \rangle\rangle_P$ ), intended for  $\mathcal{M}_P$  to send it to  $\mathcal{M}_S$ , is sent by  $\mathcal{A}$  to the simulator, again followed by  $v'$  for  $P$ , then `update`  $\langle\langle v \rangle\rangle_P$

with the components received from  $\mathcal{A}$  and send  $(\text{update}, \text{sid}, \langle\langle v \rangle\rangle_{\mathcal{P}})$  back to the ideal functionality. Wait to receive  $(\text{result?}, \text{sid})$  from the ideal functionality, and send back  $(\text{result!}, \text{sid}, v', \text{true})$  to the ideal functionality.

**Simulator for AA-secure declassification with corrupted Server.** During initialization, the simulator receives  $\Delta_{\mathcal{S}}$ . During declassification, it receives  $(\text{input}, \text{sid}, \langle\langle v \rangle\rangle_{\mathcal{S}})$  from the ideal functionality and tells the adversary that the share  $\langle\langle v \rangle\rangle_{\mathcal{S}}$  is being declassified. The simulator then expects to receive from the adversary (as  $\mathcal{M}_{\mathcal{S}}$  sending it to  $\mathcal{M}_{\mathcal{P}}$ ) the message  $(\text{msg1}, \text{sid}, \langle\langle v \rangle\rangle_{\mathcal{S}}, \text{mp} \langle\langle v \rangle\rangle_{\mathcal{S}})$ . The simulator updates  $\langle\langle v \rangle\rangle_{\mathcal{S}}$  with the components it received and sends  $(\text{update}, \text{sid}, \langle\langle v \rangle\rangle_{\mathcal{S}})$  back to the ideal functionality.

Wait for the next message from the ideal functionality. If it is  $(\text{result}, \text{sid}, \perp)$ , then send  $(\text{msg2}, \text{sid}, \perp)$  to the adversary (as message from  $\mathcal{M}_{\mathcal{P}}$  to  $\mathcal{M}_{\mathcal{S}}$ ). If the message is  $(\text{result?}, \text{sid}, v, b_{\mathcal{S}})$ , then compute

$$\begin{aligned} - \quad \langle\langle v \rangle\rangle_{\mathcal{P}} &\leftarrow v - \langle\langle v \rangle\rangle_{\mathcal{S}} \\ - \quad \text{ms} \langle\langle v \rangle\rangle_{\mathcal{P}} &\leftarrow \Delta_{\mathcal{S}} \cdot \langle\langle v \rangle\rangle_{\mathcal{P}} - \text{ms} \langle\langle v \rangle\rangle_{\mathcal{S}} \end{aligned}$$

and send  $(\text{msg2}, \text{sid}, \langle\langle v \rangle\rangle_{\mathcal{P}}, \text{ms} \langle\langle v \rangle\rangle_{\mathcal{P}})$  to  $\mathcal{A}$  (as  $\mathcal{M}_{\mathcal{S}}$  reporting receiving a message from  $\mathcal{M}_{\mathcal{P}}$ ). If  $b_{\mathcal{S}} = \text{true}$  then send  $(\text{result}, \text{sid}, v)$  to the ideal functionality, otherwise send  $(\text{result}, \text{sid}, \perp)$  to the ideal functionality.

**Protocol and ideal functionality for AP security.** In Sec. 3.1 we have already discussed how the protocol for AP-secure declassification would look like: it would remove some message parts and checks from Alg. 3. Let us now give the description explicitly in Func. 3 and Prot. 2. We notice that the ideal functionality still ensures that if  $\mathcal{S}$  is honest, then it learns either the to-be-declassified value  $v$  or nothing at all. But if  $\mathcal{S}$  is corrupt, then the adversary  $\mathcal{S}$  can freely add a change  $\hat{v}$  to the value that is received by  $\mathcal{P}$ . Indeed, after  $\mathcal{S}$  has received the  $(\text{input}, \text{sid}, \langle\langle v \rangle\rangle_{\mathcal{S}})$ -message, it has to add  $\hat{v}$  to the  $v$ -component of  $\langle\langle v \rangle\rangle_{\mathcal{S}}$  in the **update**-message to impose the same change to  $v$ . This change does not affect the validity of any MACs.

**Simulator for AP-secure declassification with corrupted Phone.** During initialization, the simulator does not receive anything. During declassification, it receives  $(\text{input}, \text{sid}, \langle\langle v \rangle\rangle_{\mathcal{P}}, \langle\langle v \rangle\rangle_{\mathcal{S}})$  from the ideal functionality and tells the adversary that the share  $\langle\langle v \rangle\rangle_{\mathcal{P}}$  is being declassified (as if  $\mathcal{M}_{\mathcal{P}}$  reporting that it got the **declassify**-command from  $\mathcal{P}$ ). It creates the message  $(\text{msg1}, \langle\langle v \rangle\rangle_{\mathcal{S}})$  and sends it to  $\mathcal{A}$  (as  $\mathcal{M}_{\mathcal{P}}$  reporting to  $\mathcal{A}$  that it received this message from  $\mathcal{M}_{\mathcal{S}}$ ).

If the adversary sends back (as the command to  $\mathcal{M}_{\mathcal{P}}$  to send a message to  $\mathcal{M}_{\mathcal{S}}$ ) the message  $(\text{msg2}, \text{sid}, \perp)$ , followed by the command to  $\mathcal{M}_{\mathcal{P}}$  to output  $v'$  to  $\mathcal{P}$ , then the simulator sends the message  $(\text{update}, \text{sid}, \langle\langle v \rangle\rangle_{\mathcal{P}})$  back to the ideal functionality, waits to receive  $(\text{result?}, \text{sid})$  from the ideal functionality, and sends back  $(\text{result!}, \text{sid}, v', \text{false})$  to the ideal functionality.

- On input  $(\text{corrupt}, \text{sid}, X)$  from  $\mathcal{S}$  before the initialization, mark that  $X$  is corrupted and send  $(\text{corrupted}, \text{sid})$  to  $X$ .
- On input  $(\text{init}, \text{sid})$  from  $P$  and  $(\text{init}, \text{sid}, \Delta_S)$  from  $S$ : Store  $\Delta_S$ . If  $S$  is corrupted, then send  $(\text{globkeys}, \Delta_S)$  to the adversary.
- On input  $(\text{declassify}, \text{sid}, \langle v \rangle_P)$  from  $P$  and  $(\text{declassify}, \text{sid}, \langle v \rangle_S)$  from  $S$ , proceed as follows:
  1. Notify  $\mathcal{S}$ .
    - If nobody is corrupt, send  $(\text{proceed?}, \text{sid})$  to  $\mathcal{S}$ , get back  $(\text{proceed!}, \text{sid})$ .
    - If  $P$  is corrupt, send  $(\text{input}, \text{sid}, \langle v \rangle_P, \llbracket v \rrbracket_S)$  to  $\mathcal{S}$  and get back  $(\text{update}, \text{sid}, \langle v \rangle_P)$ .
    - If  $S$  is corrupt, send  $(\text{input}, \text{sid}, \langle v \rangle_S)$  to  $\mathcal{S}$  and get back  $(\text{update}, \text{sid}, \langle v \rangle_S)$ .
 Update the component of  $\langle v \rangle$  with the value received from  $\mathcal{S}$ .
  2. Recover  $v$  from  $\langle v \rangle$ . Let the boolean  $b_S$  indicate whether  $_{ms}\langle v \rangle$  verifies.
  3. Output the result. Proceed as follows:
    - If nobody is corrupt: Send  $(\text{result}, \text{sid}, v)$  to  $P$ . If  $b_S$ , send it to  $S$ , too, otherwise send  $(\text{result}, \text{sid}, \perp)$  to  $S$ .
    - If  $S$  is corrupt: send  $(\text{result?}, \text{sid}, v, b_S)$  to  $\mathcal{S}$  and  $(\text{result}, \text{sid}, v)$  to  $P$ . Let  $\mathcal{S}$  send back  $(\text{result}, \text{sid}, v')$ . Send  $(\text{result}, \text{sid}, v')$  to  $S$ .
    - If  $P$  is corrupt: send  $(\text{result?}, \text{sid})$  to  $\mathcal{S}$ . Receive back  $(\text{result!}, \text{sid}, v', b)$ . Send  $(\text{result}, \text{sid}, v')$  to  $P$ . If  $b \wedge b_S$ , then send  $(\text{result}, \text{sid}, v)$  to  $S$ , otherwise send  $(\text{result}, \text{sid}, \perp)$  to  $S$ .

**Functionality 3:** AP-secure declassification

- On input  $(\text{corrupted}, \text{sid})$  from  $\mathcal{A}$  to  $\mathcal{M}_X$  before the initialization, machine  $\mathcal{M}_X$  sends  $(\text{corrupted}, \text{sid})$  to  $X$  and becomes corrupted.
- On input  $(\text{init}, \text{sid}, \Delta_S)$  from  $S$  to  $\mathcal{M}_S$ : store  $\Delta_S$ . If  $\mathcal{M}_S$  is corrupt, send  $(\text{globkeys}, \text{sid}, \Delta_S)$  to  $\mathcal{A}$ .
- On input  $(\text{init}, \text{sid})$  from  $P$  to  $\mathcal{M}_P$ : (nothing).
- On input  $(\text{declassify}, \text{sid}, \langle v \rangle_S)$  from  $S$  to  $\mathcal{M}_S$ , the machine  $\mathcal{M}_S$  works as follows:
  - Send  $(\text{msg1}, \text{sid}, \langle v \rangle_S)$  to  $\mathcal{M}_P$ .
  - Expect back  $(\text{msg2}, \text{sid}, \langle v \rangle_P)$ .
    - \* If the message was  $(\text{msg2}, \text{sid}, \perp)$ , then send  $(\text{result}, \text{sid}, \perp)$  to  $S$  and do not proceed further.
  - Recover  $v \leftarrow \langle v \rangle_P + \langle v \rangle_S$ .
  - Verify  $S$ 's MAC: check that  $\langle v \rangle_P \cdot \Delta_S = \text{ms}\langle v \rangle_P + \text{ms}\langle v \rangle_S$
  - If the MAC verifies, send  $(\text{result}, \text{sid}, v)$  to  $S$ .
  - If the MAC does not verify, send  $(\text{result}, \text{sid}, \perp)$  to  $S$ .
- On input  $(\text{declassify}, \text{sid}, \langle v \rangle_P)$  from  $P$  to  $\mathcal{M}_P$ , the machine  $\mathcal{M}_P$  works as follows:
  - Expect  $(\text{msg1}, \langle v \rangle_S)$  from  $\mathcal{M}_S$ .
  - Recover  $v \leftarrow \langle v \rangle_P + \langle v \rangle_S$ .
  - Send  $(\text{msg2}, \text{sid}, \langle v \rangle_P)$  to  $\mathcal{M}_S$  and  $(\text{result}, \text{sid}, v)$  to  $P$ .

**Protocol 2:** AP declassification protocol

But if the adversary sends back the message  $(\text{msg2}, \text{sid}, \langle v \rangle_{\mathcal{P}})$  for  $\mathcal{M}_{\mathcal{S}}$ , again followed by  $v'$  for  $\mathcal{P}$ , then the simulator updates  $\langle v \rangle_{\mathcal{P}}$  and sends  $(\text{update}, \text{sid}, \langle v \rangle_{\mathcal{P}})$  back to the ideal functionality. Waits to receive  $(\text{result?}, \text{sid})$  from the ideal functionality, and sends back  $(\text{result!}, \text{sid}, v', \text{true})$  to the ideal functionality.

**Simulator for AP-secure declassification with corrupted Server.** During initialization, the simulator receives  $\Delta_{\mathcal{S}}$ . During declassification, it receives  $(\text{input}, \text{sid}, \langle v \rangle_{\mathcal{S}})$  from the ideal functionality and tells the adversary that the share  $\langle v \rangle_{\mathcal{S}}$  is being declassified.

The simulator expects  $\mathcal{A}$  to tell  $\mathcal{M}_{\mathcal{S}}$  to send the first message  $(\text{msg1}, \text{sid}, \llbracket v' \rrbracket_{\mathcal{S}})$  to  $\mathcal{M}_{\mathcal{P}}$ . The simulator then sends to the ideal functionality the message  $(\text{update}, \text{sid}, (\llbracket v' \rrbracket_{\mathcal{S}}, \text{ms} \langle v \rangle_{\mathcal{S}}))$ , effectively changing the to-be-declassified value by  $\varepsilon = \llbracket v' \rrbracket_{\mathcal{S}} - \llbracket v \rrbracket_{\mathcal{S}}$ .

The ideal functionality sends  $(\text{result?}, \text{sid}, v, b_{\mathcal{S}})$  to the simulator. The simulator computes

$$\begin{aligned} - \llbracket v \rrbracket_{\mathcal{P}} &\leftarrow v - \llbracket v \rrbracket_{\mathcal{S}} \\ - \text{ms} \langle v \rangle_{\mathcal{P}} &\leftarrow \Delta_{\mathcal{S}} \cdot \llbracket v \rrbracket_{\mathcal{P}} - \text{ms} \langle v \rangle_{\mathcal{S}} \end{aligned}$$

and send  $(\text{msg2}, \text{sid}, \langle v \rangle_{\mathcal{P}})$  to  $\mathcal{A}$  (as if  $\mathcal{M}_{\mathcal{S}}$  notifying  $\mathcal{A}$  about the message it received from  $\mathcal{M}_{\mathcal{P}}$ ). The simulator gets  $v'$  from the adversary (as the value that  $\mathcal{S}$  should receive) and sends  $(\text{result}, \text{sid}, v')$  to the ideal functionality.

## 4.2 Ideal functionalities corresponding to algorithms

The ideal functionalities for all AA-secure protocols above are instances of the generic functionality given in Func. 4. The functionality is parametrized by the function  $f$  it is computing, which may be probabilistic. The functionality is also parametrized by the privacy levels of its inputs: parts of the inputs are secret-shared, while others are available in the open. The outputs are always private; this is the reason we did not declassify the outputs of Alg. 5 or Alg. 9. There is another constraint on outputs, less visible in Func. 4: the outputs may not be linearly dependent on other outputs or inputs. For this reason,  $\mathbf{A} \cdot \langle \mathbf{y} \rangle$  was computed in both Alg. 9 and Alg. 16, and  $\langle \mathbf{t} \rangle$  was not computed in Alg. 5. Finally, a predicate  $\mathbf{B}$  limiting the domain of  $f$  is a parameter of the functionality.

Not all AA-secure protocols in Sec. 3 use all features of  $f$  in Func. 4. Clearly, if a protocol is generating random values, then the corresponding  $f$  is randomized. The function  $f$  for “symmetric reshare to parts” is also randomized: it is resharing either  $v$  or  $v + q$  depending on whether  $r \leq v$  or not, for a random  $r \in \mathbb{Z}_q$ . The predicate  $\mathbf{B}$  is materially used only in the functionality describing the “zero check” (Alg. 6); for all others,  $\mathbf{B} \equiv 1$ .

The ideal functionalities for all AP-secure protocols, *except* for Alg. 10, are instances of the generic functionality given in Func. 5. Alg. 10 is not an instance, because the shares of the outputs of that protocol are not uniformly distributed over  $\mathbb{Z}_Q$ , and thus cannot be described in the manner allowed by Func. 5. Its corresponding ideal functionality will be given in Func. 8.



- On input  $(\text{corrupt}, \text{sid}, X)$  from  $\mathcal{S}$  before the initialization, mark that  $X$  is corrupted and send  $(\text{corrupted}, \text{sid})$  to  $X$ .
- On input  $(\text{init}, \text{sid}, \Delta_P)$  from  $P$  and  $(\text{init}, \text{sid}, \Delta_S)$  from  $S$ : Store  $\Delta_P$  and  $\Delta_S$ . If party  $X$  is corrupted, then send  $(\text{globkeys}, \Delta_X)$  to the adversary.
- On input  $(\text{compute}, \text{sid}, \langle\langle v^{\text{priv}} \rangle\rangle_P, v^{\text{pub}})$  from  $P$  and  $(\text{compute}, \text{sid}, \langle\langle v^{\text{priv}} \rangle\rangle_S, v^{\text{pub}})$  from  $S$ , do the following:
  - If a party  $X$  is corrupted, send  $v^{\text{pub}}$  to  $\mathcal{S}$ . Also send  $\langle\langle v^{\text{priv}} \rangle\rangle_X$  to the  $\mathcal{S}$ , and receive back an adjusted version of  $\langle\langle v^{\text{priv}} \rangle\rangle_X$ .
  - Recover  $v^{\text{priv}}$  from the shares; check the MACs. If an honest party's MAC does not verify, then stop.
  - Generate random  $r$  of the type expected by  $f$ .
  - If  $\mathbf{B}(v^{\text{priv}}, v^{\text{pub}}) = 0$ , then send  $(v^{\text{priv}}, v^{\text{pub}})$  to  $\mathcal{S}$  and get back  $w$ . Otherwise compute  $w = f(v^{\text{priv}}, v^{\text{pub}}, r)$ .
  - Randomly secret-share  $w$  (using  $\Delta_P$  and  $\Delta_S$ ), resulting in  $\langle\langle w \rangle\rangle$ .
  - If  $X$  is corrupted, then send  $\langle\langle w \rangle\rangle_X$  to  $\mathcal{S}$ .
  - Wait for the adversary to permit to proceed; also get updated value of  $\langle\langle w \rangle\rangle_X$ .
  - Send  $(\text{result}, \text{sid}, \langle\langle w \rangle\rangle_S)$  to  $S$  and  $(\text{result}, \text{sid}, \langle\langle w \rangle\rangle_P)$  to  $P$ .

**Functionality 4:** Generic AA-secure functionality for the function  $f$

- On input  $(\text{corrupt}, \text{sid}, X)$  from  $\mathcal{S}$  before the initialization, mark that  $X$  is corrupted and send  $(\text{corrupted}, \text{sid})$  to  $X$ .
- On input  $(\text{init}, \text{sid})$  from  $P$  and  $(\text{init}, \text{sid}, \Delta_S)$  from  $S$ : Store  $\Delta_S$ . If  $S$  is corrupted, then send  $(\text{globkeys}, \Delta_S)$  to the adversary.
- On input  $(\text{compute}, \text{sid}, \langle v^{\text{priv}} \rangle_P, v^{\text{pub}})$  from  $P$  and  $(\text{compute}, \text{sid}, \langle v^{\text{priv}} \rangle_S, v^{\text{pub}})$  from  $S$ , do the following:
  - If a party  $X$  is corrupted, send  $v^{\text{pub}}$  to  $\mathcal{S}$ . Also send  $\langle v^{\text{priv}} \rangle_X$  to the adversary, and receive back an adjusted version of  $\langle v^{\text{priv}} \rangle_X$ .
  - Recover  $v^{\text{priv}}$  from the shares; check the MACs. If  $S$  is honest, but the MACs does not verify, then stop.
  - Generate random  $r$  of the type expected by  $f$ .
  - If  $S$  is honest, then compute  $w = f(v^{\text{priv}}, v^{\text{pub}}, r)$ .
  - If  $S$  is corrupted, then randomly generate the output share  $\langle w \rangle_S$  and send it to the adversary.
  - If  $S$  is corrupted, then receive the description of an algorithm  $\mathfrak{A}$  from the adversary, compute  $w = \mathfrak{A}(\langle v^{\text{priv}} \rangle_P, r)$ ; it must have the correct format.
  - Compute a sharing of  $w$ , giving  $\langle w \rangle$ . If  $\langle w \rangle_S$  already exists, then compute  $\langle w \rangle_P$  according to it and  $w$  and  $\Delta_S$ .
  - If  $X$  is corrupted, then send  $\langle w \rangle_X$  to  $\mathcal{S}$ . Wait for the adversary's permission to proceed; also get updated  $\langle w \rangle_X$ .
  - Send  $(\text{result}, \text{sid}, \langle w \rangle_S)$  to  $S$  and  $(\text{result}, \text{sid}, \langle w \rangle_P)$  to  $P$ .

**Functionality 5:** Generic AP-secure functionality for the function  $f$

- On input  $(\text{init}, \text{sid}, \Delta_P)$  from  $P$  and  $(\text{init}, \text{sid}, \Delta_S)$  from  $S$ : Store  $\Delta_P$  and  $\Delta_S$ . If party  $X$  is corrupted, then send  $(\text{globkeys}, \Delta_X)$  to the adversary.
  - For AP-secure functionalities, the message from  $P$  does not contain  $\Delta_P$ .
- On input  $(\text{crrreq}, \text{sid})$  from both  $P$  and  $S$ , generate the random values according to the desired distribution. Let  $\mathbf{R}_P$  be the values meant for  $P$ , and  $\mathbf{R}_S$  the values meant for  $S$ .
- If no party is corrupted, then send  $(\text{proceed?}, \text{sid})$  to  $S$ . If party  $X$  is corrupted, then send  $(\text{cr}, \text{sid}, \mathbf{R}_X)$  to  $S$ .
- Wait for  $S$  to send back  $(\text{proceed!}, \text{sid})$  (if no party is corrupted) or  $(\text{update}, \text{sid}, \mathbf{R}'_X)$  (if party  $X$  is corrupted). In the latter case, update  $\mathbf{R}_X := \mathbf{R}'_X$ .
- Send  $(\text{cr}, \text{sid}, \mathbf{R}_P)$  to  $P$  and  $(\text{cr}, \text{sid}, \mathbf{R}_S)$  to  $S$ .

**Functionality 6:**  $\mathcal{F}_{\text{Alg}}^{\text{off}}$ : Generic ideal functionality for precomputations in an algorithm “Alg”

We see that Func. 5 is parametrized similarly to Func. 4, except we have no use for the predicate  $\mathbf{B}$ . Note how we model the passive security (but active privacy) against  $S$ : if  $S$  is corrupted then it chooses the output of the functionality on the basis of inputs, but cannot learn anything. The input to  $\mathfrak{A}$  includes everything that  $S$  does not yet know: the values  $\langle\langle v^{\text{priv}} \rangle\rangle_S$  and  $v^{\text{pub}}$  may be included in the description of  $\mathfrak{A}$ .

### 4.3 Defining precomputations

Certain algorithms in Sec. 3 (Alg. 4, Alg. 6, Alg. 7, Alg. 10, and Alg. 11) require the pre-distribution of values coming from a certain (joint) probability distribution. In the UC framework, we model the generation of such values as the availability of a certain ideal functionality, given in Func. 6. We see that it is initialized by the MAC keys. Indeed, the correlated random values may need MACs on them. The only protocol-specific part in this functionality is the actual sampling of random values.

### 4.4 Protocols from Algorithms

In Sec. 3, we have described our signing protocol and its subprotocols in the form of pseudocode typically used to present algorithms on top of MPC protocol sets. Each one of these algorithms could alternatively be represented as a real system in the UC framework, consisting of the machine  $\mathcal{M}_P$ , machine  $\mathcal{M}_S$ , and zero or more ideal components  $\mathcal{F}$ ; the description of such a real system would make explicit certain details that are not mentioned in the algorithms.

A real system executing a protocol  $\Pi_{\text{Alg}}$  (given in Prot. 3) for an algorithm  $\text{Alg}$  contains  $\mathcal{M}_P$ ,  $\mathcal{M}_S$ , the functionalities  $\mathcal{F}_{\text{sub}}$  for all subroutines that  $\text{Alg}$  invokes (except for subroutines that involve only local computations), and  $\mathcal{F}_{\text{Alg}}^{\text{off}}$  if  $\text{Alg}$  uses its own correlated randomness. Note that declassification is also realized by a functionality; protocols with AA security include  $\mathcal{F}_{\text{declassify}}^{\text{AA}}$  while protocols with AP security include  $\mathcal{F}_{\text{declassify}}^{\text{AP}}$ .

- On input  $(\text{corrupt}, \text{sid}, X)$  from  $\mathcal{S}$  before any other commands, mark that  $X$  is corrupted and send  $(\text{corrupted}, \text{sid})$  to  $X$ .
- On input  $(\text{init}, \text{sid}, \Delta_X)$  from  $X$  to machine  $\mathcal{M}_X$ , store  $\Delta_X$  and send  $(\text{init}, \text{sid}, \Delta_X)$  to all ideal components as coming from  $X$ . If  $X$  is corrupted, then send  $(\text{globkeys}, \text{sid}, \Delta_X)$  to  $\mathcal{A}$ . If  $\Pi_{\text{Alg}}$  is AP-secure, then there is no  $\Delta_P$ .
- On input  $(\text{compute}, \text{sid}, \text{inputs}_X)$  from  $X$  to  $\mathcal{M}_X$ , it proceeds as follows:
  1. If  $\text{Alg}$  uses correlated randomness, then send  $(\text{creq}, \text{sid})$  to  $\mathcal{F}_{\text{Alg}}^{\text{off}}$ . Get back  $\mathbf{R}_X$ .
  2. Execute steps of the algorithm  $\text{Alg}$  on  $\text{inputs}_X$  and  $\mathbf{R}_X$ , adding to the intermediate values  $\text{interm}_X$ . In particular:
    - In order to invoke a subroutine  $\text{sub}$ : come up with a session identifier  $\text{sid}'$  in a deterministic manner, e.g. adding the name of the invoked subroutine and the line in  $\text{Alg}$  containing that invocation to  $\text{sid}$ . Send  $(\text{creq}, \text{sid}', \text{args}_X)$  to  $\mathcal{F}_{\text{sub}}$ , where  $\text{args}_X$  contains the public, and shares of the private arguments to  $\text{sub}$ . Get back  $(\text{result}, \text{sid}', \text{res}_X)$  and add it to  $\text{interm}_X$ .
    - In order to rearrange data (e.g. rotate vectors) or perform local computations: do them.
    - In order to perform linear computations on secret-shared values (including the sharing of constants): perform them on local shares, including the MAC shares.
    - In order to send a message  $M$  (only in Alg. 10): send  $(\text{msg}, \text{sid}, M)$  to  $\mathcal{M}_{\bar{X}}$ , where  $\{X, \bar{X}\} = \{P, S\}$ .
    - In order to receive a message  $M$ : receive  $(\text{msg}, \text{sid}, M)$  from  $\mathcal{M}_{\bar{X}}$ .
  3. The execution has computed  $\text{res}_X$ . Send  $(\text{result}, \text{sid}, \text{res}_X)$  back to  $X$ .

**Protocol 3:** Generic two-party protocol from the description of an algorithm “Alg”:  $\Pi_{\text{Alg}}$

#### 4.5 Pre-existing functionalities

In Sec. 3.2, we stated that we make use of certain two-party computation protocols from the literature, the description of which we do not give in this paper. We still need to give the ideal functionalities corresponding to these protocols, in order to use the UC framework to give the security proofs of the protocols that we do describe. These protocols were the following:

- Generation of a public random value modulo  $q$ :  $\text{pubrand}()$ . The corresponding ideal functionality  $\mathcal{F}_{\text{pubrand}}$  is given in Func. 7.
- Generation of a random bit modulo  $q$ :  $\text{randbit}()$ . The functionality  $\mathcal{F}_{\text{randbit}}$  is an instance of Func. 4, where the function  $f$  takes no inputs and randomly returns either 0 or 1. The secret-sharing is done modulo  $q$ .
- Bit conversion from mod-2 to mod- $q$ :  $\text{bitconv}(\langle\langle b \rangle\rangle^{(2)})$ . The functionality  $\mathcal{F}_{\text{bitconv}}$  is an instance of Func. 4, where the function  $f$  is the identity function, inputs are shared modulo 2 and outputs are shared modulo  $q$ .
- If-then-else. The functionality  $\mathcal{F}_{\text{ite}}$  is an instance of Func. 4, where the function  $f$  takes three arguments: a bit  $b$  and two values (or equal-length vectors

- On input  $(\text{corrupt}, \text{sid}, X)$  from  $\mathcal{S}$  before any other commands, mark that  $X$  is corrupted and send  $(\text{corrupted}, \text{sid})$  to  $X$ .
- On input  $(\text{generate}, \text{sid})$  from both  $\mathcal{P}$  and  $\mathcal{S}$ : sample a value  $v$  according to the distribution  $\mathcal{D}$ . Let  $v_P \leftarrow v$  and  $v_S \leftarrow v$ . Send  $(\text{result}, \text{sid}, v)$  to  $\mathcal{S}$ . If party  $X$  is corrupted, then receive  $(\text{update}, \text{sid}, v_X)$  from  $\mathcal{S}$ , otherwise receive  $(\text{proceed}, \text{sid})$  from  $\mathcal{S}$ . Send  $(\text{result}, \text{sid}, v_P)$  to  $\mathcal{P}$  and  $(\text{result}, \text{sid}, v_S)$  to  $\mathcal{S}$ .

**Functionality 7:** Public randomness generation from the distribution  $\mathcal{D}$ :  
 $\mathcal{F}_{\text{pubrand}}^{\mathcal{D}}$

of values)  $x$  and  $y$ , all secret-shared with the same modulus. The function  $f$  returns  $b \cdot (x - y) + y$ .

#### 4.6 Randomness generation

Alg. 4, parametrized with a small prime  $Q$ , gives an algorithm to generate a secret-shared random value between 0 and  $Q - 1$ . It corresponds to the protocol  $\Pi_{\text{gen\_small}}$ , which is an instance of Prot. 3. The protocol securely realizes the ideal functionality  $\mathcal{F}_{\text{gen\_small}}$  that is an instance of Func. 4, where the function  $f$  takes no arguments and returns a random value between 0 and  $Q - 1$ ; the output is secret-shared modulo  $q$ . The protocol  $\Pi_{\text{gen\_small}}$  is realized by a system consisting of  $\mathcal{M}_P$ ,  $\mathcal{M}_S$ ,  $\mathcal{F}_{\text{pubrand}}$ , and  $\mathcal{F}_{\text{gen\_small}}^{\text{off}}$  (an instance of Func. 6).

In order to show that  $\Pi_{\text{gen\_small}}$  is at least secure as  $\mathcal{F}_{\text{gen\_small}}$ , we have to construct a suitable simulator. As always, we present a construction for corrupted  $\mathcal{P}$  and another one for corrupted  $\mathcal{S}$ .

**Simulator for corrupted phone.** During initialization, the simulator learns Phone’s MAC keys via the message  $(\text{globkeys}, \Delta_P)$  from  $\mathcal{F}_{\text{gen\_small}}$ .

During computation, the simulator gets notified of the start of the computation from the ideal functionality (normally, it would get the input share of  $\mathcal{P}$  at this point, but there are no inputs to this computation). The simulator notifies  $\mathcal{A}$  of that start (via a message presumably coming from  $\mathcal{M}_P$ , telling  $\mathcal{A}$  that  $\mathcal{M}_P$  received the `compute`-command from  $\mathcal{P}$ ). The simulator asks  $\mathcal{F}_{\text{gen\_small}}$  to proceed (there are not inputs to update), and receives the output  $\langle\langle v \rangle\rangle_P$ . The simulator generates a random value  $r \in \mathbb{Z}_Q$  and a random length- $Q$  vector  $\langle\langle \mathbf{t} \rangle\rangle_P$ , such that its value at position  $r$  is equal to  $\langle\langle v \rangle\rangle_P$ . The simulator sends  $(\text{cr}, \text{sid}, \langle\langle \mathbf{t} \rangle\rangle_P)$  (as if coming from  $\mathcal{F}_{\text{gen\_small}}^{\text{off}}$ ) to  $\mathcal{A}$  and gets back  $(\text{update}, \text{sid}, \langle\langle \mathbf{t}' \rangle\rangle_P)$ .

The simulator expects the message  $(\text{generate}, \text{sid})$  from  $\mathcal{A}$  (as command for  $\mathcal{M}_P$  to send this to  $\mathcal{F}_{\text{pubrand}}$ ). The simulator (acting as  $\mathcal{F}_{\text{pubrand}}$ ) tells  $\mathcal{A}$  that the result is  $r$ ; the adversary  $\mathcal{A}$  may update this to  $r'$ .

The adversary now tells  $\mathcal{F}_{\text{gen\_small}}$  to proceed and, moreover, update the output of  $\mathcal{P}$  to  $\langle\langle t'_{r'} \rangle\rangle_P$ .

**Simulator for corrupted server.** The protocol is symmetric wrt. the roles of Phone and Server. The simulator will be the same, with P and S swapped.

#### 4.7 Key generation

Alg. 5 gives the algorithm for ML-DSA private key generation, such that the result is secret-shared. The algorithm realizes the ideal functionality  $\mathcal{F}_{\text{keygen}}$  that is a specialization of Func. 4, where the function  $f$  receives no inputs and returns a fresh private key of ML-DSA. We define the protocol  $\Pi_{\text{keygen}}$  an instance of Prot. 3, corresponding to Alg. 5.

The real system realizing  $\Pi_{\text{keygen}}$  consists of the machines  $\mathcal{M}_P$  and  $\mathcal{M}_S$ , and the ideal functionality  $\mathcal{F}_{\text{gen\_small}}$ . Let us construct simulators for corrupted P and for corrupted S.

**Simulator for corrupted phone.** During initialization, the simulator learns Phone's MAC keys via the message  $(\text{globkeys}, \Delta_P)$  from  $\mathcal{F}_{\text{keygen}}$ .

During computation, the simulator receives  $\mathbf{A}$  from  $\mathcal{F}_{\text{keygen}}$ , indicating the start of the computation. The simulator notified  $\mathcal{A}$  and sends  $\mathbf{A}$  to it (to  $\mathcal{A}$ , the origin of the message appears to be  $\mathcal{M}_P$ ). The simulator asks  $\mathcal{F}_{\text{keygen}}$  to proceed; it receives the shares  $\langle\langle \mathbf{s}_1 \rangle\rangle_P$ , and  $\langle\langle \mathbf{s}_2 \rangle\rangle_P$  and  $\langle\langle \mathbf{t} \rangle\rangle_P$ .

The simulator turns  $\langle\langle \mathbf{s}_1 \rangle\rangle_P$  and  $\langle\langle \mathbf{s}_2 \rangle\rangle_P$  into the shares of the random values that are presumably generated by  $\mathcal{F}_{\text{gen\_small}}$ . Let  $\langle\langle r_{i,t} \rangle\rangle_P$  be one of the coefficients of one of the polynomials in either  $\langle\langle \mathbf{s}_1 \rangle\rangle_P$  or  $\langle\langle \mathbf{s}_2 \rangle\rangle_P$ . If  $\eta = 2$ , then define  $\langle\langle r'_{i,t} \rangle\rangle_P \leftarrow \langle\langle r_{i,t} \rangle\rangle_P + \langle\langle 2 \rangle\rangle_P$ , where  $\langle\langle 2 \rangle\rangle_P$  denotes the classification of the constant 2. If  $\eta = 4$  then let  $\langle\langle r'_{i,t} \rangle\rangle_P$  be a random element of  $\mathbb{Z}_q$  and define  $\langle\langle r''_{i,t} \rangle\rangle_P \leftarrow \langle\langle r_{i,t} \rangle\rangle_P - 3 \cdot \langle\langle r'_{i,t} \rangle\rangle_P + \langle\langle 4 \rangle\rangle_P$ .

The simulator waits for  $\mathcal{A}$ 's command (to  $\mathcal{M}_P$ ; supposed to be passed to  $\mathcal{F}_{\text{gen\_small}}$ ) to start some session of  $\mathcal{F}_{\text{gen\_small}}$ ; the session identifier  $sid'$  used in the activation indicates, which position of  $\mathbf{s}_1$  or  $\mathbf{s}_2$  this invocation corresponds to (and in case of  $\eta = 4$ : whether it corresponds to upper or lower half of the private value). The simulator sends back  $\langle\langle r'_{i,t} \rangle\rangle_P$  or  $\langle\langle r''_{i,t} \rangle\rangle_P$ , according to the position that  $\mathcal{A}$  chose. The adversary  $\mathcal{A}$  has to send the activations for all positions in  $\mathbf{s}_1$  and  $\mathbf{s}_2$ . The adversary can also send updates to  $\langle\langle r'_{i,t} \rangle\rangle_P$  and  $\langle\langle r''_{i,t} \rangle\rangle_P$ ; the simulator records these.

Once the generations of all small random values have been completed, the simulator updates  $\langle\langle \mathbf{s}_1 \rangle\rangle_P$  and  $\langle\langle \mathbf{s}_2 \rangle\rangle_P$ , recomputing them with the updated  $\langle\langle r'_{i,t} \rangle\rangle_P$  and  $\langle\langle r''_{i,t} \rangle\rangle_P$ . The simulator tells  $\mathcal{F}_{\text{keygen}}$  to proceed, with updated  $\langle\langle \mathbf{s}_1 \rangle\rangle_P$  and  $\langle\langle \mathbf{s}_2 \rangle\rangle_P$ .

**Simulator for corrupted server.** The protocol is symmetric wrt. the roles of Phone and Server. The simulator is the same as the one for corrupted Phone, with P and S swapped.

#### 4.8 Zero equality checking

Alg. 6 gives the algorithm for checking whether a private value  $\langle\langle v \rangle\rangle$  is equal to 0, with the precondition  $v < B$ , where  $B$  is a rather small bound. It corresponds to a protocol  $\Pi_{\text{zero\_check}}$ , which is an instance of Prot. 3. The protocol realizes the ideal functionality  $\mathcal{F}_{\text{zero\_check}}$ , which is an instance of Func. 4, where the function  $f$  returns 1 iff  $v = 0$  (and returns 0 otherwise). The predicate  $\mathbf{B}$  returns 1 if  $v < B$ .

The protocol  $\Pi_{\text{zero\_check}}$  is realized by a system consisting of machines  $\mathcal{M}_{\mathbf{P}}$  and  $\mathcal{M}_{\mathbf{S}}$ , as well as the functionalities  $\mathcal{F}_{\text{declassify}}^{\text{AA}}$  and  $\mathcal{F}_{\text{zero\_check}}^{\text{off}}$ . The latter is an instance of Func. 6. Let us present the simulators to show that  $\Pi_{\text{zero\_check}}$  is at least as secure as  $\mathcal{F}_{\text{zero\_check}}$ .

**Simulator for corrupted phone.** During initialization, the simulator learns Phone’s MAC keys via the message (globkeys,  $\Delta_{\mathbf{P}}$ ) from  $\mathcal{F}_{\text{keygen}}$ .

During computation (with session ID  $sid$ ), the simulator receives  $\langle\langle v \rangle\rangle_{\mathbf{P}}$  from  $\mathcal{F}_{\text{zero\_check}}$ , and forwards it to  $\mathcal{A}$  (as if coming from  $\mathcal{M}_{\mathbf{P}}$  that reports the compute-command from the environment). If the simulator does not also receive  $v$ , then it knows that  $v < B$ . The simulator asks the ideal functionality to proceed, learning  $\langle\langle b \rangle\rangle_{\mathbf{P}}$ , where  $b$  is the output of the zero check.

The simulator generates a random  $u \in \mathbb{Z}_q$ , and random values  $\langle\langle m \rangle\rangle_{\mathbf{P}}$  and  $\langle\langle \mathbf{t} \rangle\rangle_{\mathbf{P}}$ , except that the element  $\langle\langle t_{\lfloor u/(q/B) \rfloor} \rangle\rangle_{\mathbf{P}}$  is taken to be equal to  $\langle\langle b \rangle\rangle_{\mathbf{P}}$ . The simulator sends  $\langle\langle m \rangle\rangle_{\mathbf{P}}$  and  $\langle\langle \mathbf{t} \rangle\rangle_{\mathbf{P}}$  to  $\mathcal{A}$ , as if coming from  $\mathcal{F}_{\text{zero\_check}}^{\text{off}}$ . The adversary may send back adjustments to these values. Let  $\langle\langle \mathbf{t}' \rangle\rangle_{\mathbf{P}}$  the adjusted value for  $\langle\langle \mathbf{t} \rangle\rangle_{\mathbf{P}}$ .

The simulator waits  $\mathcal{A}$  to instruct  $\mathcal{M}_{\mathbf{P}}$  to declassify the value  $\langle\langle d \rangle\rangle_{\mathbf{P}} = \langle\langle m \rangle\rangle_{\mathbf{P}} + \lfloor q/B \rfloor \cdot \langle\langle v \rangle\rangle_{\mathbf{P}}$ . The simulator already knows  $\langle\langle d \rangle\rangle_{\mathbf{P}}$ , because it is able to compute it itself. Let  $\widetilde{\langle\langle d \rangle\rangle_{\mathbf{P}}}$  denote the value computed by the simulator, while  $\widehat{\langle\langle d \rangle\rangle_{\mathbf{P}}}$  denotes the value submitted by the adversary. The simulator now computes

$$\begin{aligned} - \quad \nu \langle\langle d \rangle\rangle_{\mathbf{S}} &\leftarrow u - \nu \widetilde{\langle\langle d \rangle\rangle_{\mathbf{P}}}, \text{ and} \\ - \quad \text{mp} \langle\langle d \rangle\rangle_{\mathbf{S}} &\leftarrow \nu \langle\langle d \rangle\rangle_{\mathbf{S}} \cdot \Delta_{\mathbf{S}} - \text{mp} \widetilde{\langle\langle d \rangle\rangle_{\mathbf{P}}}, \end{aligned}$$

and sends (input,  $sid$ ,  $\widehat{\langle\langle d \rangle\rangle_{\mathbf{P}}}$ ,  $\nu \langle\langle d \rangle\rangle_{\mathbf{S}}$ ,  $\text{mp} \langle\langle d \rangle\rangle_{\mathbf{S}}$ ) to the adversary (as if coming from  $\mathcal{F}_{\text{declassify}}^{\text{AA}}$ ). The adversary  $\mathcal{A}$  sends back an update to  $\langle\langle d \rangle\rangle_{\mathbf{P}}$ . If the update is not equal to  $\widetilde{\langle\langle d \rangle\rangle_{\mathbf{P}}}$  (at least for the  $\nu$ - and  $\text{mp}$ -parts), then the simulator tells  $\mathcal{F}_{\text{zero\_check}}$  to stop. Otherwise the simulator sends (result?,  $sid$ ) to the adversary (as if coming from  $\mathcal{F}_{\text{declassify}}^{\text{AA}}$ ) and gets back (result!,  $sid$ ,  $u'$ ,  $b_{\text{cnt}}$ ). If the boolean  $b_{\text{cnt}}$  is false then it tells  $\mathcal{F}_{\text{zero\_check}}$  to stop. If  $u' \neq u$ , then the simulator updates  $\langle\langle b \rangle\rangle_{\mathbf{P}}$  to  $\langle\langle t_{\lfloor u'/(q/B) \rfloor} \rangle\rangle_{\mathbf{P}}$ .

At this point, the to-be-declassified value is fixed as  $u'$ , and the adversary is able to find the output share  $\langle\langle b \rangle\rangle_{\mathbf{P}}$ . The adversary can instruct  $\mathcal{M}_{\mathbf{P}}$  to output something else —  $\langle\langle b' \rangle\rangle_{\mathbf{P}}$  — to the environment. The simulator tells  $\mathcal{F}_{\text{zero\_check}}$  to proceed, with the output updated to  $\langle\langle b' \rangle\rangle_{\mathbf{P}}$ . Note that if  $u' \neq u$ , and  $\mathcal{F}_{\text{zero\_check}}$

is used as a component in a larger protocol (e.g. signing), then the execution of the whole protocol will halt at its next step.

If the simulator also received  $v$  at the time the computation was started, then it first generates a random  $m \in \mathbb{Z}_q$ , and computes  $u \leftarrow (m + \lfloor q/B \rfloor \cdot v) \bmod q$ . It continues as before, secret-sharing  $m$  and generating  $\langle\langle \mathbf{t} \rangle\rangle_{\mathcal{P}}$  so that  $\langle\langle t_{\lfloor u/(\lfloor q/B \rfloor)} \rangle\rangle_{\mathcal{P}} = \langle\langle b \rangle\rangle_{\mathcal{P}}$ .

**Simulator for corrupted server.** This is very similar to the simulator for corrupted phone. The only difference arises from the asymmetry of declassification.

The initialization of the components (during which the simulator learns  $\Delta_{\mathcal{S}}$ ) works the same way, as does the receiving of the share  $\langle\langle v \rangle\rangle_{\mathcal{S}}$ , the generation of the shares of correlated randomness, the updating of the correlated randomness, the computation of  $\langle\langle d \rangle\rangle_{\mathcal{S}}$  and its submission to declassification. The simulator now sends  $(\text{input}, \text{sid}, \langle\langle d \rangle\rangle_{\mathcal{S}})$  to  $\mathcal{A}$  (as if coming from  $\mathcal{F}_{\text{declassify}}^{\text{AA}}$ ). The simulator may update it with its `update`-command, but in the end, the `v`- and `mp`-components must be equal to  $\langle\langle d \rangle\rangle_{\mathcal{S}}$  as computed by the simulator; if they are different then the simulator tells  $\mathcal{F}_{\text{zero\_check}}$  to stop.

The simulator sends  $(\text{result?}, \text{sid}, u, \text{true})$  to  $\mathcal{A}$  (as if coming from  $\mathcal{F}_{\text{declassify}}^{\text{AA}}$ ) and gets back  $(\text{result}, \text{sid}, u')$ . If  $u' \neq u$ , then the simulator updates  $\langle\langle b \rangle\rangle_{\mathcal{S}}$  to  $\langle\langle t_{\lfloor u'/(\lfloor q/B \rfloor)} \rangle\rangle_{\mathcal{S}}$ . The adversary can instruct  $\mathcal{M}_{\mathcal{S}}$  to output something else —  $\langle\langle b' \rangle\rangle_{\mathcal{S}}$  — to the environment. The simulator tells  $\mathcal{F}_{\text{zero\_check}}$  to proceed, with the output updated to  $\langle\langle b' \rangle\rangle_{\mathcal{S}}$ .

#### 4.9 Symmetric reshare to parts

Alg. 7 gives the algorithm for “splitting a private value into digits”, parametrized with the basis  $\mathbf{r} = (r_0, \dots, r_{n-1})$ . It corresponds to a protocol  $\Pi_{\text{symm\_rp}}$ , an instance of Prot. 3, which is at least as secure as the ideal functionality  $\mathcal{F}_{\text{symm\_rp}}$ , which is an instance of Func. 4, where

- the randomness  $\hat{r}$  is a random element of  $\{0, \dots, q-1\}$ ;
- the function  $f(v, \hat{r})$  checks whether  $v \geq \hat{r}$ . If so, then  $f$  splits  $v$  into digits. If not, then  $f$  splits  $v+q$  into digits. In either case,  $f$  returns the characteristic vectors of these digits.

The protocol  $\Pi_{\text{symm\_rp}}$  is realized by a system consisting of machines  $\mathcal{M}_{\mathcal{P}}$  and  $\mathcal{M}_{\mathcal{S}}$ , as well as the ideal components  $\mathcal{F}_{\text{declassify}}^{\text{AA}}$ ,  $\mathcal{F}_{\text{ite}}$ ,  $\mathcal{F}_{\text{convbit}}$  and  $\mathcal{F}_{\text{symm\_rp}}^{\text{off}}$ . The latter is an instance of Func. 6, generating a random  $s \in \mathbb{Z}_q$  and the characteristic vectors  $\mathbf{d}_0, \dots, \mathbf{d}_{n-1}$  of its digits. Let us present the simulators showing that  $\Pi_{\text{symm\_rp}}$  indeed securely realizes  $\mathcal{F}_{\text{symm\_rp}}$ .

**Simulator for corrupted phone.** During initialization, the simulator learns Phone’s MAC keys via the message  $(\text{globkeys}, \Delta_{\mathcal{P}})$  from  $\mathcal{F}_{\text{keygen}}$ .

During computation, the simulator receives the input  $\langle\langle w \rangle\rangle_{\mathcal{P}}$  from  $\mathcal{F}_{\text{symm\_rp}}$ , and sends it to  $\mathcal{A}$ . Asks  $\mathcal{F}_{\text{symm\_rp}}$  to proceed, and learns the output shares  $\langle\langle \mathbf{b}_0 \rangle\rangle_{\mathcal{P}}, \dots, \langle\langle \mathbf{b}_{n-1} \rangle\rangle_{\mathcal{P}}$ .

The simulator is able to generate the values and random shares according to the probability distribution that  $\mathcal{M}_{\mathcal{P}}$  would see them distributed. This is straightforward:

- $\langle\langle s \rangle\rangle_{\mathcal{P}}$  is a triple  $(\langle\langle s \rangle\rangle_{\mathcal{P}, \text{ms}}, \langle\langle s \rangle\rangle_{\mathcal{P}, \text{mp}}, \langle\langle s \rangle\rangle_{\mathcal{P}, \text{v}})$  of random elements of  $\mathbb{Z}_q$  (for the MAC shares, a vector of random elements);
- similarly, each element of the vectors  $\langle\langle \mathbf{d}_i \rangle\rangle_{\mathcal{P}}$  is a triple of a random value and two vectors of random values over  $\mathbb{Z}_2$ ;
- the additional elements of extended vectors  $\langle\langle \mathbf{d}_i \rangle\rangle_{\mathcal{P}}$  are  $\langle\langle 0 \rangle\rangle_{\mathcal{P}}$ ;
- the argument to declassification is  $\langle\langle w \rangle\rangle_{\mathcal{P}} - \langle\langle s \rangle\rangle_{\mathcal{P}}$ ;
- $y$  is a random element of  $\mathbb{Z}_q$ ;
- $(z_0, \dots, z_{n-1})$  are the digits of  $y$ ;
- $\langle\langle \mathbf{p}'_i \rangle\rangle_{\mathcal{P}}$  and  $\langle\langle \mathbf{p}''_i \rangle\rangle_{\mathcal{P}}$  are rotations of  $\langle\langle \mathbf{d}_i \rangle\rangle_{\mathcal{P}}$  by  $z_i$  and  $(z_i + 1)$  positions, respectively;
- $\langle\langle \mathbf{p}_0 \rangle\rangle_{\mathcal{P}} = \langle\langle \mathbf{p}'_0 \rangle\rangle_{\mathcal{P}}$ ;
- each element of the vectors  $\langle\langle \mathbf{p}_i \rangle\rangle_{\mathcal{P}}$  ( $i \geq 1$ ) is a triple of a random value and two vectors of random values over  $\mathbb{Z}_2$ ;
- $\langle\langle c_i \rangle\rangle_{\mathcal{P}}$  ( $i \geq 1$ ) is the sum (i.e. XOR, computed point-wise) of certain elements of  $\langle\langle \mathbf{p}_i \rangle\rangle_{\mathcal{P}}$ ;
- arguments to `convbit` are sums (i.e. XORs) of certain bits in the vectors  $\langle\langle \mathbf{p}_i \rangle\rangle_{\mathcal{P}}$ .

The simulator does not generate these values ahead of time, but only during the simulated execution of the protocol together with the adversary  $\mathcal{A}$ . It starts with sending  $\langle\langle s \rangle\rangle_{\mathcal{P}}$  and  $\langle\langle \mathbf{d}_i \rangle\rangle_{\mathcal{P}}$  to the adversary, as if coming from  $\mathcal{F}_{\text{symm\_rp}}^{\text{off}}$ . The adversary may adjust these values, but any adjustments to the `v`- or `ms`-components will lead to the simulator instructing  $\mathcal{F}_{\text{symm\_rp}}$  to stop execution; this instruction may come either immediately or only at the time when the adjusted value is used in  $\mathcal{F}_{\text{declassify}}^{\text{AA}}$  (for  $\langle\langle s \rangle\rangle_{\mathcal{P}}$ ) or in  $\mathcal{F}_{\text{ite}}$  (for  $\langle\langle \mathbf{d}_i \rangle\rangle_{\mathcal{P}}$ ).

The adversary will ask  $\mathcal{M}_{\mathcal{P}}$  to invoke  $\mathcal{F}_{\text{declassify}}^{\text{AA}}$  in order to obtain  $y$ . The simulator simulates declassification identically to App. 4.8: there is again the share  $\widehat{\langle\langle y \rangle\rangle_{\mathcal{P}}}$  computed by the simulator, and the share  $\widehat{\langle\langle y \rangle\rangle_{\mathcal{P}}}$  initially submitted by the adversary as-if through  $\mathcal{M}_{\mathcal{P}}$  and later corrected as-if through  $\mathcal{F}_{\text{declassify}}^{\text{AA}}$ . There is the simulator computing components of  $\langle\langle y \rangle\rangle_{\mathcal{S}}$  (on the basis of the value  $y$  that it has chosen) and sending them to  $\mathcal{A}$ . In the end, the adversary may try to adjust  $y$  through the `result`-messages; this again leads to the stopping of the execution later, at the point where the changed digit  $z_i$  is handled in the main loop.

The adversary will ask  $\mathcal{M}_{\mathcal{P}}$  to invoke  $\mathcal{F}_{\text{ite}}$  for  $n$  times in a row. If the adversary adjusts the `v`- or `ms`-part of the outputs of  $\mathcal{F}_{\text{ite}}$ , then these will be caught either in the next iteration (if they change the `v`- or `ms`-part of the share of the carry bit), or in the invocation of  $\mathcal{F}_{\text{convbit}}$ .

The adversary will ask  $\mathcal{M}_{\mathcal{P}}$  to invoke  $\mathcal{F}_{\text{convbit}}$  for  $\sum \mathbf{r}$  times. The simulator will give it the components of  $\langle\langle \mathbf{b}_0 \rangle\rangle_{\mathcal{P}}, \dots, \langle\langle \mathbf{b}_{n-1} \rangle\rangle_{\mathcal{P}}$  as results. The adversary



may adjust these results. The simulator tells  $\mathcal{F}_{\text{symm\_rp}}$  to proceed and to give the adjusted values to P.

**Simulator for corrupted server.** The protocol is (almost) symmetric. The simulator for corrupted server works almost the same as the simulator for corrupted phone; the changes in handling asymmetric  $\mathcal{F}_{\text{declassify}}^{\text{AA}}$  are the same as in App. 4.8.

#### 4.10 Finding the high bits

Alg. 8 gives the algorithm for finding the high bits of a value  $\langle\langle w \rangle\rangle$ . It corresponds to a protocol  $\Pi_{\text{high\_bits}}$ , an instance of Prot. 3, which is at least as secure as the ideal functionality  $\mathcal{F}_{\text{high\_bits}}$ , which is an instance of Func. 4, where the function  $f$  computes the high bits of its argument. The real system implementing  $\Pi_{\text{high\_bits}}$  consists of machines  $\mathcal{M}_P$  and  $\mathcal{M}_S$ , as well as ideal components  $\mathcal{F}_{\text{symm\_rp}}$  and  $\mathcal{F}_{\text{zero\_check}}$ . Let us construct the simulators showing that  $\Pi_{\text{high\_bits}}$  is at least as secure as  $\mathcal{F}_{\text{high\_bits}}$ .

**Simulator for corrupted phone.** During initialization, the simulator learns Phone's MAC keys via the message  $(\text{globkeys}, \Delta_P)$  from  $\mathcal{F}_{\text{keygen}}$ .

During computation, the simulator receives the input  $\langle\langle w \rangle\rangle_P$  from  $\mathcal{F}_{\text{high\_bits}}$ , and sends it to  $\mathcal{A}$ . Asks  $\mathcal{F}_{\text{high\_bits}}$  to proceed, and learns the output share  $\langle\langle v \rangle\rangle_P$ .

The simulator waits for  $\mathcal{A}$  to ask  $\mathcal{M}_P$  to start the computation of  $\mathcal{F}_{\text{symm\_rp}}$  with the input share  $\langle\langle w' \rangle\rangle_P$ , where  $\langle\langle w' \rangle\rangle_P = \langle\langle w \rangle\rangle_P + \langle\langle \alpha/2 - 1 \rangle\rangle_P$ . The adversary can change that input before  $\mathcal{F}_{\text{symm\_rp}}$  begins. If the adversary changes either the  $v$ -component or the  $ms$ -component of  $\langle\langle w' \rangle\rangle_P$ , then the simulator tells  $\mathcal{F}_{\text{high\_bits}}$  to stop.

The simulator generates random vectors  $\langle\langle \mathbf{b}_0 \rangle\rangle_P, \dots, \langle\langle \mathbf{b}_4 \rangle\rangle_P$  and sends these to the adversary as the output shares from  $\mathcal{F}_{\text{symm\_rp}}$ . It also computes  $\langle\langle g \rangle\rangle_P \leftarrow \langle\langle 5 \rangle\rangle_P - \sum_{i=0}^3 \langle\langle b_{i,0} \rangle\rangle_P - \sum_{i=s+1}^{2s} \langle\langle b_{4,i} \rangle\rangle_P$ . The adversary may change  $\langle\langle \mathbf{b}_0 \rangle\rangle_P, \dots, \langle\langle \mathbf{b}_4 \rangle\rangle_P$ .

The simulator waits for  $\mathcal{A}$  to ask  $\mathcal{M}_P$  to start the computation of  $\mathcal{F}_{\text{zero\_check}}$  with the input share  $\langle\langle g \rangle\rangle_P$ . The adversary may change that input before  $\mathcal{F}_{\text{zero\_check}}$  begins. But if its  $v$ - or  $ms$ -components differ from the values that the simulator has previously computed, then the simulator tells  $\mathcal{F}_{\text{high\_bits}}$  to stop.

The simulator computes  $\langle\langle c \rangle\rangle_P \leftarrow \sum_{i=0}^{s-1} i \cdot \langle\langle b_{4,i} \rangle\rangle_P + \sum_{i=s}^{2s} (i-s) \cdot \langle\langle b_{4,i} \rangle\rangle_P - \langle\langle v \rangle\rangle_P$ . The simulator sends  $\langle\langle c \rangle\rangle_P$  to the adversary as the output share from  $\mathcal{F}_{\text{zero\_check}}$ . The adversary may now give an adjusted value of  $\langle\langle v \rangle\rangle_P$  to the simulator. The simulator forwards this to  $\mathcal{F}_{\text{high\_bits}}$  and asks it to proceed.

**Simulator for corrupted server.** The protocol is symmetric wrt. the roles of Phone and Server. The simulator is the same as the one for corrupted Phone, with P and S swapped.

#### 4.11 Commitment computation

Alg. 9 gives the algorithm for creating the secret  $\langle\langle \mathbf{y} \rangle\rangle$  and the corresponding FSwA commitment  $\langle\langle \mathbf{w}^H \rangle\rangle$ . We consider it as a template for the protocol  $\Pi_{\text{gcom}}$  (an instance of Prot. 3). The inputs and outputs of the protocol  $\Pi_{\text{gcom}}$  are the same as those of the ideal functionality  $\mathcal{F}_{\text{gcom}}$ . This functionality is an instance of Func. 4, where the function  $f$  receives the public matrix  $\mathbf{A}$  as an input and returns a random vector  $\mathbf{y}$  and  $\mathbf{w}^H = \text{HighBits}(\mathbf{A} \cdot \mathbf{y})$ .

The protocol  $\Pi_{\text{gcom}}$  is realized by a system consisting of the machines  $\mathcal{M}_{\text{P}}$  and  $\mathcal{M}_{\text{S}}$ , as well as the ideal functionalities  $\mathcal{F}_{\text{high\_bits}}$  and  $\mathcal{F}_{\text{randbit}}$ . Let us present the simulators showing that  $\Pi_{\text{gcom}}$  is at least as secure as  $\mathcal{F}_{\text{gcom}}$ .

**Simulator for corrupted phone.** During initialization, the simulator learns the MAC keys  $\Delta_{\text{P}}$  of the phone via the message (globkeys,  $\Delta_{\text{P}}$ ) from  $\mathcal{F}_{\text{keygen}}$ .

During computation, the simulator receives the input  $\mathbf{A}$  from  $\mathcal{F}_{\text{gcom}}$ , and sends it to  $\mathcal{A}$ . Asks  $\mathcal{F}_{\text{gcom}}$  to proceed and learns the output shares  $\langle\langle \mathbf{y} \rangle\rangle_{\text{P}}$  and  $\langle\langle \mathbf{w}^H \rangle\rangle_{\text{P}}$ .

The simulator waits for  $\mathcal{A}$  to ask  $\mathcal{M}_{\text{P}}$  to start the computations of  $\mathcal{F}_{\text{randbit}}$ . The simulator comes up with random shares  $\langle\langle b_{i,t,u} \rangle\rangle_{\text{P}}$  for  $i \in [\ell]$ ,  $t \in [256]$ , and  $u \in [(\log \gamma_1) + 1]$ , with the condition that the relevant linear combinations of these shares were equal to the coefficients of the elements of  $\langle\langle \mathbf{y} \rangle\rangle_{\text{P}}$ . Concretely, for each  $i \in [\ell]$  and  $t \in [256]$ , the simulator

- randomly generates  $\langle\langle b_{i,t,u} \rangle\rangle_{\text{P}}$  for  $u \in \{1, \dots, \log \gamma_1\}$ ,
- defines  $\langle\langle b_{i,t,0} \rangle\rangle_{\text{P}} \leftarrow \text{cff}_t(\langle\langle y_i \rangle\rangle_{\text{P}}) - \sum_{u=1}^{\log \gamma_1} 2^u \cdot \langle\langle b_{i,t,u} \rangle\rangle_{\text{P}}$ ,

where  $\text{cff}_t(p)$  denotes the coefficient of  $X^t$  in the polynomial  $p$ .

The simulator computes  $\langle\langle \mathbf{w} \rangle\rangle_{\text{P}} \leftarrow \mathbf{A} \cdot \langle\langle \mathbf{y} \rangle\rangle_{\text{P}}$ . Also, the simulator sends all  $\langle\langle b_{i,t,u} \rangle\rangle_{\text{P}}$  to  $\mathcal{A}$ , as output shares from the computations performed by  $\mathcal{F}_{\text{randbit}}$ . The adversary may adjust these shares, and the simulator will update  $\langle\langle \mathbf{y} \rangle\rangle_{\text{P}}$  and  $\langle\langle \mathbf{w} \rangle\rangle_{\text{P}}$  accordingly.

The simulator waits for  $\mathcal{A}$  to ask  $\mathcal{M}_{\text{P}}$  to start the computations of  $\mathcal{F}_{\text{high\_bits}}$  for each coefficient of each element of  $\langle\langle \mathbf{w} \rangle\rangle_{\text{P}}$ . The adversary may adjust these inputs by communicating with (presumably)  $\mathcal{F}_{\text{high\_bits}}$ . But if adversary's adjustments either to  $\langle\langle w_{i,t} \rangle\rangle_{\text{P}}$  or (earlier) to  $\langle\langle b_{i,t,u} \rangle\rangle_{\text{P}}$  cause the  $v$ - or  $ms$ -components of  $\langle\langle \mathbf{w} \rangle\rangle_{\text{P}}$  to change from what the simulator originally computed, then the simulator will tell  $\mathcal{F}_{\text{gcom}}$  to stop.

The simulator will send  $\langle\langle \mathbf{w}^H \rangle\rangle_{\text{P}}$  to the adversary as the output shares of computations of  $\mathcal{F}_{\text{high\_bits}}$ . The adversary may adjust these; the simulator will send adjusted  $\langle\langle \mathbf{w}^H \rangle\rangle_{\text{P}}$  (and also  $\langle\langle \mathbf{y} \rangle\rangle_{\text{P}}$ ) back to  $\mathcal{F}_{\text{gcom}}$  and tell it to proceed.

**Simulator for corrupted server.** The protocol is symmetric wrt. the roles of Phone and Server.

The simulator is the same as the one for corrupted Phone, with P and S swapped.

- On input  $(\text{corrupt}, \text{sid}, X)$  from  $\mathcal{S}$  before the initialization, mark that  $X$  is corrupted and send  $(\text{corrupted}, \text{sid})$  to  $X$ .
- On input  $(\text{init}, \text{sid})$  from  $\mathcal{P}$  and  $(\text{init}, \text{sid}, \Delta_S)$  from  $\mathcal{S}$ : Store  $\Delta_S$ . If  $\mathcal{S}$  is corrupted, then send  $(\text{globkeys}, \Delta_S)$  to the adversary.
- On input  $(\text{compute}, \text{sid}, \langle v \rangle_{\mathcal{P}})$  from  $\mathcal{P}$  and  $(\text{compute}, \text{sid}, \langle v \rangle_{\mathcal{S}})$  from  $\mathcal{S}$ , do the following:
  - If a party  $X$  is corrupted, send  $\langle v \rangle_X$  to the adversary, and receive back an adjusted version of  $\langle v \rangle_X$ .
  - Recover  $v$  from the shares; check the MACs. If  $\mathcal{S}$  is honest, but the MACs does not verify, then stop.
  - Pick random  $v_{\mathcal{P}} \in \mathbb{Z}_q$  and put  $v_{\mathcal{S}} \leftarrow (v - v_{\mathcal{P}}) \bmod q$ .
  - Let  $\mathbf{x}_{\mathcal{P}} \leftarrow \text{split}(v_{\mathcal{P}})$  and  $\mathbf{x}_{\mathcal{S}} \leftarrow \text{split}(v_{\mathcal{S}})$ . Let  $\mathbf{r} \leftarrow \mathbf{x}_{\mathcal{P}} \cdot \Delta_S^{(Q)}$ . Secret-share  $\mathbf{r}$  as  $[\mathbf{r}]^{(Q)}$ .
  - Define  $\langle \mathbf{p} \rangle_{\mathcal{P}} \leftarrow (\mathbf{x}_{\mathcal{P}}, [\mathbf{r}]_{\mathcal{P}})$  and  $\langle \mathbf{p} \rangle_{\mathcal{S}} \leftarrow (\mathbf{x}_{\mathcal{S}}, [\mathbf{r}]_{\mathcal{S}})$ .
  - If  $X$  is corrupted, then send  $\langle \mathbf{p} \rangle_X$  to  $\mathcal{S}$ . Wait for the adversary's permission to proceed; also get updated  $\langle \mathbf{p} \rangle_X$ .
  - Send  $(\text{result}, \text{sid}, \langle \mathbf{p} \rangle_{\mathcal{S}})$  to  $\mathcal{S}$  and  $(\text{result}, \text{sid}, \langle \mathbf{p} \rangle_{\mathcal{P}})$  to  $\mathcal{P}$ .

**Functionality 8:**  $\mathcal{F}_{\text{asymm\_rp}}$ : Ideal functionality for asymmetric reshare to parts

#### 4.12 Asymmetric reshare to parts

Alg. 10 gives the algorithm for splitting Phone's share of a value into digits and adding MACs to these digits. Alg. 10 is informally expected to have AP-security, i.e. it offers security against a malicious Phone, but only privacy against a malicious Server. We can define the protocol  $\Pi_{\text{asymm\_rp}}$ , an instance of Prot. 3, on the basis of Alg. 10. The protocol is realized by a system consisting of the machines  $\mathcal{M}_{\mathcal{P}}$  and  $\mathcal{M}_{\mathcal{S}}$ , as well as the ideal component  $\mathcal{F}_{\text{asymm\_rp}}^{\text{off}}$  for correlated randomness, which is an instance of Func. 6. Note that  $\mathcal{F}_{\text{asymm\_rp}}^{\text{off}}$  is an AP-secure functionality.

The ideal functionality  $\mathcal{F}_{\text{asymm\_rp}}$  securely realized by  $\Pi_{\text{asymm\_rp}}$  is given in Func. 8. We see that its output are the digits  $\mathbf{x}_{\mathcal{P}}$  and  $\mathbf{x}_{\mathcal{S}}$  of the shares of a random resharing of its input  $v$ ; there are also Server's MACs on the digits held by the Phone. A notable omission in Func. 8 is the corrupted Server's ability to define the algorithm  $\mathfrak{A}$  running on the input. Indeed, as the server is actually passive in Alg. 10, it does not get a chance to change the output so precisely; it can only change its own output share.

In order to show that  $\Pi_{\text{asymm\_rp}}$  is at least as secure as  $\mathcal{F}_{\text{asymm\_rp}}$ , let us present the simulators.

**Simulator for corrupted phone.** During the initialization, the simulator does not get any information, because there is no  $\Delta_{\mathcal{P}}$ . At the beginning of the computation, the simulator receives  $\langle v \rangle_{\mathcal{P}} = ([v]_{\mathcal{P}}, [V]_{\mathcal{P}})$  from  $\mathcal{F}_{\text{asymm\_rp}}$  and forwards it to the adversary (as if  $\mathcal{M}_{\mathcal{P}}$  reporting to  $\mathcal{A}$  about the start of the computation). The simulator asks the ideal functionality to continue and learns the output  $\langle \mathbf{p} \rangle_{\mathcal{P}} = (\mathbf{x}_{\mathcal{P}}, [\mathbf{r}]_{\mathcal{P}})$ .

The simulator computes  $m \leftarrow \text{split}^{-1}(\mathbf{x}_P)$ . It generates a random  $\llbracket M \rrbracket_P^{(q)}$ . It sends  $m$ ,  $\llbracket M \rrbracket_P$  and  $\llbracket \mathbf{r} \rrbracket_P$  to  $\mathcal{A}$ , as if coming from  $\mathcal{F}_{\text{asymm\_rp}}^{\text{off}}$ . The simulator waits for the adversary to tell  $\mathcal{M}_P$  that it should send the message  $(w, M_w)$  to  $\mathcal{M}_S$ . The simulator verifies that  $w = \llbracket v \rrbracket_P - m$  and  $M_w = \llbracket V \rrbracket_P - \llbracket M \rrbracket_P$ . If not, then the simulator tells  $\mathcal{F}_{\text{asymm\_rp}}$  to stop. Otherwise, the simulator receives from  $\mathcal{A}$  the values that  $\mathcal{M}_P$  should output to  $P$ ; the simulator tells  $\mathcal{F}_{\text{asymm\_rp}}$  to update  $P$ 's outputs with these values and to proceed.

**Simulator for corrupted server.** During the initialization, the simulator learns  $\Delta_S$  and forwards this to  $\mathcal{A}$ . At the beginning of the computation, the simulator receives  $\langle v \rangle_S = (\llbracket v \rrbracket_S, \llbracket V \rrbracket_S)$  from  $\mathcal{F}_{\text{asymm\_rp}}$  and forwards it to the adversary. The simulator asks the ideal functionality to continue and gets the output  $\langle \mathbf{p} \rangle_S = (\mathbf{x}_S, \llbracket \mathbf{r} \rrbracket_S)$ . The simulator computes  $v_S = \text{split}^{-1}(\mathbf{x}_S)$  and  $w \leftarrow v_S - \llbracket v \rrbracket_S$ . The simulator generates a random  $\llbracket M \rrbracket_S \in \mathbb{Z}_q$  and sends it and  $\llbracket \mathbf{r} \rrbracket_S$  to  $\mathcal{A}$ , as if coming from  $\mathcal{F}_{\text{asymm\_rp}}^{\text{off}}$ . The simulator tells  $\mathcal{A}$  that  $\mathcal{M}_S$  received the message  $w, M_w$  from  $\mathcal{M}_P$ , where  $M_w = w \cdot \Delta_S - (\llbracket V \rrbracket_S - \llbracket M \rrbracket_S)$ . The simulator then expects  $\mathcal{A}$  to tell it, what  $\mathcal{M}_S$  should output to the environment; the simulator tells  $\mathcal{F}_{\text{asymm\_rp}}$  to update  $S$ 's outputs with these values and to proceed.

#### 4.13 Characteristic vector

Alg. 11 gives the algorithm for computing the characteristic vector of a value shared modulo  $Q$ , where the resulting bits are shared modulo  $N$ . We can define an AP-secure protocol  $\Pi_{\text{ch\_vec}}$  as an instance of Prot. 3, implementing Alg. 11. The protocol  $\Pi_{\text{ch\_vec}}$  is realized by a system consisting of machines  $\mathcal{M}_P$  and  $\mathcal{M}_S$ , as well as the ideal components  $\mathcal{F}_{\text{declassify}}^{\text{AP}}$  and  $\mathcal{F}_{\text{ch\_vec}}^{\text{off}}$ , where the latter is an instance of Func. 6. Let us give the simulators showing that  $\Pi_{\text{ch\_vec}}$  is a secure implementation of  $\mathcal{F}_{\text{ch\_vec}}$ , where the latter is an instance of Func. 5, where the function  $f$  computes the characteristic vector of its input, the inputs to the functionality are secret-shared modulo  $Q$ , and the outputs are shared modulo  $N$ .

**Simulator for corrupted phone.** During the initialization, the simulator notifies  $\mathcal{A}$  of the initialization. At the beginning of the computation, the simulator receives  $\langle v \rangle_P^{(Q)}$  from  $\mathcal{F}_{\text{ch\_vec}}$  and forwards it to  $\mathcal{A}$ . The simulator asks the ideal functionality to continue and receives the output  $\langle \mathbf{b} \rangle_P^{(N)}$ .

The simulator generates a random  $\langle r \rangle_P^{(Q)}$ . It also generates a random  $f \in \mathbb{Z}_Q$  and defines  $\langle d_i \rangle_P \leftarrow \langle b_{(i+f) \bmod Q} \rangle_P$  for all  $i \in \mathbb{Z}_Q$ . The simulator sends  $\langle r \rangle_P^{(Q)}$  and  $\langle \mathbf{d} \rangle_P$  to  $\mathcal{A}$ , as if coming from  $\mathcal{F}_{\text{ch\_vec}}^{\text{off}}$ . The adversary may respond with updates to  $\langle r \rangle_P^{(Q)}$  and  $\langle \mathbf{d} \rangle_P$ ; if there are updates to  $\langle r \rangle_P^{(Q)}$  then the simulator tells  $\mathcal{F}_{\text{ch\_vec}}$  to stop.

The simulator computes  $\langle f \rangle_P \leftarrow \langle v \rangle_P - \langle r \rangle_P$  and  $\llbracket f \rrbracket_S \leftarrow f - \llbracket f \rrbracket_P$ , and it expects  $\mathcal{A}$  to command  $\mathcal{M}_P$  to tell  $\mathcal{F}_{\text{declassify}}^{\text{AP}}$  to declassify  $\langle f' \rangle_P$ . The simulator

sends  $(\text{input}, \text{sid}, \langle f' \rangle_{\mathcal{P}}, \llbracket f \rrbracket_{\mathcal{S}})$  to  $\mathcal{A}$  (as if coming from  $\mathcal{F}_{\text{declassify}}^{\text{AP}}$ ) and expects back an update to  $\langle f' \rangle_{\mathcal{P}}$ . If the updated share is different from  $\langle f \rangle_{\mathcal{P}}$ , then the simulator tells  $\mathcal{F}_{\text{ch\_vec}}$  to stop. Otherwise, the simulator tells  $\mathcal{A}$  that Server's MAC check passed; in response,  $\mathcal{A}$  may update the declassified value  $f$  to  $\hat{f}$ . The simulator tells  $\mathcal{F}_{\text{ch\_vec}}$  to proceed, with the final result to  $\mathcal{P}$  being updated to the rotation of (updated)  $\langle \mathbf{d} \rangle_{\mathcal{P}}$  by  $\hat{f}$  positions.

**Simulator for corrupted server.** During the initialization, the simulator receives  $\Delta_{\mathcal{S}}$  from  $\mathcal{F}_{\text{ch\_vec}}$  and forwards it to  $\mathcal{A}$ . At the start of the execution with session identifier  $\text{sid}$ , the simulator receives  $\langle v \rangle_{\mathcal{S}}$  from the ideal functionality and forwards it to  $\mathcal{A}$ . The simulator also receives  $\langle \mathbf{b} \rangle_{\mathcal{S}}$  from the ideal functionality.

The simulator picks a random  $f \in \mathbb{Z}_Q$ . It generates a random  $r \in \mathbb{Z}_Q$  and secret-shares it as  $\langle r \rangle$  (uses  $\Delta_{\mathcal{S}}$ ) to compute the MAC. Let  $\mathbf{d}$  be the characteristic vector of  $r$ . The simulator secret-shares  $\mathbf{d}$  as  $\langle \mathbf{d} \rangle$  with the additional constraint that  $\langle d_i \rangle_{\mathcal{S}} = \langle b_{(i+f) \bmod Q} \rangle_{\mathcal{S}}$  for each  $i \in \mathbb{Z}_Q$ . The simulator sends  $\langle r \rangle_{\mathcal{S}}$  and  $\langle \mathbf{d} \rangle_{\mathcal{S}}$  to the adversary, as if coming from  $\mathcal{F}_{\text{ch\_vec}}^{\text{off}}$ .

The simulator waits for  $\mathcal{A}$  to instruct  $\mathcal{M}_{\mathcal{S}}$  to declassify  $\langle g \rangle_{\mathcal{S}}$ . The simulator sends  $(\text{input}, \text{sid}, \langle g \rangle_{\mathcal{S}})$  to  $\mathcal{A}$  (as if coming from  $\mathcal{F}_{\text{declassify}}^{\text{AP}}$ ) and receives back an update for it. The simulator knows that this represents the adversarially-introduced error  $\varepsilon = \llbracket v \rrbracket_{\mathcal{S}} - \llbracket r \rrbracket_{\mathcal{S}} - \llbracket g \rrbracket_{\mathcal{S}}$  in the value of  $v$ . The simulator defines  $g \leftarrow f + \varepsilon$  and secret-shares  $g$  into  $\langle g \rangle$ , with the constraint that  $\langle g \rangle_{\mathcal{S}}$  has already been given by the adversary. This “secret-sharing under a constraint” actually means that the simulator computes

$$\begin{aligned} & - \llbracket g \rrbracket_{\mathcal{P}} \leftarrow g - \llbracket g \rrbracket_{\mathcal{S}}, \text{ and} \\ & - \text{ms} \langle g \rangle_{\mathcal{P}} \leftarrow \llbracket g \rrbracket_{\mathcal{P}} \cdot \Delta_{\mathcal{S}} - \text{ms} \langle g \rangle_{\mathcal{S}}. \end{aligned}$$

Here all computations are modulo  $Q$ . Previously,  $\langle \mathbf{d} \rangle^{(N)}$  had been computed in the same manner.

The simulator sends  $(\text{result?}, \text{sid}, g, \text{true})$  to  $\mathcal{A}$ , as if coming from  $\mathcal{F}_{\text{declassify}}^{\text{AP}}$ . The adversary responds with  $(\text{result}, \text{sid}, g')$  to  $\mathcal{F}_{\text{declassify}}^{\text{AP}}$ , which the simulator can ignore. The simulator now creates the following description of an algorithm  $\mathfrak{A}$  that would take  $\langle v \rangle_{\mathcal{P}}$  as an argument:

- Recover  $v$  from  $\llbracket v \rrbracket_{\mathcal{P}}$  and  $\llbracket v \rrbracket_{\mathcal{S}}$ .
- Return the characteristic vector of  $v + \varepsilon$ .

The simulator sends the description of  $\mathfrak{A}$  to the ideal functionality. The simulator expects updated  $\langle \mathbf{b} \rangle_{\mathcal{S}}$  from  $\mathcal{A}$ , which it forwards to the ideal functionality, to be outputted to  $\mathcal{S}$ .

#### 4.14 Short overflow

Alg. 12 gives the algorithm for computing the bits that indicate whether the value  $v$ , secret-shared over a small modulus  $Q$ , is equal to a constant  $r - 1$ , or is greater than or equal to  $r$ . The returned bits may be shared over a different

modulus  $N$ . We can define an AP-secure protocol  $\Pi_{\text{short\_of}}$  as an instance of Prot. 3, implementing Alg. 12. This protocol is realized by a system consisting of the machines  $\mathcal{M}_P$  and  $\mathcal{M}_S$ , as well as the ideal component  $\mathcal{F}_{\text{ch\_vec}}$ . The protocol securely implements the ideal functionality  $\mathcal{F}_{\text{short\_of}}$ , which is an instance of Func. 5, where the inputs are shared modulo  $Q$ , the outputs are shared modulo  $N$ , and the function  $f$  computes the previously mentioned two bits. Let us present the simulators showing that  $\Pi_{\text{short\_of}}$  indeed is at least as secure as  $\mathcal{F}_{\text{short\_of}}$ .

**Simulator for corrupted phone.** During the initialization, the simulator notifies  $\mathcal{A}$  of the initialization. At the beginning of the computation, the simulator receives  $\langle v \rangle_P^{(Q)}$  from  $\mathcal{F}_{\text{short\_of}}$  and forwards it to  $\mathcal{A}$ . The simulator asks  $\mathcal{F}_{\text{short\_of}}$  to continue and receives the output shares  $\langle b \rangle_P^{(N)}$  and  $\langle c \rangle_P^{(N)}$ .

The simulator waits for  $\mathcal{A}$  to instruct  $\mathcal{M}_P$  to invoke the computation of  $\mathcal{F}_{\text{ch\_vec}}$  with the argument  $\langle v \rangle_P$ . If the argument received from  $\mathcal{A}$  is the different from what was received from  $\mathcal{F}_{\text{short\_of}}$  earlier, then the simulator tells  $\mathcal{F}_{\text{short\_of}}$  to stop. The simulator generates a random vector  $\langle \mathbf{d} \rangle_P^{(N)}$  of length  $Q$ , with constraints  $\langle d_{r-1} \rangle_P = \langle b \rangle_P$  and  $\sum_{i=r}^{Q-1} \langle d_i \rangle_P = \langle c \rangle_P$ . The simulator sends  $\langle \mathbf{d} \rangle_P$  to  $\mathcal{A}$  as output from  $\mathcal{F}_{\text{ch\_vec}}$ . The adversary may send adjustments to  $\langle \mathbf{d} \rangle_P$ ; the simulator will update  $\langle b \rangle_P$  and  $\langle c \rangle_P$  accordingly. The simulator tells  $\mathcal{F}_{\text{short\_of}}$  to proceed, with adjusted  $\langle b \rangle_P$  and  $\langle c \rangle_P$ .

**Simulator for corrupted server.** During the initialization, the simulator receives  $\Delta_S$  from the ideal functionality and forwards it to  $\mathcal{A}$ . At the beginning of the computation, the simulator receives  $\langle v \rangle_S$  from the ideal functionality and forwards it to  $\mathcal{A}$ . Also receives the shares  $\langle b \rangle_S^{(N)}$  and  $\langle c \rangle_S^{(N)}$  of the output.

The simulator waits for  $\mathcal{A}$  to instruct  $\mathcal{M}_S$  to invoke the computation of  $\mathcal{F}_{\text{ch\_vec}}$  with the argument  $\langle v \rangle_S$ ; the simulator responds (as  $\mathcal{F}_{\text{ch\_vec}}$ ) that computation with  $\langle v \rangle_S$  has indeed started. As  $\mathcal{F}_{\text{ch\_vec}}$  is an instance of Func. 5, the adversary now expects to receive its share of the output from this functionality. The simulator thus generates a random  $\langle \mathbf{d} \rangle_S^{(N)}$  satisfying the constraints  $\langle d_{r-1} \rangle_S = \langle b \rangle_S$  and  $\sum_{i=r}^{Q-1} \langle d_i \rangle_S = \langle c \rangle_S$ , and sends it to the adversary. The adversary now sends the description of the algorithm  $\mathfrak{A}$  that it expects to be executed by  $\mathcal{F}_{\text{ch\_vec}}$ . The algorithm  $\mathfrak{A}$  expects to get  $\langle v \rangle_P$  as the input, and it will return a vector of booleans, which may be the characteristic vector of  $v$ .

The simulator will now create the description of the following algorithm  $\mathfrak{B}$  that takes  $\langle v \rangle_P$  as the input:

1. compute  $\mathbf{d} \leftarrow \mathfrak{A}(\langle v \rangle_P)$ , where  $\mathbf{d} \in \mathbb{Z}_N^Q$ ;
2. let  $b \leftarrow d_{r-1}$  and  $c \leftarrow (\sum_{i=r}^{Q-1} d_i) \bmod N$ ;
3. return  $b$  and  $c$ .

The simulator sends the description of  $\mathfrak{B}$  to  $\mathcal{F}_{\text{short\_of}}$ . It will get updates from  $\mathcal{A}$  to  $\langle b \rangle_S^{(N)}$  and  $\langle c \rangle_S^{(N)}$  and forward these to  $\mathcal{F}_{\text{short\_of}}$ , telling it to proceed with outputs to  $P$  and  $S$ .

#### 4.15 Long overflow

Alg. 13 gives the algorithm for computing the bit indicating whether the length- $d$  vector of values  $\mathbf{p}$ , secret-shared over a small modulus  $Q$ , is larger than  $r^d$ , when we interpret the elements of  $\mathbf{p}$  as digits in basis  $r$  (and the digits themselves are allowed to be larger than  $r - 1$ ). The returned bit may be shared over a different modulus  $M$ , and there is a “middle” modulus  $N$  for the bits indicating the overflow of each single digit. We can define an AP-secure protocol  $\Pi_{\text{long\_of}}$  as an instance of Prot. 3, implementing Alg. 13. This protocol is realized by a system consisting of the machines  $\mathcal{M}_{\mathcal{P}}$  and  $\mathcal{M}_{\mathcal{S}}$ , as well as ideal components  $\mathcal{F}_{\text{short\_of}}$  (with input modulus  $Q$  and output modulus  $N$ ) and  $\mathcal{F}_{\text{ch\_vec}}$  (with input modulus  $N$  and output modulus  $M$ ). The protocol securely implements the ideal functionality  $\mathcal{F}_{\text{long\_of}}$ , which is an instance of Func. 5, where the inputs are shared modulo  $Q$ , the outputs are shared modulo  $M$ , and the function  $f$ , applied to  $(p_0, \dots, p_{d-1})$ , outputs whether  $\sum_{i=0}^{d-1} r^i p_i$  is greater or equal to  $r^d$ . Let us present the simulators showing that  $\Pi_{\text{long\_of}}$  indeed is at least as secure as  $\mathcal{F}_{\text{long\_of}}$ .

**Simulator for corrupted phone.** During the initialization, the simulator notifies  $\mathcal{A}$  of the initialization. At the beginning of the computation, the simulator receives  $\langle \mathbf{p} \rangle_{\mathcal{P}}^{(Q)}$  from  $\mathcal{F}_{\text{long\_of}}$  and forwards it to  $\mathcal{A}$ . The simulator asks  $\mathcal{F}_{\text{long\_of}}$  to continue and receives the output share  $\langle z \rangle_{\mathcal{P}}^{(M)}$ . The simulator generates random  $\langle b_i \rangle_{\mathcal{P}}^{(N)}$  and  $\langle c_i \rangle_{\mathcal{P}}^{(N)}$  for  $i \in [d]$ .

The simulator waits for  $\mathcal{A}$  to command  $\mathcal{M}_{\mathcal{P}}$  to start the computation of  $\mathcal{F}_{\text{short\_of}}$  with each  $\langle p_i \rangle_{\mathcal{P}}$ . The simulator simulates  $\mathcal{F}_{\text{short\_of}}$  running and responds that the shares of the output are  $\langle b_i \rangle_{\mathcal{P}}^{(N)}$  and  $\langle c_i \rangle_{\mathcal{P}}^{(N)}$ . The adversary may ask  $\mathcal{F}_{\text{short\_of}}$  to change either its inputs, but any such ask will result in the simulator asking  $\mathcal{F}_{\text{long\_of}}$  to stop. Indeed, the ideal component  $\mathcal{F}_{\text{short\_of}}$  of the real system would catch such changes, because it checks the MACs. The adversary may also ask  $\mathcal{F}_{\text{short\_of}}$  to change its outputs; the simulator will ignore these requests.

The simulator computes  $\langle m \rangle_{\mathcal{P}} \leftarrow \sum_{i=1}^{d-1} 2^{i-1} \cdot \langle b_i \rangle_{\mathcal{P}} + \sum_{i=0}^{d-1} 2^i \cdot \langle c_i \rangle_{\mathcal{P}}$ . It waits for  $\mathcal{A}$  to command  $\mathcal{M}_{\mathcal{P}}$  to start the computation of  $\mathcal{F}_{\text{ch\_vec}}$  with the argument that should be equal to  $\langle m \rangle_{\mathcal{P}}$ . If it is not (after  $\mathcal{A}$  has had the chance to ask  $\mathcal{F}_{\text{ch\_vec}}$  to change its inputs), then the simulator tells  $\mathcal{F}_{\text{long\_of}}$  to stop, because  $\mathcal{F}_{\text{ch\_vec}}$  would have caught a wrong MAC.

The simulator generates a random  $\langle \mathbf{k} \rangle_{\mathcal{P}}^{(M)}$  of length  $N$ , with the constraint  $\sum_{i=2^d-1}^{N-1} \langle k_i \rangle_{\mathcal{P}} = \langle z \rangle_{\mathcal{P}}$ . It tells  $\mathcal{A}$  that the output of  $\mathcal{F}_{\text{ch\_vec}}$  is  $\langle \mathbf{k} \rangle_{\mathcal{P}}$ . The adversary can submit its changes to  $\langle \mathbf{k} \rangle_{\mathcal{P}}$ . The simulator recomputes  $\langle z \rangle_{\mathcal{P}} := \sum_{i=2^d-1}^{N-1} \langle k_i \rangle_{\mathcal{P}}$  and tells the adversary that this is  $\mathcal{P}$ 's output from the protocol. The adversary may change this  $\langle z \rangle_{\mathcal{P}}$  as well, the simulator sends the changed value back to  $\mathcal{F}_{\text{long\_of}}$  and tells it to proceed with outputs.

**Simulator for corrupted server.** During the initialization, the simulator receives  $\Delta_{\mathcal{S}}$  from the ideal functionality and forwards it to  $\mathcal{A}$ . At the beginning of the computation, the simulator receives  $\langle \mathbf{p} \rangle_{\mathcal{S}}$  from the ideal functionality and

forwards it to  $\mathcal{A}$ . Also receives the share  $\langle z \rangle_S^{(M)}$  of the output, because  $\mathcal{F}_{\text{long\_of}}$  is an instance of Func. 5.

The simulator waits for  $\mathcal{A}$  to command  $\mathcal{M}_S$  to start the computation of  $\mathcal{F}_{\text{short\_of}}$  with each  $\langle p_i \rangle_S$ . The adversary expects to get back the shares of the output; the simulator makes up random  $\langle b_i \rangle_S$  and  $\langle c_i \rangle_S$  and sends them to  $\mathcal{A}$ . The adversary then sends the descriptions of algorithms  $\mathfrak{A}_0, \dots, \mathfrak{A}_{d-1}$  to the simulator (these interactions for all the  $d$  invocations may be arbitrarily interleaved).

The simulator waits for  $\mathcal{A}$  to command  $\mathcal{M}_S$  to start the computation of  $\mathcal{F}_{\text{ch\_vec}}$  with  $\langle m \rangle_S^{(N)}$ . If the adversary does not try to affect the computations then  $\langle m \rangle_S = \sum_{i=1}^{d-1} 2^{i-1} \cdot \langle b_i \rangle_S + \sum_{i=0}^{d-1} 2^i \cdot \langle c_i \rangle_S$ , but if the adversary is performing an active attack, then it may be something else. The simulator now creates a random  $\langle \mathbf{k} \rangle_S^{(M)}$  of length  $N$ , with the constraint  $\sum_{i=2^{d-1}}^{N-1} \langle k_i \rangle_S = \langle z \rangle_S$ . The simulator sends  $\langle \mathbf{k} \rangle_S^{(M)}$  to the adversary, as if it being the output share of  $\mathcal{F}_{\text{ch\_vec}}$ . The adversary sends the description of an algorithm  $\mathfrak{B}$  to the simulator.

The simulator now constructs the following algorithm  $\mathfrak{C}$  that takes  $\langle \mathbf{p} \rangle_P$  as an argument:

1. For each  $i \in [d]$ , let  $(b_i, c_i) \leftarrow \mathfrak{A}_i(\langle p_i \rangle_P)$ . The values  $b_i$  and  $c_i$  are elements of  $\mathbb{Z}_N$ .
2. For each  $i \in [d]$ , compute  $\langle b_i \rangle_P$  from  $b_i$  and  $\langle b_i \rangle_S$ , using  $\Delta_S$  to compute the MAC.
3. Similarly, compute  $\langle c_i \rangle_P$  for each  $i \in [d]$ .
4. Let  $\langle m \rangle_P \leftarrow \sum_{i=1}^{d-1} 2^{i-1} \cdot \langle b_i \rangle_P + \sum_{i=0}^{d-1} 2^i \cdot \langle c_i \rangle_P$ .
5. Let  $\mathbf{k} \leftarrow \mathfrak{B}(\langle m \rangle_P)$ . The elements of the vector  $\mathbf{k}$  belong to  $\mathbb{Z}_M$ .
6. Return  $\sum_{i=2^{d-1}}^{N-1} k_i$ .

The simulator sends the description of  $\mathfrak{C}$  to  $\mathcal{F}_{\text{long\_of}}$ . It may get an update from  $\mathcal{A}$  to  $\langle z \rangle_S^{(M)}$ , and will forward this to  $\mathcal{F}_{\text{long\_of}}$ , telling it to proceed with outputs to  $\mathcal{P}$  and  $\mathcal{S}$ .

#### 4.16 Inequality check

Alg. 14 gives the algorithm to compute the bit indicating whether a secret-shared value is less than a constant. The input is shared over the modulus  $q$ , while the output may be shared over some different modulus  $M$ , and there are also moduli  $Q$  and  $N$  (all prime), radix  $r$  and length  $d$  parametrizing the algorithm. We can define an AP-secure protocol  $\Pi_{\text{ineq}}$  as an instance of Prot. 3, implementing Alg. 14. This protocol is realized by a system consisting of the machines  $\mathcal{M}_P$  and  $\mathcal{M}_S$ , as well as ideal components  $\mathcal{F}_{\text{asymm\_rp}}$  (with output modulus  $Q$ ) and  $\mathcal{F}_{\text{long\_of}}$  (with input modulus  $Q$ , “middle” modulus  $N$ , and output modulus  $M$ ). The protocol securely implements the ideal functionality  $\mathcal{F}_{\text{ineq}}$ , which is an instance of Func. 5, where the inputs are shared modulo  $q$ , the output bit is shared modulo  $M$ , and the function  $f$ , applied to private  $v$  and public  $C$ , outputs whether  $v < C$ . Let us present the simulators showing that  $\Pi_{\text{ineq}}$  indeed is at least as secure as  $\mathcal{F}_{\text{ineq}}$ .



**Simulator for corrupted phone.** During the initialization, the simulator notifies  $\mathcal{A}$  of the initialization. At the beginning of the computation, the simulator receives  $\langle v \rangle_{\mathcal{P}}$  and  $C$  from the ideal functionality and forwards them to  $\mathcal{A}$ . The simulator asks  $\mathcal{F}_{\text{ineq}}$  to continue, and gets the output share  $\langle z \rangle_{\mathcal{P}}^{(M)}$ . The simulator waits for  $\mathcal{A}$  to instruct  $\mathcal{M}_{\mathcal{P}}$  to submit  $\langle v \rangle_{\mathcal{P}}$  to  $\mathcal{F}_{\text{asymm\_rp}}$ ; the adversary may further adjust it by sending message directly to  $\mathcal{F}_{\text{asymm\_rp}}$ . If the value finally chosen by  $\mathcal{A}$  is different from  $\langle v \rangle_{\mathcal{P}}$ , then the simulator instructs  $\mathcal{F}_{\text{ineq}}$  to stop. Otherwise the simulator generates a random vector  $\langle \mathbf{p} \rangle_{\mathcal{P}}^{(Q)}$  and sends it to the adversary, as if coming from  $\mathcal{F}_{\text{asymm\_rp}}$ .

The simulator waits for  $\mathcal{A}$ 's commands to  $\mathcal{M}_{\mathcal{P}}$  to submit  $\langle \mathbf{p} \rangle_{\mathcal{P}}$  to  $\mathcal{F}_{\text{long\_of}}$  twice. The adversary is able to change the input to the two computations made by  $\mathcal{F}_{\text{long\_of}}$ , but any change will lead to the simulator telling  $\mathcal{F}_{\text{ineq}}$  to stop. The simulator generates random  $\langle x \rangle_{\mathcal{P}}^{(M)}$  and  $\langle y \rangle_{\mathcal{P}}^{(M)}$  satisfying the constraint  $\langle z \rangle_{\mathcal{P}} = \langle 1 \rangle_{\mathcal{P}} - \langle y \rangle_{\mathcal{P}} + \langle x \rangle_{\mathcal{P}} - \langle 0 \rangle_{\mathcal{P}}$ . The simulator sends  $\langle x \rangle_{\mathcal{P}}$  and  $\langle y \rangle_{\mathcal{P}}$  to  $\mathcal{A}$ , as if coming from the two computations done by  $\mathcal{F}_{\text{long\_of}}$ . The adversary can submit its changes to  $\langle x \rangle_{\mathcal{P}}$  and  $\langle y \rangle_{\mathcal{P}}$ ; the simulator will update  $\langle z \rangle_{\mathcal{P}}$  accordingly. The simulator notifies  $\mathcal{A}$  that the output for  $\mathcal{P}$  is  $\langle z \rangle_{\mathcal{P}}$ , the adversary may change it, the simulator tells  $\mathcal{F}_{\text{ineq}}$  to proceed with changed  $\langle z \rangle_{\mathcal{P}}$ .

**Simulator for corrupted server.** During the initialization, the simulator receives  $\Delta_{\mathcal{S}}$  from the ideal functionality and forwards it to  $\mathcal{A}$ . At the beginning of the computation, the simulator receives  $\langle v \rangle_{\mathcal{S}}$  from the ideal functionality and forwards it to  $\mathcal{A}$ . Also receives the share  $\langle z \rangle_{\mathcal{S}}^{(M)}$  of the output, because  $\mathcal{F}_{\text{ineq}}$  is an instance of Func. 5.

Wait for  $\mathcal{A}$  to instruct  $\mathcal{S}$  to submit  $\langle v \rangle_{\mathcal{S}}$  to  $\mathcal{F}_{\text{asymm\_rp}}$ . Inform (as  $\mathcal{F}_{\text{asymm\_rp}}$ )  $\mathcal{A}$  that  $\langle v \rangle_{\mathcal{S}}$  was submitted, allow  $\mathcal{A}$  to update it. Let  $\varepsilon \in \mathbb{Z}_q$  be the difference between the updated  $\llbracket v \rrbracket_{\mathcal{S}}$  and the original  $\llbracket v \rrbracket_{\mathcal{S}}$  received from  $\mathcal{F}_{\text{ineq}}$ . Generate a random  $\langle \mathbf{p} \rangle_{\mathcal{S}}^{(Q)}$  and send it to  $\mathcal{A}$  as if coming from  $\mathcal{F}_{\text{asymm\_rp}}$ . Let  $w_{\mathcal{S}} \leftarrow \text{split}^{-1}(\llbracket \mathbf{p} \rrbracket_{\mathcal{S}})$ . Let  $\langle c \rangle_{\mathcal{S}}^{(M)} \leftarrow \langle 0 \rangle_{\mathcal{S}}$ ; update  $\llbracket c \rrbracket_{\mathcal{S}}$  to 1 if  $w_{\mathcal{S}} \geq C$ .

Wait for  $\mathcal{A}$  to instruct  $\mathcal{S}$  to submit  $\langle \mathbf{a} \rangle_{\mathcal{S}}^{(Q)}$  and  $\langle \mathbf{b} \rangle_{\mathcal{S}}^{(Q)}$  to  $\mathcal{F}_{\text{long\_of}}$  for computation. The adversary expects to get the output shares  $\langle x \rangle_{\mathcal{S}}^{(M)}$  and  $\langle y \rangle_{\mathcal{S}}^{(M)}$ . The simulator randomly generates them, such that the constraint  $\langle z \rangle_{\mathcal{S}} = \langle 1 \rangle_{\mathcal{S}} + \langle x \rangle_{\mathcal{S}} - \langle y \rangle_{\mathcal{S}} - \langle c \rangle_{\mathcal{S}}$  is satisfied, and sends them to  $\mathcal{A}$ . The adversary sends the descriptions of algorithms  $\mathfrak{A}_1$  and  $\mathfrak{A}_2$  meant for the two computations of  $\mathcal{F}_{\text{long\_of}}$ . These algorithms expect  $\langle \mathbf{p} \rangle_{\mathcal{P}}$  as an input. The simulator creates the description of the following algorithm  $\mathcal{B}$ , expecting  $\langle v \rangle_{\mathcal{P}}$  as an input:

1. Recover  $v$  from  $\llbracket v \rrbracket_{\mathcal{P}}$  and  $\llbracket v \rrbracket_{\mathcal{S}}$ .
2. Let  $\llbracket \mathbf{p} \rrbracket_{\mathcal{P}} \leftarrow \text{split}(v + \varepsilon) - \llbracket \mathbf{p} \rrbracket_{\mathcal{S}}$ . I.e.  $\llbracket \mathbf{p} \rrbracket_{\mathcal{P}}$  is computed so that the condition for the output of Alg. 10 holds, if we put  $(v + \varepsilon) \bmod q$  in the position of  $v$  there.
3. Extend  $\llbracket \mathbf{p} \rrbracket_{\mathcal{P}}$  to  $\langle \mathbf{p} \rangle_{\mathcal{P}}$  by computing  $\mathcal{P}$ 's MAC share from  $\llbracket \mathbf{p} \rrbracket_{\mathcal{P}}$ ,  $\Delta_{\mathcal{S}}$  and  $\mathcal{S}$ 's MAC share.
4. Let  $x \leftarrow \mathfrak{A}_1(\langle \mathbf{p} \rangle_{\mathcal{P}})$  and  $y \leftarrow \mathfrak{A}_2(\langle \mathbf{p} \rangle_{\mathcal{P}})$ . Here  $x, y \in \mathbb{Z}_M$ .

5. Output  $(1 + x - y - c) \bmod M$ .

The simulator sends the description of  $\mathfrak{B}$  to  $\mathcal{F}_{\text{ineq}}$ . It may get an update from  $\mathcal{A}$  to  $\langle z \rangle_{\mathcal{S}}^{(M)}$ , and will forward this to  $\mathcal{F}_{\text{ineq}}$ , telling it to proceed with outputs to  $\mathcal{P}$  and  $\mathcal{S}$ .

#### 4.17 Rejection check

Alg. 15 gives the algorithm for the “second half” of a signing attempt: the rejection check. It takes a number of secret-shared values and returns 1 if all these values are inside bounds. We can define an AP-secure protocol  $\Pi_{\text{rej\_check}}$  as an instance of Prot. 3, implementing Alg. 15. The protocol  $\Pi_{\text{rej\_check}}$  is implemented by a system consisting of machines  $\mathcal{M}_{\mathcal{P}}$  and  $\mathcal{M}_{\mathcal{S}}$ , and the ideal components  $\mathcal{F}_{\text{ineq}}$  and  $\mathcal{F}_{\text{ch\_vec}}$  (with parameters named in Alg. 15). The protocol  $\Pi_{\text{rej\_check}}$  is at least as secure as the ideal functionality  $\mathcal{F}_{\text{rej\_check}}$ , which is an instance of Func. 5, where the function  $f$  receives  $\mathbf{z}$  and  $\mathbf{x}$  as private inputs, and outputs a bit showing whether they all passed the rejection check. Let us present the simulators that attest to this.

**Simulator for corrupted phone.** During the initialization, the simulator notifies  $\mathcal{A}$  of the initialization. At the beginning of the computation, the simulator receives  $\langle \mathbf{z} \rangle_{\mathcal{P}}$  and  $\langle \mathbf{x} \rangle_{\mathcal{P}}$  from the ideal functionality and forwards them to  $\mathcal{A}$ . The simulator asks  $\mathcal{F}_{\text{rej\_check}}$  to continue and receives the output share  $\langle b_0 \rangle_{\mathcal{P}}^{(2)}$ .

The simulator waits for  $\mathcal{A}$  to ask  $\mathcal{M}_{\mathcal{P}}$  to start the computation of  $\mathcal{F}_{\text{ineq}}$  for each coefficient of each element of  $\langle \mathbf{z} \rangle_{\mathcal{P}}$  and  $\langle \mathbf{x} \rangle_{\mathcal{P}}$  (shifted by appropriate amount). The adversary has the chance to change these coefficients, but any change will result in the simulator asking  $\mathcal{F}_{\text{rej\_check}}$  to stop. The simulator generates a random vector  $\langle \mathbf{v} \rangle_{\mathcal{P}}$  (using the modulus  $M$ ) of the correct length ( $256 \cdot (k + \ell)$ ), and uses the elements of this vector as Phone’s output shares from all the computations of  $\mathcal{F}_{\text{ineq}}$ . The simulator sends the vector  $\langle \mathbf{v} \rangle_{\mathcal{P}}^{(M)}$  to  $\mathcal{A}$ , element by element.

The simulator computes  $\langle s \rangle_{\mathcal{P}} \leftarrow \sum_{i=0}^{256(k+\ell)-1} (\langle 1 \rangle_{\mathcal{P}} - \langle v_i \rangle_{\mathcal{P}})$  and expects  $\mathcal{A}$  to ask  $\mathcal{M}_{\mathcal{P}}$  to start the computation of  $\mathcal{F}_{\text{ch\_vec}}$  with the input  $\langle s \rangle_{\mathcal{P}}$ . If the input is anything else (after the adversary has had its opportunity to change it by communicating with (presumably)  $\mathcal{F}_{\text{ch\_vec}}$ , then the simulator tells  $\mathcal{F}_{\text{rej\_check}}$  to stop. Otherwise, the simulator generates a random vector  $\langle \mathbf{b} \rangle_{\mathcal{P}}^{(2)}$ , where  $\langle b_0 \rangle_{\mathcal{P}}$  has already been defined. The simulator sends  $\langle \mathbf{b} \rangle_{\mathcal{P}}$  to the adversary. The adversary may make changes; the simulator tells  $\mathcal{F}_{\text{rej\_check}}$  to proceed, using the perhaps changed  $\langle b_0 \rangle_{\mathcal{P}}$ .

**Simulator for corrupted server.** During the initialization, the simulator receives  $\Delta_{\mathcal{S}}$  from the ideal functionality and forwards it to  $\mathcal{A}$ . At the beginning of the computation, the simulator receives  $\langle \mathbf{z} \rangle_{\mathcal{S}}$  and  $\langle \mathbf{x} \rangle_{\mathcal{S}}$  from the ideal functionality and forwards them to  $\mathcal{A}$ . The simulator also receives the output share  $\langle b_0 \rangle_{\mathcal{S}}$  from the adversary.

The simulator waits for  $\mathcal{A}$  to ask  $\mathcal{M}_S$  to start the computation of  $\mathcal{F}_{\text{ineq}}$  for each coefficient of each element of  $\langle \mathbf{z} \rangle_S$  and  $\langle \mathbf{x} \rangle_S$  (shifted by appropriate amount). The adversary expects to get the output shares of the results of each comparison. Hence the simulator generates a random  $\langle \mathbf{v} \rangle_S$  (of the correct length) and sends it to  $\mathcal{A}$ , element by element. The adversary responds by sending the algorithm descriptions of the algorithms  $\mathfrak{A}_0, \dots, \mathfrak{A}_{2^{56(k+\ell)}-1}$ , each of them expecting a share  $\langle z_i \rangle_P$  or  $\langle x_i \rangle_P$  as input and producing an element of  $\mathbb{Z}_M$  as output, to the simulator.

The simulator computes  $\langle s \rangle_S \leftarrow \sum_{i=0}^{2^{56(k+\ell)}-1} (\langle 1 \rangle_S - \langle v_i \rangle_S)$  and expects  $\mathcal{A}$  to ask  $\mathcal{M}_S$  to start the computation of  $\mathcal{F}_{\text{ch\_vec}}$  for  $\langle s \rangle_S$ . The adversary expects to get the output share of this computation; the simulator generates a random  $\langle \mathbf{b} \rangle_S^{(2)}$ , where the element  $\langle b_0 \rangle_S$  has already been received from  $\mathcal{F}_{\text{rej\_check}}$ , and sends  $\langle \mathbf{b} \rangle_S$  to  $\mathcal{A}$ . The adversary responds by sending to the simulator the description of the algorithm  $\mathfrak{B}$  that takes a share  $\langle s \rangle_t^{(M)}$  *extsfP* as input and returns a vector of bits.

The simulator creates the description of the following algorithm  $\mathfrak{C}$  that takes  $\langle \mathbf{z} \rangle_P$  and  $\langle \mathbf{x} \rangle_P$  as input and outputs a bit:

1. Apply each of  $\mathfrak{A}_i$  to one of the elements of  $\langle \mathbf{z} \rangle_P$  or  $\langle \mathbf{x} \rangle_P$ , shifted by the required amount (either by  $\gamma_1 - \beta - 1$  or by  $\gamma_2 - \beta - 1$ ). Let  $\mathbf{v} \in \mathbb{Z}_M^{2^{56(k+\ell)}}$  be the vector of outputs from these algorithms  $\mathfrak{A}_i$ .
2. Let  $s \leftarrow \sum_i v_i$ . Compute  $\langle s \rangle_P$  so, that  $(\langle s \rangle_P, \langle s \rangle_S)$  will be a secret-sharing of  $s$ . This computation uses  $\mathfrak{A}_S$ .
3. Let  $\mathbf{b} \leftarrow \mathfrak{B}(\langle s \rangle_P)$ .
4. Return  $b_0$ .

The simulator sends the description of  $\mathfrak{C}$  to  $\mathcal{F}_{\text{rej\_check}}$ . It may get an update from  $\mathcal{A}$  to  $\langle b_0 \rangle_S^{(2)}$ , and will forward this to  $\mathcal{F}_{\text{rej\_check}}$ , telling it to proceed with outputs to  $P$  and  $S$ .

## 5 Security of the ML-DSA protocol

Protocol  $\Pi_{\text{ML-DSA}}$ , given in Prot. 4, corresponds to Alg. 16, but it also contains the rest of the functionality implementing  $\mathcal{F}_{\text{ML-DSA}}$  (Func. 1) — initialization and key generation. The system implementing  $\Pi_{\text{ML-DSA}}$  consists of the two protocol machines  $\mathcal{M}_P$  and  $\mathcal{M}_S$ , and six ideal components that we have described above:  $\mathcal{F}_{\text{keygen}}$ ,  $\mathcal{F}_{\text{gcom}}$ ,  $\mathcal{F}_{\text{rej\_check}}$ ,  $\mathcal{F}_{\text{declassify}}^{\text{AA}}$ ,  $\mathcal{F}_{\text{declassify}}^{\text{AP}}$ , and  $\mathcal{F}_{\text{pubrand}}^{\mathcal{U}}$ , where  $\mathcal{U}$  is the uniform distribution over matrices  $\mathbf{A}$  in the public key of ML-DSA.

To show that  $\Pi_{\text{ML-DSA}}$  is at least as secure as  $\mathcal{F}_{\text{ML-DSA}}$ , we need to construct a simulator. We describe it for the case of corrupted phone, and for the case of corrupted server.

### 5.1 Simulator for corrupted phone

**Start.** In the following, the adversary  $\mathcal{A}$  expects to send messages to  $\mathcal{M}_P$  and the ideal components in  $\Pi_{\text{ML-DSA}}$ , as well as receive messages from them. All these

- On input  $(\text{corrupted}, \text{sid})$  from  $\mathcal{A}$  or any ideal component to  $\mathcal{M}_X$  before the initialization, machine  $\mathcal{M}_X$  sends  $(\text{corrupted}, \text{sid})$  to  $X$  and becomes corrupted.
- On input  $(\text{keygen}, \text{sid})$  from  $X$  to  $\mathcal{M}_X$ , machine  $\mathcal{M}_X$  generates MAC keys  $\Delta_X$ . Sends  $(\text{init}, \text{sid}, \Delta_X)$  to all ideal components that have to be initialized (Messages from  $\mathcal{M}_P$  to  $\mathcal{F}_{\text{rej\_check}}$  and  $\mathcal{F}_{\text{declassify}}^{\text{AP}}$  do not include  $\Delta_P$ ). Machine  $\mathcal{M}_X$  will then
  1. send  $(\text{generate}, \text{sid})$  to  $\mathcal{F}_{\text{pubrand}}^{\text{U}}$  and get back  $(\text{result}, \text{sid}, \mathbf{A})$ ;
  2. send  $(\text{compute}, \text{sid})$  to  $\mathcal{F}_{\text{keygen}}$  and get back  $(\text{result}, \text{sid}, \langle\langle \mathbf{s}_1 \rangle\rangle_X, \langle\langle \mathbf{s}_2 \rangle\rangle_X)$ ;
  3. Compute  $\langle\langle \mathbf{t} \rangle\rangle_X \leftarrow \mathbf{A} \cdot \langle\langle \mathbf{s}_1 \rangle\rangle_X + \langle\langle \mathbf{s}_2 \rangle\rangle_X$ ;
  4. send  $(\text{declassify}, \text{sid}, \langle\langle \mathbf{t} \rangle\rangle_X)$  to  $\mathcal{F}_{\text{declassify}}^{\text{AA}}$  and get back  $(\text{result}, \text{sid}, \mathbf{t})$ .
 It stores the received values and sends  $(\text{pkey}, \text{sid}, \mathbf{A}, \mathbf{t})$  back to  $X$ .
- On input  $(\text{sign}, \text{sid}, \mu)$  from  $P$  to  $\mathcal{M}_P$ , the machine  $\mathcal{M}_P$  works as follows, where  $X \equiv P$ :
  1. Send  $(\text{compute}, \text{sid}, \mathbf{A})$  to  $\mathcal{F}_{\text{gcom}}$  and get back  $(\text{result}, \text{sid}, \langle\langle \mathbf{y} \rangle\rangle_X, \langle\langle \mathbf{w}^H \rangle\rangle_X)$
  2. Compute  $\langle\langle \mathbf{w}^L \rangle\rangle_X \leftarrow \mathbf{A} \cdot \langle\langle \mathbf{y} \rangle\rangle_X - \alpha \cdot \langle\langle \mathbf{w}^H \rangle\rangle_X$
  3. Send  $(\text{declassify}, \text{sid}, \langle\langle \mathbf{w}^H \rangle\rangle_X)$  to  $\mathcal{F}_{\text{declassify}}^{\text{AA}}$  and get back  $(\text{result}, \text{sid}, \mathbf{w}^H)$
  4. Compute  $c \leftarrow H(\mu, \mathbf{w}^H)$ ,  $\langle\langle \mathbf{z} \rangle\rangle_X \leftarrow \langle\langle \mathbf{y} \rangle\rangle_X + c \cdot \langle\langle \mathbf{s}_1 \rangle\rangle_X$ ,  
 $\langle\langle \mathbf{x} \rangle\rangle_X \leftarrow \langle\langle \mathbf{w}^L \rangle\rangle_X - c \cdot \langle\langle \mathbf{s}_2 \rangle\rangle_X$
  5. Obtain  $\langle\langle \mathbf{z} \rangle\rangle_X$  and  $\langle\langle \mathbf{x} \rangle\rangle_X$  from  $\langle\langle \mathbf{z} \rangle\rangle_X$  and  $\langle\langle \mathbf{x} \rangle\rangle_X$  by forgetting the shares of  $P$ 's MACs
  6. Send  $(\text{compute}, \text{sid}, \langle\langle \mathbf{z} \rangle\rangle_X, \langle\langle \mathbf{x} \rangle\rangle_X)$  to  $\mathcal{F}_{\text{rej\_check}}$  and get back  $(\text{result}, \text{sid}, \langle\langle b \rangle\rangle_X)$
  7. Send  $(\text{declassify}, \text{sid}, \langle\langle b \rangle\rangle_X)$  to  $\mathcal{F}_{\text{declassify}}^{\text{AP}}$  and get back  $(\text{result}, \text{sid}, b)$
  8. If  $b = 0$ , send  $(\text{result}, \text{sid}, \perp)$  to  $X$
  9. If  $b = 1$ , then receive  $(\text{ss}, \text{sid}, \llbracket \mathbf{z} \rrbracket_S)$  from  $\mathcal{M}_S$ . Recover  $\mathbf{z}$ . Verify the signature  $(\mathbf{z}, c)$  on the message  $\mu$ . If it verifies, send  $(\text{sig}, \text{sid}, \mathbf{z})$  to  $\mathcal{M}_S$  and  $(\text{signature}, \text{sid}, (\mathbf{z}, c))$  to  $P$ . If it does not verify, then stop working.
- On input  $(\text{sign}, \text{sid}, \mu)$  from  $S$  to  $\mathcal{M}_S$ , the machine  $\mathcal{M}_S$  works as follows:
  1. Perform the steps (1)–(8) above, with  $X \equiv S$
  9. If  $b = 1$ , then send  $(\text{ss}, \text{sid}, \llbracket \mathbf{z} \rrbracket_S)$  to  $\mathcal{M}_P$ . Receive  $(\text{sig}, \text{sid}, \mathbf{z})$  from  $\mathcal{M}_P$ . Verify the signature  $(\mathbf{z}, c)$  on the message  $\mu$ . If it verifies, send  $(\text{signature}, \text{sid}, (\mathbf{z}, c))$  to  $S$ . If it does not verify, then stop working.

**Protocol 4:** ML-DSA protocol,  $\Pi_{\text{ML-DSA}}$

messages terminate or originate at the simulator  $\text{Sim}$  that we are constructing. The messages seemingly going from  $\mathcal{M}_P$  to  $\mathcal{A}$  are the reports of the messages and queries that  $\mathcal{M}_P$  has received from other components of  $\Pi_{\text{ML-DSA}}$  and the environment  $\mathcal{Z}$ , while the messages from  $\mathcal{A}$  to  $\mathcal{M}_P$  are commands to send a message to a certain component or  $\mathcal{Z}$ . The simulator  $\text{Sim}$  has already received from the adversary  $\mathcal{A}$  the corruption query for  $\mathcal{M}_P$ , and forwarded to  $\mathcal{F}_{\text{ML-DSA}}$ .

**Key generation.** On input  $(\text{pkey}, \text{sid}, \mathbf{A}, \mathbf{t})$  from  $\mathcal{F}_{\text{ML-DSA}}$ , proceed as follows. Generate the MAC keys  $\Delta_S$ . Notify  $\mathcal{A}$  of the start of key generation; receive MAC keys  $\Delta_P$  from it (as part of the (init)-queries that  $\mathcal{M}_P$  has to send to ideal components); send these keys back to  $\mathcal{A}$  inside (globkeys)-queries from ideal components.

Receive  $\mathcal{A}$ 's command to  $\mathcal{M}_P$  to send (`generate`,  $sid$ ) to  $\mathcal{F}_{pubrand}$ . Send (`result`,  $sid$ ,  $\mathbf{A}$ ) to  $\mathcal{A}$ .

Receive  $\mathcal{A}$ 's command to  $\mathcal{M}_P$  to send (`compute`,  $sid$ ,  $\mathbf{A}$ ) to  $\mathcal{F}_{keygen}$ . Generate random  $\langle\langle \mathbf{s}_1 \rangle\rangle_P$  and  $\langle\langle \mathbf{s}_2 \rangle\rangle_P$ . Compute  $\langle\langle \mathbf{t} \rangle\rangle_P \leftarrow \mathbf{A} \cdot \langle\langle \mathbf{s}_1 \rangle\rangle_P + \langle\langle \mathbf{s}_2 \rangle\rangle_P$ . Send (`result`,  $sid$ ,  $\langle\langle \mathbf{s}_1 \rangle\rangle_P$ ,  $\langle\langle \mathbf{s}_2 \rangle\rangle_P$ ) to  $\mathcal{A}$ .

Receive  $\mathcal{A}$ 's command to  $\mathcal{M}_P$  to send (`declassify`,  $sid$ ,  $\langle\langle \mathbf{t} \rangle\rangle_P$ ) to  $\mathcal{F}_{declassify}^{AA}$ . Compute  $\llbracket \mathbf{t} \rrbracket_S \leftarrow \mathbf{t} - \llbracket \mathbf{t} \rrbracket_P$  and send (as if coming from  $\mathcal{F}_{declassify}^{AA}$ ) the message (`input`,  $sid$ ,  $\langle\langle \mathbf{t} \rangle\rangle_P$ ,  $\llbracket \mathbf{t} \rrbracket_S$ ,  $\llbracket \llbracket \mathbf{t} \rrbracket_S \cdot \Delta_P \rrbracket_S$ ) to  $\mathcal{A}$ . Receive  $\mathcal{A}$ 's updates to  $\langle\langle \mathbf{t} \rangle\rangle_P$ . If  $\mathcal{A}$  has changed  $\llbracket \mathbf{t} \rrbracket_P$  or Server's MAC on it, then tell  $\mathcal{F}_{ML-DSA}$  to stop. Send (`result?`,  $sid$ ) to  $\mathcal{A}$  (as if coming from  $\mathcal{F}_{declassify}^{AA}$ ), receive any updates to  $\mathbf{t}$ , send these to  $\mathcal{F}_{ML-DSA}$  and allow it to proceed.

**Signing.** On input (`signature`,  $sid$ ,  $\mu$ ,  $\sigma$ ) from  $\mathcal{F}_{ML-DSA}$ , where the signature  $\sigma$  is  $(\mathbf{z}, c)$ , proceed as follows. The simulator finds  $\mathbf{x}_0 \leftarrow A \cdot \mathbf{z} - c \cdot \mathbf{t}$ ,  $\mathbf{w}^H \leftarrow \mathbf{x}_0^H$ , and  $\mathbf{x} \leftarrow \mathbf{x}_0 - \alpha \cdot \mathbf{w}^H$ .

The simulator randomly generates  $\langle\langle \mathbf{z} \rangle\rangle_P$  and  $\langle\langle \mathbf{w}^L \rangle\rangle_P$ .

The simulator computes

$$\begin{aligned} & - \langle\langle \mathbf{y} \rangle\rangle_P \leftarrow \langle\langle \mathbf{z} \rangle\rangle_P - c \cdot \langle\langle \mathbf{s}_1 \rangle\rangle_P \\ & - \langle\langle \mathbf{w} \rangle\rangle_P \leftarrow A \cdot \langle\langle \mathbf{y} \rangle\rangle_P \\ & - \langle\langle \mathbf{x} \rangle\rangle_P \leftarrow \langle\langle \mathbf{w}^L \rangle\rangle_P - c \cdot \langle\langle \mathbf{s}_2 \rangle\rangle_P \\ & - \langle\langle \mathbf{w}^H \rangle\rangle_P \leftarrow \alpha^{-1} \cdot (\langle\langle \mathbf{w} \rangle\rangle_P - \langle\langle \mathbf{w}^L \rangle\rangle_P) \\ & - \llbracket \mathbf{z} \rrbracket_S \leftarrow \mathbf{z} - \llbracket \mathbf{z} \rrbracket_P \end{aligned}$$

The simulator (as  $\mathcal{M}_P$ ) notifies  $\mathcal{A}$  that a signing session for  $\mu$  has started. Receive  $\mathcal{A}$ 's command to  $\mathcal{M}_P$  to send (`compute`,  $sid$ ,  $\mathbf{A}$ ) to  $\mathcal{F}_{gcom}$ . Send  $\langle\langle \mathbf{y} \rangle\rangle_P$  and  $\langle\langle \mathbf{w}^H \rangle\rangle_P$  to  $\mathcal{A}$ , as if coming from  $\mathcal{F}_{gcom}$ .

Receive  $\mathcal{A}$ 's command to  $\mathcal{M}_P$  to send (`declassify`,  $sid$ ,  $\langle\langle \mathbf{w}^H \rangle\rangle_P$ ) to  $\mathcal{F}_{declassify}^{AA}$ , and  $\mathcal{A}$ 's updates to  $\langle\langle \mathbf{w}^H \rangle\rangle_P$ . Continue as in the simulation of declassification during key generation;  $\mathcal{A}$  will learn  $\mathbf{w}^H$ .

Receive  $\mathcal{A}$ 's command to  $\mathcal{M}_P$  to send (`compute`,  $sid$ ,  $\langle\langle \mathbf{z} \rangle\rangle_P$ ,  $\langle\langle \mathbf{x} \rangle\rangle_P$ ) to  $\mathcal{F}_{rej\_check}$ . The simulator already knows the expected values of  $\langle\langle \mathbf{z} \rangle\rangle_P$  and  $\langle\langle \mathbf{x} \rangle\rangle_P$ ; if the adversary submits something else, then the simulator treats it as a failure in verifying Server's MACs and tells the ideal functionality to not output the signature. Simulator generates a random  $\langle\langle b \rangle\rangle_P$  and sends it to  $\mathcal{A}$ , as if coming from  $\mathcal{F}_{rej\_check}$ . Following that, the simulator expects  $\mathcal{A}$ 's command to  $\mathcal{M}_P$  to send (`declassify`,  $sid$ ,  $\langle\langle b \rangle\rangle_P$ ) to  $\mathcal{F}_{declassify}^{AP}$ . Perform the declassification as above;  $\mathcal{A}$  learns that  $b = 1$ .

The simulator follows this up with (`ss`,  $sid$ ,  $\llbracket \mathbf{z} \rrbracket_S$ ), presumably from  $\mathcal{M}_S$  to  $\mathcal{M}_P$ . The simulator expects (`sig`,  $sid$ ,  $\mathbf{z}$ ) from  $\mathcal{A}$  (as if  $\mathcal{M}_P$  sent it to  $\mathcal{M}_S$ ), and, having received this, instructs the ideal functionality to output the signature.

But if the simulator received the query (`signature`,  $sid$ ,  $M$ ,  $\perp$ ) from  $\mathcal{F}_{ML-DSA}$ , then the simulator creates a random  $\mathbf{w}^H$  and computes  $c = H(M, \mathbf{w}^H)$ . It also creates random  $\langle\langle \mathbf{y} \rangle\rangle_P$  and  $\langle\langle \mathbf{w}^L \rangle\rangle_P$ . It computes  $\langle\langle \mathbf{z} \rangle\rangle_P$ ,  $\langle\langle \mathbf{w} \rangle\rangle_P$ ,  $\langle\langle \mathbf{y} \rangle\rangle_P$ , and  $\langle\langle \mathbf{w}^H \rangle\rangle_P$  as before, and again sends  $\langle\langle \mathbf{y} \rangle\rangle_P$ ,  $\langle\langle \mathbf{w}^L \rangle\rangle_P$ ,  $\langle\langle \mathbf{w}^H \rangle\rangle_P$ , and  $\mathbf{w}^H$  to the adversary through the same interactions as before. It again expects to get back  $\langle\langle z \rangle\rangle_P$  and

$\langle x \rangle_{\mathcal{P}}$  from the adversary, sent as inputs to  $\mathcal{F}_{\text{rej\_check}}$ . The simulator then tells the adversary that the result of rejection checking is 0.

## 5.2 Simulator for corrupted server

**Key generation.** The key generation protocol and ideal functionality are symmetric between the Phone and the Server, except for the slight asymmetry of  $\mathcal{F}_{\text{declassify}}^{\text{AA}}$ . Similarly, the simulator learns  $(\mathbf{A}, \mathbf{t})$  from  $\mathcal{F}_{\text{ML-DSA}}$ , generates  $\Delta_{\mathcal{P}}$ , gets  $\Delta_{\mathcal{S}}$  from the adversary, generates  $\langle \mathbf{s}_1 \rangle_{\mathcal{S}}$  and  $\langle \mathbf{s}_2 \rangle_{\mathcal{S}}$  and sends them to  $\mathcal{A}$ . The simulator then expects  $\mathcal{A}$  to tell  $\mathcal{M}_{\mathcal{S}}$  to submit (declassify,  $\text{sid}$ ,  $\langle \mathbf{t} \rangle'_{\mathcal{S}}$ ) to  $\mathcal{F}_{\text{declassify}}^{\text{AA}}$ , where  $\langle \mathbf{t} \rangle'_{\mathcal{S}}$  is supposed to be equal to  $\langle \mathbf{t} \rangle_{\mathcal{S}} = \mathbf{A} \cdot \langle \mathbf{s}_1 \rangle_{\mathcal{S}} + \langle \mathbf{s}_2 \rangle_{\mathcal{S}}$ . The simulator, acting as  $\mathcal{F}_{\text{declassify}}^{\text{AA}}$ , sends (input,  $\text{sid}$ ,  $\langle \mathbf{t} \rangle'_{\mathcal{S}}$ ) back to  $\mathcal{A}$ . The adversary  $\mathcal{A}$  returns (update,  $\text{sid}$ ,  $\langle \mathbf{t} \rangle''_{\mathcal{S}}$ ,  $\mathbf{t}'$ ) to the simulator. If  $\langle \mathbf{t} \rangle''_{\mathcal{S}}$  differs from  $\langle \mathbf{t} \rangle_{\mathcal{S}}$  (except for the share of Server's MAC) then the simulator tells  $\mathcal{F}_{\text{ML-DSA}}$  to stop. Otherwise the simulator tells  $\mathcal{F}_{\text{ML-DSA}}$  to return the public key to  $\mathcal{P}$ , and the public key with  $\mathcal{A}$ 's updates  $(\mathbf{A}', \mathbf{t}')$  to  $\mathcal{S}$ .

**Successful signing.** On input (signature,  $\text{sid}$ ,  $\mu$ ,  $(\mathbf{z}, c)$ ) from  $\mathcal{F}_{\text{ML-DSA}}$ , proceed as follows. The simulator finds  $\mathbf{x}_0 \leftarrow \mathbf{A} \cdot \mathbf{z} - c \cdot \mathbf{t}$ ,  $\mathbf{w}^H \leftarrow \mathbf{x}_0^H$ , and  $\mathbf{x} \leftarrow \mathbf{x}_0 - \alpha \cdot \mathbf{w}^H$ . It secret-shares  $\mathbf{z}$  into  $\langle \mathbf{z} \rangle$ , using  $\Delta_{\mathcal{S}}$  and  $\Delta_{\mathcal{P}}$  to compute the MACs. The simulator randomly generates  $\langle \mathbf{w}^L \rangle_{\mathcal{S}}$ . The simulator computes

$$\begin{aligned} - \langle \mathbf{y} \rangle_{\mathcal{S}} &\leftarrow \langle \mathbf{z} \rangle_{\mathcal{S}} - c \cdot \langle \mathbf{s}_1 \rangle_{\mathcal{S}} \\ - \langle \mathbf{w} \rangle_{\mathcal{S}} &\leftarrow A \cdot \langle \mathbf{y} \rangle_{\mathcal{S}} \\ - \langle \mathbf{x} \rangle_{\mathcal{S}} &\leftarrow \langle \mathbf{w}^L \rangle_{\mathcal{S}} - c \cdot \langle \mathbf{s}_2 \rangle_{\mathcal{S}} \\ - \langle \mathbf{w}^H \rangle_{\mathcal{S}} &\leftarrow \alpha^{-1} \cdot (\langle \mathbf{w} \rangle_{\mathcal{S}} - \langle \mathbf{w}^L \rangle_{\mathcal{S}}) \end{aligned}$$

The simulator also computes  $\langle \mathbf{w}^H \rangle_{\mathcal{P}}$  and  $\langle \mathbf{x} \rangle_{\mathcal{P}}$  on the basis of  $\langle \mathbf{w}^H \rangle_{\mathcal{S}}$ ,  $\mathbf{w}^H$ ,  $\langle \mathbf{x} \rangle_{\mathcal{S}}$ ,  $\mathbf{x}$ ,  $\Delta_{\mathcal{P}}$  and  $\Delta_{\mathcal{S}}$ . The simulator (as  $\mathcal{M}_{\mathcal{S}}$ ) notifies  $\mathcal{A}$  that a signing session for  $\mu$  has started. Receives  $\mathcal{A}$ 's command to  $\mathcal{M}_{\mathcal{S}}$  to send (compute,  $\text{sid}$ ,  $\mathbf{A}$ ) to  $\mathcal{F}_{\text{gcom}}$ . Sends  $\langle \mathbf{y} \rangle_{\mathcal{S}}$  and  $\langle \mathbf{w}^H \rangle_{\mathcal{S}}$  to  $\mathcal{A}$ , as if coming from  $\mathcal{F}_{\text{gcom}}$ .

Next, the simulator receives  $\mathcal{A}$ 's command to  $\mathcal{M}_{\mathcal{S}}$  to send the message (declassify,  $\text{sid}$ ,  $\langle \mathbf{w}^H \rangle_{\mathcal{S}}$ ) to  $\mathcal{F}_{\text{declassify}}^{\text{AA}}$ , and  $\mathcal{A}$ 's updates to  $\langle \mathbf{w}^H \rangle_{\mathcal{S}}$ . Continue as in the simulation of declassification during key generation;  $\mathcal{A}$  will learn  $\mathbf{w}^H$ .

The simulator expects  $\mathcal{A}$  to command  $\mathcal{M}_{\mathcal{S}}$  to send to  $\mathcal{F}_{\text{rej\_check}}$  the message (compute,  $\text{sid}$ ,  $\langle \mathbf{z} \rangle_{\mathcal{S}}$ ,  $\langle \mathbf{x} \rangle_{\mathcal{S}}$ ). The simulator generates a random  $\langle b \rangle_{\mathcal{S}}$  and sends it back to  $\mathcal{A}$  (as if coming from  $\mathcal{F}_{\text{rej\_check}}$ ). The simulator then receives the description of the algorithm  $\mathfrak{A}$  from  $\mathcal{A}$  and applies it to  $\langle \mathbf{z} \rangle_{\mathcal{P}}$  and  $\langle \mathbf{x} \rangle_{\mathcal{P}}$  that it generated while secret-sharing  $\mathbf{z}$  and  $\mathbf{x}$ . The algorithm returns a boolean  $b$ . The simulator creates the share  $\langle b \rangle_{\mathcal{P}}$ , such that together with  $\langle b \rangle_{\mathcal{S}}$  it would declassify to  $b$ .

The simulator expects  $\mathcal{A}$  to command  $\mathcal{M}_{\mathcal{S}}$  to submit (declassify,  $\text{sid}$ ,  $\langle b \rangle_{\mathcal{S}}$ ) to  $\mathcal{F}_{\text{declassify}}^{\text{AP}}$ . The simulator simulates the interaction of  $\mathcal{F}_{\text{declassify}}^{\text{AP}}$ , making  $\mathcal{A}$  learn  $b$ .

If  $b$  happened to be 0, then the simulator tells  $\mathcal{F}_{\text{ML-DSA}}$  to not proceed. If  $b = 1$ , then the simulator expects to receive ( $\text{ss}$ ,  $\text{sid}$ ,  $\llbracket \mathbf{z}' \rrbracket_{\mathcal{S}}$ ) from the adversary. If  $\llbracket \mathbf{z}' \rrbracket_{\mathcal{S}}$  is equal to  $\llbracket \mathbf{z} \rrbracket_{\mathcal{S}}$ , then the simulator sends (sig,  $\text{sid}$ ,  $\mathbf{z}$ ) back to the adversary

and allows  $\mathcal{F}_{\text{ML-DSA}}$  to proceed. If  $\llbracket \mathbf{z}' \rrbracket_{\mathcal{S}}$  is different from  $\llbracket \mathbf{z} \rrbracket_{\mathcal{S}}$ , then the simulator sends nothing back to the adversary and tells the ideal functionality to not proceed.

Indeed, sending a different  $\llbracket \mathbf{z}' \rrbracket_{\mathcal{S}}$  corresponds to changing the signature  $(\mathbf{z}, c)$  into  $(\mathbf{z} + \mathbf{z}^\delta, c)$ , where  $\mathbf{z}^\delta = \llbracket \mathbf{z}' \rrbracket_{\mathcal{S}} - \llbracket \mathbf{z} \rrbracket_{\mathcal{S}}$  can be computed by the simulator. The size of the individual elements of  $\mathbf{z}^\delta$  is bounded by  $2(\gamma_1 - \beta)$ . For the changed signature to verify, the equality  $(\mathbf{A} \cdot \mathbf{z})^H = (\mathbf{A} \cdot (\mathbf{z} + \mathbf{z}^\delta))^H$  must hold, or we have a collision of the hash function  $H$ . This equality implies that  $\mathbf{A} \cdot \mathbf{z}^\delta \approx \mathbf{0}$ , i.e. we have a short solution  $\mathbf{x}$  for the task  $(\mathbf{A} \mid \mathbf{I}) \cdot \mathbf{x} = 0$ .

**Unsuccessful signing.** On input  $(\text{signature}, \text{sid}, \mu, \perp)$  from  $\mathcal{F}_{\text{ML-DSA}}$ , the simulator  $\text{Sim}$  proceeds as follows. It generates a random  $\langle\langle \mathbf{w} \rangle\rangle$ , secret-shares  $\langle\langle \mathbf{w}^H \rangle\rangle$  and computes  $\langle\langle \mathbf{w}^L \rangle\rangle \leftarrow \langle\langle \mathbf{w} \rangle\rangle - \alpha \cdot \langle\langle \mathbf{w}^H \rangle\rangle$ . The simulator also makes up random  $\langle\langle \mathbf{z} \rangle\rangle_{\mathcal{S}}$  and  $\langle\langle \mathbf{x} \rangle\rangle_{\mathcal{S}}$ . With these values in hand, the simulation proceeds identically to the “Successful signing” case (Sec. 5.2), up to the point where the adversary  $\mathcal{A}$  has sent the description of the algorithm  $\mathfrak{A}$  to  $\mathcal{F}_{\text{rej\_check}}$  (received by the simulator). The declassification of  $\mathbf{w}^H$  does not help  $\mathcal{A}$  to figure out whether it is in a real or in a simulated execution [28]. The simulator now applies  $\mathfrak{A}$  to such  $\langle\langle \mathbf{z} \rangle\rangle_{\mathcal{P}}$  and  $\langle\langle \mathbf{x} \rangle\rangle_{\mathcal{P}}$  that (1) wouldn’t pass the rejection check together with  $\langle\langle \mathbf{z} \rangle\rangle_{\mathcal{S}}$  and  $\langle\langle \mathbf{x} \rangle\rangle_{\mathcal{S}}$ , and (2) spell the message “You’re in a simulation”.

At this point, the algorithm  $\mathfrak{A}$  “knows” that it is in an execution with  $\mathcal{F}_{\text{ML-DSA}}$  and  $\text{Sim}$ , not with real system consisting of  $\mathcal{M}_{\mathcal{P}}$ , (corrupted)  $\mathcal{M}_{\mathcal{S}}$ , and all the smaller ideal components. But  $\mathfrak{A}$  does not have the ability to communicate this finding back to  $\mathcal{A}$ . It may return the boolean  $b = 0$ , in which case the unsuccessful signing would “successfully finish”. The same “successful finish” would also take place for  $b = 0$  in an execution with the real system. Or,  $\mathfrak{A}$  may return  $b = 1$ , leading to the Phone determining that the Server has cheated in both systems. A way for  $\mathcal{A}$  to distinguish between the two systems is to get  $b = 1$  from  $\mathfrak{A}$  and then try to change  $\llbracket \mathbf{z} \rrbracket_{\mathcal{S}}$  so that the signature would be valid. In this case the real system would return the signature to the environment, while the ideal system would return  $\perp$ . But if the value of  $b$  may be wrong, then  $\mathcal{A}$  does not know yet whether it is in the “successful signing” or “unsuccessful signing” scenario. Hence  $\mathcal{A}$  would be able to change the signature also when  $\mathcal{F}_{\text{ML-DSA}}$  has created an actual signature, the impossibility of which we have discussed above.

This discussion has already explained how the simulation for “unsuccessful signing” concludes:  $\mathfrak{A}$  creates a bit  $b$ , this gets secret-shared as  $\langle\langle b \rangle\rangle$  and declassified. If  $b = 0$ , then the simulator tells  $\mathcal{F}_{\text{ML-DSA}}$  to proceed with outputting  $\perp$ . If  $b = 1$  then the simulator waits for the message  $(\text{ss}, \text{sid}, \llbracket \mathbf{z} \rrbracket_{\mathcal{S}})$ , ignores its content and tells  $\mathcal{F}_{\text{ML-DSA}}$  to stop.

## 6 Evaluation

We have implemented Trilithium in Rust, and measured its performance. All measurements were performed on Lenovo laptop with AMD Ryzen 5 PRO

Category	P → S	S → P	CRP → S
ML-DSA-44	460.26 KiB	460.26 KiB	1.42 MiB
ML-DSA-65	880.11 KiB	880.11 KiB	2.66 MiB
ML-DSA-87	1852.12 KiB	1852.11 KiB	4.44 MiB

**Table 1.** The volume of transmitted data between the parties during ML-DSA key pair generation. The correlated randomness is transmitted to the server in a single packet stream before the protocol is run.

Category	P → S <i>or</i> S → P (single direction)			CR
	Commitment	RejCheck	Total	
ML-DSA-44	56.85 KiB	36.50 KiB	93.36 KiB	55.05 MiB
ML-DSA-65	73.09 KiB	49.35 KiB	122.43 KiB	71.23 MiB
ML-DSA-87	97.45 KiB	66.81 KiB	164.25 KiB	95.95 MiB

**Table 2.** Volume of transmitted data between client and server during a signing attempt. The presented values are the average of P → S and S → P communication.

5650U. Traffic control (tc) was used to add latency between the phone and the server. Our set-up of the three parties during the performance measurements imitates the intended deployment of our protocol: The Server and CRP will be located close-by (but under independent control), with a high-bandwidth and low-latency connection between them. The Phone is located elsewhere, and the latency of the connection between the Phone and the Server will be significant. As usual, the parameters of the connection between the Phone and CRP are not significant: the CR share for the Phone is generated from a single random seed. The CRP knows that seed and generates Server’s CR share so, that it matches Phone’s share.

While the communication between CRP and Phone is negligible (and the same seed can be used over many signings), the size of randomness sent by CRP to the Server is very significant. That randomness is dominated by the MAC shares, and depends on the number of MACs we have given to each value. We have chosen the numbers of MACs so, that the probability of guessing the MAC keys for a single modulus is at most  $2^{-128}$  per try. This means 6 MACs for values shared over  $q$ , and more for smaller moduli.

The number of communicated bytes in key generation and signing is reported in Tables 1 and 2. These numbers were collected in the networking layer of our implementation. We see that they are very much in the realm of practicality. Table 3 gives the bandwidth requirements for sustained computation of signatures.

Our key generation protocol has 3 rounds, while the signing protocol has 14 rounds. We have benchmarked their execution, and report the running times in Tables 4–7. We have varied only the latency of the network, not the bandwidth, because the number of communicated bytes is small. Each reported number is the average of 100 runs. Again, the timings are practical.

As we saw, for higher latencies, we have considered running several signing sessions in parallel, in order to reduce the effect of rejections on latency. Based



Category	P → S	S → P	CRP → S
ML-DSA-44	404 KiB/s	387 KiB/s	234 MiB/s
ML-DSA-65	638 KiB/s	607 KiB/s	362 MiB/s
ML-DSA-87	647 KiB/s	616 KiB/s	370 MiB/s

**Table 3.** The bandwidth required to produce one ML-DSA signature per second. The value is computed as the bandwidth required for a single signing attempt multiplied by the expected number of repetitions.

Category	No latency + CR	30 ms + CR	100 ms + CR
ML-DSA-44	90 + 10	408 + 40	1342 + 110
ML-DSA-65	114 + 36	650 + 66	1658 + 135
ML-DSA-87	99 + 18	549 + 49	1677 + 119

**Table 4.** Mean time (in milliseconds) for key generation. Mean time for creating CR is given separately.

Category	CR	Commitment	RejCheck	Total w/o CR
ML-DSA-44	288	141	104	245
ML-DSA-65	430	155	155	310
ML-DSA-87	587	181	183	364

**Table 5.** Mean time in milliseconds required for a signing iteration with no latency.

Category	CR	Commitment	RejCheck	Total w/o CR
ML-DSA-44	479	523	418	941
ML-DSA-65	976	544	431	975
ML-DSA-87	974	531	475	1006

**Table 6.** Mean time in milliseconds required for 3 parallel signing iterations with 30 ms latency.

Category	CR	Commitment	RejCheck	Total w/o CR
ML-DSA-44	551	1436	1010	2446
ML-DSA-65	1042	1500	1069	2569
ML-DSA-87	1370	1562	1098	2660

**Table 7.** Mean time in milliseconds required for 3 parallel signing iterations with 100 ms latency.

Category	1	2	3	4	6	8
ML-DSA-44	2271	1984	1585	1631	1677	2109
ML-DSA-65	3965	2742	2837	3035	3806	4099
ML-DSA-87	3752	3249	3497	3716	4251	5077

**Table 8.** Mean time in milliseconds of signature creation in dependence on the number of parallel iterations with no latency.

Category	1	2	3	4	6	8
ML-DSA-44	4078	2821	2516	2580	2692	2910
ML-DSA-65	6662	5143	3886	4065	5090	4797
ML-DSA-87	6605	4262	4975	4763	4671	6188

**Table 9.** Mean time in milliseconds of signature creation in dependence on the number of parallel iterations with 30 ms latency.

Category	1	2	3	4	6	8
ML-DSA-44	9763	6704	5753	4830	4950	5080
ML-DSA-65	15667	9422	7558	6582	6837	6956
ML-DSA-87	11741	8211	7248	8065	7409	7656

**Table 10.** Mean time in milliseconds of signature creation in dependence on the number of parallel iterations with 100 ms latency.

on experiments, the best trade-off was achieved when executing 3–4 sessions in parallel, during which more than 50% of signatures are produced in the first run. The averages from these experiments are reported in Tables 8–10.

For ML-DSA-44, executions with 3 parallel sessions and latency of 30 ms managed to create a signature within 1.5 seconds in more than 50% of cases. A major part of the time is spent on the computation and transmission of CR from the CRP to the server. The correlated randomness can be precomputed, reducing the signing time to around 0.75 seconds.

## 7 Concluding remarks

In this paper, we presented Trilithium, a new two-party protocol (with CRP assistance) which allows Phone and Server collaboratively and efficiently produce ML-DSA signatures. It should not be too hard to generalize it to a larger number of parties and to different access structures, as long as these are supported by the underlying MPC protocol set. Indeed, the key generation and the high bits computation readily generalize; they can be re-stated to run on top of an *Arithmetic Black Box (ABB)* [25], as long as this ABB supports computations modulo  $q$  and modulo 2, and conversions between the two moduli. Our rejection checks use a comparison protocol specifically for two parties; this protocol would have to be replaced in other settings.

In a certification-heavy ecosystem, the certifiability of implementations and deployments of protocols has to be considered. In case of Trilithium, one also has

to consider the protection of the CRP, and the size of data sent from the CRP to the Server. E.g. Smart-ID recorded a billion transactions in Baltic states in 2023<sup>15</sup>; if all these had used Trilithium for ML-DSA-44 signatures, the average CRP-to-Server communication rate would have been  $\approx 7.5$  GB/s. While the CRP-Server-pairs could be replicated, the necessary bandwidth is still significant. A Hardware Security Module (HSM) in the form factor of a PCI Express (that easily supports such communication rates) expansion card may be a suitable platform for the CRP. The hosting of the Server party in a similar platform may satisfy both the performance and certification requirements, once these have settled for quantum-safe cryptographic service providers.

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