

Arithmetisation of computation via polynomial semantics for first-order logic

Murdoch J. Gabbay, www.gabbay.org.uk

We propose a compositional shallow translation from a first-order logic with equality, into polynomials; that is, we *arithmetise* the semantics of first-order logic. Using this, we can translate specifications of mathematically structured programming into polynomials, in a form amenable to succinct cryptographic verification.

We give worked example applications, and we propose a proof-of-concept succinct verification scheme based on inner product arguments.

Additional Key Words and Phrases: First-order logic, cryptology, verifiable computation, verifiable logic, zero-knowledge proofs, polynomial semantics

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1. INTRODUCTION

We take a step towards unifying mathematically structured programming (by which we mean ‘*computation that is specified using logic*’) with cryptographically verifiable computation (meaning ‘*executions that can be cryptographically verified*’). We do this by giving

- (1) a *compositional, shallow translation* of first-order logic to polynomials (Figure 2),
- (2) a sound and complete translation of logical validity (i.e. predicates being true) to corresponding polynomial equalities (Theorems 2.3.4 and 4.2.7), and
- (3) a schema for *succinct proofs* (in the cryptographic sense) of the resulting polynomial equalities.

Schemes with this effect exist for von Neumann style abstract machines (a brief but clear survey is included [here](#)), logic circuits [DFGK14; LCL⁺24], and even LISP [LUR] (reference list not exhaustive).¹ Yet first-order logic has as good a claim as any to be a universal language of mathematics — and in particular it is a commonly and successfully used as a high-level language for specifying computation. To our knowledge, it has not yet been compositionally encoded in polynomials in the manner of this paper.

We summarise what this means and why it matters. This summary is aimed not only at cryptographers but also at readers from a logic and computation background, to provide some context:

Boolean circuits contrasted with arithmetic circuits. Logic uses Boolean circuits: combinations of *and*, *or*, and *not* operating on variables that each hold one bit of information: *true* or *false*. Because *and* and *or* distribute over one another, and because *not* satisfies de Morgan dualities, Boolean circuits have a normal form called a *disjunctive normal form*.

Cryptography uses arithmetic circuits: combinations of $+$, $*$, and $x \in \mathbb{Z}$ operating on variables that hold numbers (either from a finite field or possibly just integers). Because $+$ distributes over $*$, arithmetic circuits also have a normal form: *polynomials*, as familiar e.g. from maths in school.

Polynomials are very expressive: they can represent functions (through interpolation [SW22, Chapter 9]²), vectors of integers (by evaluating at $\{1, \dots, n\}$ where n is the length of the vector), multisets (by identification with their multiset of roots), and they can be composed. Polynomials over integers can be evaluated to numbers, and this has unexpected implications including the [Schwartz-Zippel lemma](#) ([permalink](#)) [Tha22, Lemma 3.3, Subsection 3.4].

Propositions in disjunctive normal form are not quite as expressive as polynomials, but they have excellent properties: distributivity of *and* over *or* and of *or* over *and*; de Morgan duality between *true* and *false*; and bitwise number representation which are the basis of computer chips;³ and an extremely well-developed theory which includes logic (including first-, higher-order, and modal logics), declarative programming languages, type-checkers, model-checkers, SAT solvers, formal verification tools, and moving on from there to dependent type theories, modal logics (like TLA+), model theory, and more.

Mathematically structured programming contrasted with cryptography. Mathematically structured programming (**MSP**) is an approach to programming, based on logic, that tries to minimise the distance between a program and its specification, such that (ideally) the specification *is* the program

¹...and it would be impossible to even try, because progress in verifiable computation is currently so rapid.

²See also [this compact but clear overview](#).

³There are two distinct notions of ‘number’ in play here: *integers*, which relate to arithmetic circuits; and *bitwise integers*, which are a distinct thing and relate to Boolean circuits.

and is directly executable, without requiring the programmer to code up a realisation in a lower-level programming language.⁴

Benefits include (arguably) easier verification, and greater productivity, because the code is kept close to its logical intention. Disadvantages include lower efficiency: it can be harder to write high-level code that runs quickly.⁵

Cryptography has historically been about secure and verifiable communication (think: encryption and error-correcting codes); but with the rise of distributed systems, cloud computing, and layer 2 blockchains, the focus of interest in cryptographic techniques has shifted and broadened to include *secure and verifiable computation*. Thus we see modern interest in applications such as succinct proofs of executions, fully homomorphic encryption, multiparty computation, zero knowledge computation, garbled circuits, and more.

The Unique Selling Point. First-order logic has as good a claim as any logic to be a universal (or at least a canonical) mathematical language, and it is heavily used in mathematically structured programming. Thus, giving a compositional shallow mapping of first-order logic predicates to polynomials, and mapping judgements to polynomial equalities, such that succinct proofs for these equalities can be provided, has as good a claim as any to being a universal (or at least a canonical) treatment of verifiable computation.

Note that our mapping encodes *semantics and validity*, not *syntax and derivability*. In particular, it would be mistaken to assume that this paper works by encoding the syntax of FOL and the structure of well-formed derivation trees into arithmetic circuits. We take a semantic approach that is arguably simpler; see Remark 6.3.4 for more discussion, with an example.

We will see how we can write specifications in first-order logic, compile them to polynomials, and evaluate them there. The translation has low overhead, and it bridges the gap between the abstractness of logic and having direct access to the underlying polynomials. Because our translation is very compositional, we will be able to compose simple specifications to make more complex ones. We hope this will inform both concrete applications as well as new research in mathematical logic.

2. POLYNOMIAL SEMANTICS

2.1. Some background on polynomials

We recall some standard definitions and notations. We start off very simply:

NOTATION 2.1.1. We will be interested in \mathbb{Z} (integers), \mathbb{N} (nonnegative whole numbers), and \mathbb{Q} (rationals).

We may use subscripts to indicate ranges, writing e.g. $\mathbb{N}_{\geq 0}$ (which is just \mathbb{N}), $\mathbb{N}_{\geq 1}$ (strictly positive whole numbers), $\mathbb{Q}_{> 0}$, and so on.

Note in particular that $\mathbb{N} = \{0, 1, 2, \dots\}$; as is standard in computer science, we start counting at 0, not 1. However, in keeping with standard usage in mathematics, we will start indexing matrix rows and columns from 1, not 0.

DEFINITION 2.1.2. For the purposes of this paper, a **rational univariate polynomial** (or just **polynomial** for short) is an element $P \in \mathbb{Q}[X]$, where X is a **variable**.

Thus, $P \in \mathbb{Q}[X]$ is a formal sum of powers of X . For example 0 , $1 + X$, and X^2 are all polynomials. Polynomials can be added and multiplied in the usual way. For example, $(1 + X) * (1 - X) = 1 - X^2$.

⁴Of course, all programming languages do this to some extent (an [XKCD comic strip](#) makes a pithy, and entertaining, comment on this), but MSP takes this abstraction to its logical (pun intended) extreme. Aside from the exceptionally high levels of abstraction, MSP is also characterised by something else: using specification languages based on mathematical logic and its semantics.

⁵... on a von Neumann architecture implemented in silicon. But polynomials are not silicon, and it is debatable whether a von Neumann architecture is an optimal compilation target in a world made out of polynomials. A [remarkable article in The Register](#) ([permalink](#)) provides further context and is well worth reading (see the discussion of Dylan).

$$\begin{aligned}
t ::= X & \mid q \in \mathbb{Q} \mid t + t \mid t - t \mid t * t \\
& \mid C_i(t) \mid \text{len}(C) \mid \text{term}(\phi) \quad (1 \leq i \in \mathbb{N}_{\geq 1} \leq \text{arity}(C)) \\
\phi, \psi ::= \top & \mid \perp \mid t = t \mid \phi \wedge \phi \mid \phi \vee \phi \\
& \mid \forall X_C. \phi \mid \exists X_C. \phi \mid C_i < \text{vft} \mid \text{vft} < C_i \\
& \quad (\text{vft variable-free}, 1 \leq i \in \mathbb{N}_{\geq 1} \leq \text{arity}(C))
\end{aligned}$$

The grammars above are in BNF style, such that ϕ to the right of $::=$ represents any predicate. In particular, $\phi \wedge \phi$ is not a typo; it denotes a conjunction of two (possibly non-equal) predicates. Similarly the two t in $t = t$ denote two (possibly non-equal) terms.

Fig. 1: Syntax of terms and predicates (Definition 2.2.3(3))

REMARK 2.1.3. Definition 2.1.2 might seem simple, but for clarity we note a distinction between *expressions denoting polynomials*, and polynomials.

For example: $(1 + X) * (1 - X)$ a polynomial expression. It denotes the polynomial $1 - X^2$, but is not identical to it.

So given $P, Q \in \mathbb{Q}[X]$, when we write $P * Q$ the reader should understand this polynomial expression to mean ‘that polynomial in $\mathbb{Q}[X]$ obtained by performing a polynomial multiplication of P and Q ’, unless stated otherwise.

Polynomial interpolation in Definition 2.1.4 describes a standard method for using a polynomial to express a vector [SW22, Chapter 9].⁶

DEFINITION 2.1.4 (Polynomial interpolation). Suppose $n \geq 0$ and suppose that $M = (m_1, \dots, m_n) \in \mathbb{Z}^n$ is an n -tuple of integers.

- (1) Say that a polynomial $P \in \mathbb{Q}[X]$ **interpolates** M at $(1, 2, \dots, n)$ when $P(i) = m_i$ for each $1 \leq (i \in \mathbb{N}_{\geq 1}) \leq n$.
- (2) Recall that every n -tuple of integers M can be interpolated by a unique **polynomial interpolant**

$$\text{intrplnt}(m_1, \dots, m_n) \in \mathbb{Q}[X]$$

of degree $n-1$ (at most). Thus, $\text{intrplnt}(m_1, \dots, m_n)(i) = m_i$ for $1 \leq (i \in \mathbb{N}_{\geq 1}) \leq n$.

See also Notation 5.1.3, where we extend interpolation to matrices.

NOTATION 2.1.5 (Instantiation). Suppose $P, Q \in \mathbb{Q}[X]$ are polynomials. Write $P(Q)$ for the polynomial obtained by **instantiating** X to Q in P . For example if $P = X + 1$ and $Q = 2 * X$ then

$$P(Q) = 2 * X + 1.$$

In the case that Q is just a number $x \in \mathbb{Q}$, then we may identify this with the number obtained by instantiating X to x and evaluating the result using arithmetic. For example:

$$(X * (X + 1))(6) = 6 * (6 + 1) = 42.$$

It will always be clear what is intended.

2.2. Definition of the denotation, and some discussion

REMARK 2.2.1. Our goal in this paper is to define a logic, give it a semantics in $\mathbb{Q}[X]$, and explore the properties of this semantics. We will:

- (1) define the formal syntax of terms and predicates in Definition 2.2.3;
- (2) discuss some intuitions in Remark 2.2.5;
- (3) define the polynomial semantics for our formal syntax in Definition 2.2.8;
- (4) discuss the semantics in Remark 2.2.9; and

⁶The [Wikipedia page](#) and [this overview](#) are excellent and very accessible introductions.

(5) prove a soundness and completeness result in Theorem 2.3.4.

REMARK 2.2.2. Note that in the interpretation we are about to build in Figure 2, 0 represents truth and any non-zero value represents false; furthermore $\mathbf{\wedge}$ and $\mathbf{\forall}$ (conjunction and universal quantification) map to + (addition), and $\mathbf{\vee}$ and $\mathbf{\exists}$ (disjunction and existential quantification) map to *.

If the reader is used to thinking of truth as 1 and false as 0, as is traditional in logic circuits, then be careful: everything below will be flipped with respect to what you initially expect.

One motivation for representing truth by 0 is that it gives us Theorem 2.3.4. More on this in Remarks 2.2.9 and 2.3.5).

DEFINITION 2.2.3 (Syntax of terms and predicates).

- (1) Fix one **index variable symbol** X .
- (2) Fix a set of **matrix variable symbols** $C, D \in \text{MatrixVar}$.
To each matrix variable symbol we associate a fixed but arbitrary **arity** $\text{arity}(C) \in \mathbb{N}_{\geq 1}$.
- (3) Define **terms** and **predicates** inductively as in Figure 1. In that Figure:
 - (a) i ranges over whole numbers between 1 and the arity of C inclusive, or in symbols: $1 \leq (i \in \mathbb{N}_{\geq 1}) \leq \text{arity}(C)$.
 - (b) This is just formal syntax; we give it meaning later in Figure 2. In particular, $C_i, C_i(t), \text{len}(C)$, and $\text{term}(\phi)$ are just formal syntax.
 - (c) We call a predicate or term **variable-free** when it does not mention X .
 vft in the clauses $C_i < vft$ and $vft < C_i$ in Figure 2 ranges over variable-free terms. For instance, vft could be 0, or $\text{len}(C)$. Put simply, vft ranges over terms that will denote a polynomial that is just a number.

NOTATION 2.2.4. We set up some convenient abbreviations, writing:

$$\begin{array}{ll}
 vft \leq C_i & \text{for } vft - 1 < C_i \\
 C_i \leq vft & \text{for } C_i < vft + 1 \\
 vft < C_i < vft' & \text{for } (vft < C_i) \mathbf{\wedge} (C_i < vft') \\
 vft \leq C_i \leq vft' & \text{for } (vft - 1 < C_i) \mathbf{\wedge} (C_i < vft + 1).
 \end{array}$$

Note that this is meaningful because when we build our semantics, our notion of valuation in Definition 2.2.8(1) will evaluate matrix variable symbols C to *integer* matrices.

There is not much to this otherwise: we could also just as well take the notation above as primitive in our syntax from Figure 1, and interpret \leq (a binary predicate symbol in our formal syntax) using \leq (the binary predicate ‘less than or equals’ on numbers, which returns a truth-value) in Figure 2, alongside interpreting $<$ (the predicate symbol) using $<$ (the predicate ‘strictly less than’).

REMARK 2.2.5. We give some intuitive discussion of the syntax in Definition 2.2.3. Formal definitions will follow below:

- (1) The index variable symbol X is the (unique) variable of the polynomial semantics. Terms and predicates will map to $\mathbb{Q}[X]$ (polynomials over X), as per Figure 2.
- (2) Intuitively a matrix variable symbol $C \in \text{MatrixVar}$ ranges over all integer matrices of size $\text{arity}(C) \times n$, for all $n \in \mathbb{N}_{\geq 1}$.
Using interpolation (Definition 2.1.4) we can also think of C as ranging over elements of $\mathbb{Q}[X]^{\text{arity}(C)} \times \mathbb{N}_{\geq 1}$, where the first component is an $\text{arity}(C)$ -tuple of polynomials over $\mathbb{Q}[X]$, and the second component is the $n \geq 1$ that tells us how to reconstruct the matrix by considering the values of those polynomials on $\{1, \dots, n\}$.
- (3) Terms let us build elements of $\mathbb{Q}[X]$.
The intuition of $C_i(t)$ is an interpolating polynomial for the i th row of C , applied to t (more on this below). The intuition of $\text{len}(C)$ is the number of columns in C (its *length*). The intuition of $\text{term}(\phi)$ is to treat ϕ as a number (this makes the logic *impredicative*; we fold predicates back down into terms).
- (4) Predicates are based on first-order logic. There is equality (of terms) but no negation.

$$\begin{aligned}
[X]_{\zeta} &= X \\
[q]_{\zeta} &= q \quad (q \in \mathbb{Q}) \\
[t + t']_{\zeta} &= [t]_{\zeta} + [t']_{\zeta} \\
[t - t']_{\zeta} &= [t]_{\zeta} - [t']_{\zeta} \\
[t * t']_{\zeta} &= [t]_{\zeta} * [t']_{\zeta} \\
[C_i(t)]_{\zeta} &= \text{intrplnt}(\zeta(C)_i)([t]_{\zeta}) \\
[\text{len}(C)]_{\zeta} &= \text{len}(\zeta(C)) \\
[\text{term}(\phi)]_{\zeta} &= [\phi]_{\zeta} \\
\\
[\top]_{\zeta} &= 0 \\
[\perp]_{\zeta} &= 1 \\
[t = t']_{\zeta} &= ([t]_{\zeta} - [t']_{\zeta})^2 \\
[\phi \wedge \phi']_{\zeta} &= [\phi]_{\zeta} + [\phi']_{\zeta} \\
[\phi \vee \phi']_{\zeta} &= [\phi]_{\zeta} * [\phi']_{\zeta} \\
[\forall X_C. \phi]_{\zeta} &= \sum_{1 \leq (x \in \mathbb{N}_{\geq 1}) \leq \text{len}(\zeta(C))} [\phi]_{\zeta} \\
[\exists X_C. \phi]_{\zeta} &= \prod_{1 \leq (x \in \mathbb{N}_{\geq 1}) \leq \text{len}(\zeta(C))} [\phi]_{\zeta} \\
[C_i < vft]_{\zeta} &= \begin{cases} 0 & \text{if } \forall 1 \leq j \leq \text{len}(\zeta(C)). (\zeta(C)_{i,j} < [vft]_{\zeta}) \\ 1 & \text{otherwise} \end{cases} \\
[vft < C_i]_{\zeta} &= \begin{cases} 0 & \text{if } \forall 1 \leq j \leq \text{len}(\zeta(C)). ([vft]_{\zeta} < \zeta(C)_{i,j}) \\ 1 & \text{otherwise} \end{cases} \\
x \vDash_{\zeta} \phi &\iff [\phi]_{\zeta}(x) = 0
\end{aligned}$$

Negation is expressible using the rest of the logic: see Subsection 4.2.2.

Fig. 2: Polynomial semantics for terms and predicates (Definition 2.2.8)

Intuitively, quantification $\forall X_C$ quantifies over X ranging over whole numbers from 1 to the number of columns in the matrix denoted by C ; so if C denotes an $\text{arity}(C) \times n$ matrix, then $\forall X_C. \phi$ checks $\phi(1), \dots, \phi(n)$, where $\phi(i)$ is the value of (some polynomial denotation which we have not yet constructed of) ϕ for X instantiated to $i \in \{1, \dots, n\}$.

- (5) The intuition of the range check $vft < C_i$ and $C_i < vft$ is to statically check that elements of the i th row of C takes values greater than or less than vft respectively. We use this later to statically check for out-of-bounds pointer errors (see the discussion in Examples 3.2.8 and 3.2.9), and we make further use of this in Section 4.

REMARK 2.2.6. We make a few comments on some simple (non)redundancies in the syntaxes of terms and predicates:

- (1) Subtraction in terms is redundant: we can just express $t - t'$ as $t + (-1) * t'$.
- (2) \top and \perp are redundant: we can just express \top as $0 = 0$, and \perp as $1 = 0$.
- (3) If we admit a unary predicate-former $\text{isZero}(t)$ with semantics $[\text{isZero}(t)]_{\zeta} = [t]_{\zeta}^2$, then \top , \perp , and $t - t'$ become expressible as

$$\top = \text{isZero}(0), \quad \perp = \text{isZero}(1), \quad \text{and} \quad (t = t') = \text{isZero}(t - t').$$

- (4) Negation does not appear explicitly in the syntax or semantics of Figures 1 and 2. Surprisingly, negation is expressible in the rest of the logic: see Subsection 4.2.2.

See also Remark 2.3.6.

Some notation will be useful for Figure 2 and Definition 2.2.8:

NOTATION 2.2.7 (Some basic matrix syntax). Suppose M is a matrix. Then:

- (1) We may write $len(M)$ for the number of columns in M , and we may call this the **length** of M .
- (2) We may write M_i for the i th row in M , which is a vector of length $len(M)$.
- (3) As usual, we may write $M_{i,j}$ for the i, j -th element in M ; i is the row number and j is the column number.

DEFINITION 2.2.8 (Polynomial semantics).

- (1) A **valuation** ς assigns to each matrix variable symbol $C \in \text{MatrixVar}$ with arity $arity(C) \in \mathbb{N}_{\geq 1}$ an $arity(C) \times n$ integer matrix (= having elements from \mathbb{Z}).
Using interpolation (Definition 2.1.4) the reader can also think of $\varsigma(C)$ as a vector of $arity(C)$ many polynomials in X , along with a number n encoding the information that we care about the values of these polynomials on $\{1, \dots, n\}$.

- (2) Given a valuation ς , define **polynomial semantics**

$$[t]_{\varsigma} \in \mathbb{Q}[X] \quad \text{and} \quad [\phi]_{\varsigma} \in \mathbb{Q}[X]$$

for terms and predicates as in Figure 2. See Remark 2.2.9 below for exposition on the notation and design.

If ς is irrelevant (e.g. because no matrix variable symbols are mentioned), we may drop the subscript ς and write $[t]_{\varsigma}$ just as $[t]$, or $[\phi]_{\varsigma}$ just as $[\phi]$. For example, we may write $[2=0] = 4$.

- (3) Given $x \in \mathbb{Q}$, define a **judgement** by

$$x \vDash_{\varsigma} \phi \quad \text{means} \quad [\phi]_{\varsigma}(x) = 0,$$

as per Figure 2. Judgements return truth-values ('true' or 'false').⁷

- (4) If x or ς is irrelevant in $x \vDash_{\varsigma} \phi$ (e.g. because ϕ is fully-quantified, or mentions no matrix variable symbols) then we may omit it, writing

$$\vDash_{\varsigma} \phi \quad \text{or} \quad x \vDash \phi \quad \text{or even just} \quad \vDash \phi.$$

REMARK 2.2.9. Some comments on Figure 2:

- (1) \top maps to $[\top]_{\varsigma} = 0$ and \perp maps to $[\perp]_{\varsigma} = 1$. Conjunction and universal quantification map to addition, and disjunction and existential quantification map to multiplication (cf. also Remark 2.3.5). Consequently we can say:

In our denotation, 'zero' means 'true' and 'nonzero' means 'false'.

This might be confusing because usually (e.g. in logic gates) 1 means 'true' and (just) 0 means 'false', but setting things up as we do gives us Theorem 2.3.4.

- (2) The semantics maps terms and predicates to polynomials over X . So for example, the clause $[X]_{\varsigma} = X$ says that the X denotes itself.
- (3) Similarly the clause $[q]_{\varsigma} = q$ says that $q \in \mathbb{Q}$ denotes itself.
- (4) The clause $[C_i(t)]_{\varsigma} = \text{intrplnt}(\varsigma(C)_i)([t]_{\varsigma})$ carries an implicit condition that $1 \leq (i \in \mathbb{N}) \leq arity(C)$, since as per Definition 2.2.3(3a) and Notation 2.2.7(2) it would not make sense to talk about C_i or $\varsigma(C)_i$ otherwise.
- (5) We break down the clause $[C_i(t)]_{\varsigma} = \text{intrplnt}(\varsigma(C)_i)([t]_{\varsigma})$, as follows:

— $\varsigma(C)$ is an integer matrix with $arity(C)$ rows and $len(\varsigma(C))$ columns. For brevity we may write

$$C = \varsigma(C).$$

Note that the number of rows in C *must* be $arity(C)$; but the number of columns in C is determined by the choice of ς .

— As per Notation 2.2.7(2), C_i denotes the i th row of C , which is a vector of $len(C)$ integers.

— $\text{intrplnt}(C_i) = P$ is a polynomial in X that interpolates the vector C_i at values $1, \dots, n$, as per Definition 2.1.4.

⁷So if ϕ is a predicate and $x \in \mathbb{Q}$ and ς is a valuation then: $[\phi]_{\varsigma}$ is a polynomial; $[\phi]_{\varsigma}(x)$ is a rational number; and $x \vDash_{\varsigma} \phi$ is a truth-value.

— $P([t]_\zeta)$ is P instantiated at $[t]_\zeta$, as per Notation 2.1.5.

- (6) Reading the previous point, the reader might ask why we bother with matrices at all. Why not just let $\zeta(C)_i$ be a polynomial?

Because a matrix and its interpolant are not the same thing, if we care about bounds (which we do); a vector has a *length*, and a polynomial *a priori* does not. For example, the same polynomial X interpolates many distinct vectors, including (1) , $(1, 2)$ and $(1, 2, 3)$.⁸

REMARK 2.2.10. Some comments on Definition 2.2.8:

- (1) $\zeta(C)$ in Definition 2.2.8(1) is an integer matrix C with *arity*(C) rows and n columns. The number of rows in C is determined by *arity*(C), but its number of columns depends on ζ .
- (2) Valuations ζ map matrix variable symbols to integer matrices. They do not instantiate the (single) variable symbol X !
- (3) In Definition 2.2.8(2), $[\phi]_\zeta$ is a polynomial in X — it is not a truth-value. For example,

$$[X = 2 * X]_\zeta = X^2$$

(there are no matrix variable symbols here so the choice of ζ does not matter).

- (4) In Definition 2.2.8(3), $x \models_\zeta \phi$ is a truth-value — it is not a polynomial. For example,

$$0 \models_\zeta X = 2 * X \text{ is true, and } 1 \models_\zeta X = 2 * X \text{ is false.}$$

- (5) The denotation of universal quantification \forall is a summation, like the denotation of \wedge . However, \forall cannot be emulated in the logic by repeated \wedge , because the length of the summation depends on the number of columns in the matrix $\zeta(C)$, which varies depending on the choice of ζ . So universal quantification has its own distinct identity.
- (6) A design path which we do not explore in this paper is that our denotation is compatible with notions of ‘logical implication’ $P \implies Q$ and ‘logical equivalence’ $P \iff Q$ on polynomials $P, Q \in \mathbb{Q}[X]$, such that

$$\begin{aligned} P \implies Q & \text{ when } \forall x \in \mathbb{Q}. P(x) = 0 \implies Q(x) = 0 & \text{ and} \\ P \iff Q & \text{ when } \forall x \in \mathbb{Q}. P(x) = 0 \iff Q(x) = 0. \end{aligned}$$

Note that $P \implies Q$ does not quite mean the same thing as $P \mid Q$ (P divides Q), because the multiplicities of the roots in P may differ from those in Q . Similarly, $P \iff Q$ does not quite mean the same thing as $P = Q$. For example:

$$(X - 1) \iff (X - 1) * (X^2 + 1) \quad \text{and} \quad (X - 1) \iff (X - 1)^2.$$

We do not explore these notions of implication and equivalence explicitly in what follows, but they are implicit in much of the maths; exploring this explicitly is future work.

For now, it seems to be more useful to explore a technically different design path and consider a *complementation operation* $\sim\phi$; see Notation 4.2.6 and Theorem 4.2.7.

2.3. Soundness and completeness of the denotation

DEFINITION 2.3.1. Call a polynomial $P \in \mathbb{Q}[X]$ **nonnegative** when $P(x) \geq 0$ for every $x \in \mathbb{Q}$.

LEMMA 2.3.2. Suppose ϕ is a predicate and ζ is a valuation and $x \in \mathbb{Q}$.

Then $[\phi]_\zeta$ is a nonnegative polynomial (Definition 2.3.1).

Proof. By a routine induction, following Figure 2:

— $[\top]_\zeta = 0$ and $[\perp]_\zeta = 1$. We note that 0 and 1 are nonnegative.

— $[t = t'] = ([t]_\zeta - [t']_\zeta)^2$. It is a fact of arithmetic that this is a nonnegative polynomial, because it is a square.⁹

⁸Obviously, in an implementation we care about efficiency so we might (or might not) prefer the rows of our matrix to be delivered as a vector of interpolating polynomials, along with an intended length. We can choose how best to represent our data. But, what that representation *represents*, is a matrix.

⁹Indeed, the square is there precisely to guarantee nonnegativity.

- $[\phi \wedge \phi']_\zeta = [\phi]_\zeta + [\phi']_\zeta$ and $[\phi \vee \phi']_\zeta = [\phi]_\zeta * [\phi']_\zeta$. We observe that the sum and product of two (by inductive hypothesis) nonnegative polynomials, is nonnegative.
- As per figure 2, $[\forall X_C.\phi] = \sum_{1 \leq (x \in \mathbb{N}_{\geq 1}) \leq \text{len}(\zeta(C))} [\phi]_\zeta$. We observe that this is a sum of (by inductive hypothesis) nonnegative polynomials, and so it is also nonnegative. Similarly for $\exists X_C.\phi$.
- $[vft < C_i]_\zeta$ and $[C_i < vft]_\zeta$ are equal to 0 or 1, and both are nonnegative. \square

REMARK 2.3.3. Lemma 2.3.2 is interesting because it suggests that we can identify the nonnegative polynomials in $\mathbb{Q}[X]$ as having *logical content*, in the sense that these can be the denotations of predicates; in contrast to the class of all polynomials in $\mathbb{Q}[X]$, which can be the denotation of terms but not necessarily of predicates.

THEOREM 2.3.4 (Soundness & completeness). *Suppose ϕ and ϕ' are predicates, and t and t' are terms, and ζ is a valuation and $x \in \mathbb{Q}$. Then:*

- (1) $x \models_\zeta \top$ and $x \not\models_\zeta \perp$
- (2) $x \models_\zeta t = t'$ if and only if $[t]_\zeta(x) = [t']_\zeta(x)$.
- (3) $x \models_\zeta \phi \wedge \phi'$ if and only if $x \models_\zeta \phi \wedge x \models_\zeta \phi'$.
- (4) $x \models_\zeta \phi \vee \phi'$ if and only if $x \models_\zeta \phi \vee x \models_\zeta \phi'$.
- (5) $x \models_\zeta \forall X_C.\phi$ if and only if $x' \models_\zeta \phi$ for every $1 \leq (x' \in \mathbb{N}_{\geq 1}) \leq \text{len}(\zeta(C))$.
- (6) $x \models_\zeta \exists X_C.\phi$ if and only if $x' \models_\zeta \phi$ for some $1 \leq (x' \in \mathbb{N}_{\geq 1}) \leq \text{len}(\zeta(C))$.
- (7) $x \models_\zeta vft < C_i$ if and only if every entry in the i th row of $\zeta(C)$ is strictly greater than $[vft]_\zeta$.¹⁰
- (8) $x \models_\zeta C_i < vft$ if and only if every entry in the i th row of $\zeta(C)$ is strictly less than $[vft]_\zeta$.

Proof. We consider each part in turn, unpacking Figure 2. The argument is just by routine arithmetic:

- (1) $x \models \top$ when $0 = 0$, which is true. $x \models \perp$ when $1 = 0$, which is false.
- (2) $x \models t = t'$ when $(t - t')^2(x) = 0$. By elementary facts of arithmetic, this is if and only if $t(x) = t'(x)$.
- (3) $x \models \phi \wedge \phi'$ when $[\phi](x) + [\phi'](x) = 0$. Using Lemma 2.3.2, this holds if and only if $[\phi] = 0$ and $[\phi'] = 0$, and thus if and only if $x \models \phi \wedge x \models \phi'$.
- (4) $x \models \phi \vee \phi'$ when $[\phi] * [\phi'] = 0$. Using the fact that \mathbb{Q} is an *integral domain* [Hun12, page 48], this holds and only if $[\phi] = 0$ or $[\phi'] = 0$.
- (5) As for the case of \forall , using Lemma 2.3.2 and arithmetic, the sum $\sum_{1 \leq (x \in \mathbb{N}_{\geq 1}) \leq \text{len}(\zeta(C))} [\phi]_\zeta$ is zero if and only if all of its component summands are zero.
- (6) The product $\prod_{1 \leq (x \in \mathbb{N}_{\geq 1}) \leq \text{len}(\zeta(C))} [\phi]_\zeta$ is zero if and only if one of its component summands is zero.
- (7) By Figure 2, $[vft < C_i]_\zeta$ means precisely that vft is strictly less than every entry in the i th row of $\zeta(C)$. Similarly for $C_i < vft$.

We will also use $vft \leq C_i$ and $C_i \leq vft$ as per Notation 2.2.4. Because $\zeta(C)$ is an integer matrix, $[vft \leq C_i]_\zeta$ means precisely that vft is less than or equal to every entry in the i th row of $\zeta(C)$, and similarly for $C_i \leq vft$. \square

REMARK 2.3.5. Theorem 2.3.4 is important — a logical judgements is valid precisely when its polynomial translation is — but mathematically it is elementary. It just dresses up the fact that in \mathbb{Q} ,

- a sum of two nonnegative numbers is zero if and only if both numbers are zero, and
- a product of two nonnegative numbers is zero if and only if at least one of the numbers is zero.

But of course, this is just what we need to precisely interpret logical conjunction / universal quantification, and logical disjunction / existential quantification, respectively.

What is absent from Theorem 2.3.4, because it is absent from our syntax in Figure 1, is *negation* — there is no explicit predicate-former $\neg\phi$. This is because it can be expressed using the rest of the

¹⁰Note that $[vft]_\zeta$ is just a number, because vft is a term that is assumed to not mention X .

logic; we will see later in Notation 4.2.6 and Theorem 4.2.7 that our logic is *complemented*. That is: given ϕ , we can write a complement predicate $\sim\phi$ that is valid if and only if ϕ is not valid.

But note also that our examples will show that even the negation-free fragment of FOL is very expressive (expressive enough to e.g. encode a Turing-complete model of computation; see Subsection 3.9).

For now, we just note that Theorem 2.3.4 is more powerful than it looks, and it will turn out that it can indeed lay claim to expressing full first-order logic with negation.

Remark 2.3.6 is in a sense a continuation of Remark 2.2.6:

REMARK 2.3.6 (Dualities).

- (1) \exists is a natural de Morgan dual to \forall (as usual). We will use \forall all the time in our practical examples below, but not make much use of \exists . It costs nothing to include it anyway.
- (2) term maps from nonnegative polynomials to all polynomials just by being the identity. This is in a sense dual to the ‘comparison with zero’ ($t=0$), which maps from all polynomials to nonnegative polynomials by squaring. We *will* use this, most notably in our complementation operation from Notation 4.2.6.

REMARK 2.3.7. Later on in Subsection 5.1.3 we show how to obtain succinct proofs of our polynomial semantics using an inner product argument. Soundness and completeness of the overall translation follow by combining Theorem 2.3.4 with soundness and completeness of the inner product argument. This is not complicated, but readers from cryptography may not be expecting it (they may be used to seeing monolithic arguments for soundness and completeness). We see this as a feature, not a bug, because it makes the reasoning simple and modular.

3. EXPRESSING FUNCTIONS

3.1. Simpler is better

REMARK 3.1.1. We have our logic and its denotation. We are now ready to use it to specify functions.

The format of the results in this section is generally the same:

- (1) We specify some (well-known) mathematical function, such as exponentiation.
- (2) We write a predicate in our formal syntax from Definition 2.2.3(3&3). This will correspond in some fairly natural way to the specification.
- (3) We state a soundness lemma, whose proof is usually obvious from Theorem 2.3.4, that the semantics of the predicate as per Figure 2 accurately captures the specification.

This can be a bit fiddly, because concretely manipulating large-ish expressions by hand can be fiddly, but mathematically it is straightforward. There is *supposed* to be a close match between the mathematical intuition, the formal syntax, and the semantics, so that the soundness proofs become straightforward. Our framework is designed to impose a minimal overhead, above and beyond any complexities that may be inherent in what is being expressed.

So for example, if the reader

- looks at Definition 3.2.1 (mathematical definition of exponentiation), and then
- looks at Figure 3 (formal predicate in our logic), and finally perhaps
- looks at Example 3.2.11 (unpacking the polynomial to which the formal predicate compiles)

and finds that this all looks rather straightforward, then that is a good thing and a sign that matters are working out as intended.

3.2. Expressing a power function (with worked examples)

We start with a simple example: how to express a power function (exponentiation) a^b for $a, b \in \mathbb{N}_{\geq 0}$.

DEFINITION 3.2.1. Recall the standard inductive definition of a^b for $a, b \in \mathbb{N}_{\geq 0}$:

$$\begin{array}{ll} \text{base case} & \text{pow}(a, 0) = 1 \\ \text{inductive step} & \text{pow}(a, b + 1) = a * \text{pow}(a, b) \end{array}$$

REMARK 3.2.2. The reader may know that this definition is not particularly efficient. In Remark 3.2.14 we mention the efficient definition; but for the purposes of an initial example, Definition 3.2.1 will serve us very well.

Note an elementary point that definitions like Definition 3.2.1 implicitly equate *computation* with *derivation*, because when we try to compute a value — $\text{pow}(2, 2)$, say — we end up with a simple derivation-tree as follows:

$$\begin{array}{l} \text{base case} \\ \hline \text{pow}(2, 0) = 1 \\ \text{inductive step} \\ \hline \text{pow}(2, 1) = 2 \\ \text{inductive step} \\ \hline \text{pow}(2, 2) = 4 \end{array}$$

This derivation-tree, simple as it is, illustrates the close link between logic and computation.

DEFINITION 3.2.3. Assume a matrix variable symbol pow with arity $\text{arity}(\text{pow}) = 4$. Thus, a denotation $\varsigma(\text{pow})$ is a $4 \times n$ integer matrix, for $n \geq 1$. We will encode an assertion that pow encodes a (partial) power function, such that:

- (1) Row 1 (the values of $\text{pow}_1(X)$) stores input values a .
- (2) Row 2 (the values of $\text{pow}_2(X)$) stores input values b .
- (3) Row 3 (the values of $\text{pow}_3(X)$) stores result values a^b .
- (4) Row 4 (the values of $\text{pow}_4(X)$) stores a pointer (specifically: a column index) to a column in the pow matrix that represents the recursive call to pow in clause 2 of Definition 3.2.1 above.

The schema in Definition 3.2.5 is typical of how we will work, so we spell it out now:

NOTATION 3.2.4.

- (1) For each matrix variable symbol C of interest, we write out a corresponding **validity predicate** χ_C , in which we assume X is not free (e.g. it is bound by a $\forall X$ quantifier).
- (2) We say that ς **satisfies** this validity predicate when $\models_{\varsigma} \chi_C$.
Recall that the $\models_{\varsigma} \chi_C$ notation is from Definition 2.2.8(4); it means $x \models_{\varsigma} \chi_C$ and so $[\chi_C](x) = 0$ for some x , the choice of which does not matter, because X is not free in χ_C .

DEFINITION 3.2.5. In Figure 3 we

- (1) define syntactic sugar,
- (2) use it to express the clauses from Definition 3.2.1, and
- (3) write a *validity predicate* χ_{pow} , to express that pow encodes a partial power function.

Examples of matrices that do and do not satisfy the validity predicate χ_{pow} are in Examples 3.2.8 and 3.2.9.

DEFINITION 3.2.6. Suppose $n \in \mathbb{N}_{\geq 1}$. Say that a $4 \times n$ matrix M **encodes a partial power function** when

$$\forall 1 \leq x \leq n. M(3, x) = M(1, x)^{M(2, x)}.$$

In words, M encodes a partial power function when for each column, the third entry of that column is equal to the first entry raised to the power of the second.

LEMMA 3.2.7. Suppose ς is a valuation (Definition 2.2.8(1)). Then

- if $\models_{\varsigma} \chi_{\text{pow}}$
- then $\varsigma(\text{pow})$ encodes a partial power function (Definition 3.2.6).

$$\begin{array}{ll} \text{pow}_1(t) \text{ sugars to } a(t) & \text{pow}_2(t) \text{ sugars to } b(t) \\ \text{pow}_3(t) \text{ sugars to } \text{out}(t) & \text{pow}_4(t) \text{ sugars to } \text{rec}(t) \end{array}$$

$$\begin{aligned} \text{clause1}(X) &= (b(X) = 0) \wedge (\text{out}(X) = 1) \\ \text{clause2}(X) &= a(X) = a(\text{rec}(X)) \wedge \\ &\quad b(X) = b(\text{rec}(X)) + 1 \wedge \\ &\quad \text{out}(X) = a(X) * \text{out}(\text{rec}(X)) \\ \chi_{\text{pow}} &= 1 \leq \text{rec} \leq \text{len}(\text{pow}) \wedge \forall X_{\text{pow}}. (\text{clause1}(X) \vee \text{clause2}(X)) \end{aligned}$$

Fig. 3: Expressing a power function (exponentiation) a^b (Definitions 3.2.1 and 3.2.5)

Proof. We examine the clauses in Figure 3 and using Theorem 2.3.4 we check that they correctly encode the definition in Definition 3.2.1. \square

EXAMPLE 3.2.8. We illustrate some matrices that pass the validity check $\models_{\zeta} \chi_{\text{pow}}$ and so by Lemma 3.2.7 encode a partial power function:

$$\begin{pmatrix} 2 & 2 & 2 \\ 0 & 1 & 2 \\ 1 & 2 & 4 \\ 1 & 1 & 2 \end{pmatrix} \quad \text{and} \quad \begin{pmatrix} 2 & 2 & 2 \\ 2 & 1 & 0 \\ 4 & 2 & 1 \\ 2 & 3 & 3 \end{pmatrix} \quad \text{and} \quad \begin{pmatrix} 2 & 2 & 2 & 2 \\ 0 & 2 & 1 & 0 \\ 1 & 4 & 2 & 1 \\ 2 & 3 & 1 & 1 \end{pmatrix} \quad \text{and} \quad \begin{pmatrix} 3 & 2 & 3 & 2 \\ 0 & 0 & 1 & 1 \\ 1 & 1 & 3 & 2 \\ 1 & 1 & 1 & 2 \end{pmatrix}$$

Note how all of these matrices can be viewed as concrete implementations of the derivation-tree illustrated in Remark 3.2.2:

- (1) The leftmost matrix encodes the base case of a derivation that $2^2 = 4$ in column 1, the second step in column 2, and the third and final step of the derivation in column 3. The fourth entry in columns 2 and 3 contain a number which is a pointer to the previous step/column in the derivation; the fourth entry in column 1 also contains a number, but it is not used.
- (2) The columns do not need to appear in any particular order, so long as the pointers match up; the next matrix illustrates this (by putting the columns in the reverse order).
- (3) Columns can also be duplicated; the third matrix illustrates this.
- (4) The fourth (rightmost) matrix includes information from two computations; that $2^1 = 2$ and $3^1 = 3$.

EXAMPLE 3.2.9. The converse implication for Lemma 3.2.7 does not hold: it is possible for $\zeta(\text{pow})$ to encode a partial power function yet $\neg(\models_{\zeta} \chi_{\text{pow}})$. Take for example

$$\zeta(\text{pow}) = \begin{pmatrix} 2 \\ 2 \\ 4 \\ 2 \end{pmatrix}$$

This one-column matrix encodes a partial power function because $2^2 = 4$, but it fails the validity predicate because the entry in the fourth row does not point to a column showing that $2^1 = 2$, so this fails the last line of clause2 in Figure 3. We can think of it as corresponding to an *incomplete* derivation-tree:

$$\frac{\text{???}}{\text{pow}(2, 2) = 4} \text{ inductive step}$$

We consider another example:

$$\zeta(\text{pow}) = \begin{pmatrix} 2 & 2 & 2 \\ 2 & 1 & 0 \\ 4 & 2 & 1 \\ 2 & 3 & 0 \end{pmatrix}$$

This matrix encodes a partial power function because $2^2 = 4$ and $2^1 = 2$ and $2^0 = 1$. Furthermore, the entry in the fourth row of the first column *does* point to a column showing that $2^1 = 2$, that the fourth row of the second column does point to a column showing that $2^0 = 1$. However, this fails the range check just for a technical reason: the entry in the fourth row of the third column is 0, which is not in the range 1 to 3.

REMARK 3.2.10. In *constructive logic*, something is only deemed ‘true’ when we have a concrete witness for its truth.¹¹

The way our encoding of logic works, as illustrated in Examples 3.2.8 and 3.2.9, feels constructive in this sense. It might help such a reader to think of matrices as follows: the matrices are used to encode deduplicated derivation-trees. Columns correspond to nodes, and we include index pointers to link these nodes together.

EXAMPLE 3.2.11. We take a moment to unpack the concrete polynomial that Figure 3 expresses.

Assume we have some valuation ζ and write $\text{pow} = \zeta(\text{pow})$. Recall from Definition 2.2.8(1) that this is a $4 \times n$ matrix for some $n \geq 1$. Mirroring the sugar in Figure 3, we write:

$$\begin{aligned} a &= \text{intrplnt}(\text{pow}_1) \\ b &= \text{intrplnt}(\text{pow}_2) \\ \text{out} &= \text{intrplnt}(\text{pow}_3) \\ \text{rec} &= \text{intrplnt}(\text{pow}_4) \end{aligned}$$

Assume that every entry in pow_4 is a valid index of a column in pow , so that pow passes the range check and

$$[0 < \text{pow}_4 < \text{len}(\text{pow}) + 1]_{\zeta} = 0$$

— meaning that this is ‘true’.

Then $[\chi_{\text{pow}}]_{\zeta}$ from Figure 3 expands as follows — where, as per Figure 2, $\mathbf{\Lambda}$ maps to $+$, \mathbf{V} maps to $*$, \mathbf{V} maps to \sum , and $t = t'$ maps to $(t - t')^2$:

$$\begin{aligned} \chi_{\text{pow}} &= 1 \leq \text{pow}_4 \leq \text{len}(\text{pow}) \mathbf{\Lambda} \\ &\quad \mathbf{V} X_{\text{pow}} \cdot (\text{clause1}(X) \mathbf{V} \text{clause2}(X)) \\ [\chi_{\text{pow}}]_{\zeta} &= 0 + \\ &\quad \sum_{1 \leq x \leq n} \left((a(x)^2 + (\text{out}(x) - 1)^2) * \right. \\ &\quad \quad \left((a(x) - a(\text{rec}(x)))^2 + \right. \\ &\quad \quad \left. (b(x) - (b(\text{rec}(x)) + 1))^2 + \right. \\ &\quad \quad \left. (\text{out}(x) - a(x) * \text{out}(\text{rec}(x)))^2 \right) \end{aligned}$$

If this evaluates to zero, then pow really does encode a partial power function.¹²

REMARK 3.2.12 (Analysis of polynomial degree). Examining the polynomial in Example 3.2.11, we can make some comments on its degree:

- (1) Range checks correspond to constant numbers (0 if true, 1 if false). This makes range checks ‘free’, in the sense that they contribute nothing to the degree of the polynomial.

¹¹For example, in classical logic $P \implies Q$ is equivalent to $\neg P \vee Q$. Constructively, $P \implies Q$ is a concrete method for turning a witness to the truth of P , into a witness to the truth of Q .

¹²We consider a succinct check of this using the Schwartz-Zippel lemma later, in Lemma 5.1.6.

- (2) $\mathbf{\Lambda}$ and \mathbf{V} corresponds to $+$. This means that conjunction and universal quantification are ‘free’, in the sense that they do not increase the degree of a polynomial beyond the degrees of the components.
- (3) \mathbf{V} corresponds to $*$. This is linearly expensive, in the sense that the degree is the sum of the degrees of the components.
- (4) Polynomial instantiation, as triggered e.g. by the recursive call $out(rec(x))$ above, is multiplicatively expensive: it multiplies degrees of the polynomials involved. For example, if we set $P(X) = X^2$ and $Q(X) = X^3$ then $P(Q)(X) = (X^3)^2 = X^6$.
- (5) The length of the matrix is linearly expensive in the degree of the interpolating polynomial: as per Definition 2.1.4(2), to interpolate a vector of length n requires a polynomial of degree at most $n-1$.

For brevity write $n = len(pow)$; so this is the number of columns in pow and is (one plus) the degree of the interpolants a , b , out , and rec . Checking the polynomial, we see that its degree is $O(n^2)$, coming from the component $(a * out(rec))^2$ on the far right-hand side, and in particular from the polynomial instantiation $out(rec)$. Now for pow as specified in Definition 3.2.1, n (the number of columns) is equal to b , so the *degree order* of this specification, by which we mean the degree of the polynomials it produces, is $O(b^2)$.

REMARK 3.2.13. Just because a polynomial has high degree does not necessarily make it complicated nor expensive to compute. For instance, X^{100} had degree 100, but 10^{100} is not particularly expensive to compute, and the polynomial has only one root up to multiplicities, so in the logical sense mentioned in Remark 2.2.10(6) it is equivalent to X^2 .¹³

Degree order as a complexity metric is its own thing.

REMARK 3.2.14. Continuing Remark 3.2.12, we can also try to keep n as small as possible, which amounts to minimising the number of calls to pow . As is well-known, a more efficient specification of a^b is this:

$$\begin{aligned} pow(a, 0) &= 1 \\ pow(a, 2 * b) &= pow(a, b) * pow(a, b) \\ pow(a, 2 * b + 1) &= a * pow(a, b) * pow(a, b) \end{aligned}$$

Logically, this leads to smaller derivations, and algorithmically it yields a runtime that is logarithmic in b rather than linear (parametrically over the complexity of our multiplication algorithm).

We could translate this into our logic as we did in Definition 3.2.5, and this would yield an implementation of degree order $O(\log(b)^2)$. More on efficiency in Remark 3.4.6(2) and Section 5.

3.3. Expressing a square root function

The square root \sqrt{a} is a simple example which nicely illustrates that validity predicates are *assertions*, not computations. In particular, we can verify that a matrix represents a partial square root function; just square the claimed root (see Definition 3.3.3). We do not have to design a square root algorithm ourselves. This is an elementary point, which may be familiar to readers familiar with succinct proofs, but we spell out the details:

DEFINITION 3.3.1. Recall that for $a \in \mathbb{Q}$, the **(positive) square root function** \sqrt{a} is specified by

$$(\sqrt{a})^2 = a \wedge \sqrt{a} \geq 0.$$

DEFINITION 3.3.2. Assume a matrix variable symbol sqrt with arity $arity(\text{sqrt}) = 2$. In Figure 4 we use our logic to express a validity predicate χ_{sqrt} that sqrt encodes a partial positive square root function, such that:

¹³It might be possible to efficiently ‘renormalise’ our polynomials to eliminate repeated roots, using formal derivatives and polynomial long division. Indeed, what really matters to us is not the degree of the polynomial itself, but how many distinct rational roots it has. But for now, just looking at the degree of the polynomial will suffice.

$$\chi_{\text{sqrt}} = 0 \leq \text{sqrt}_2 \wedge \forall X_{\text{sqrt}}. \text{sqrt}_2(X) * \text{sqrt}_2(X) = \text{sqrt}_1(X)$$

Fig. 4: Expressing a positive square root function \sqrt{a} (Definitions 3.3.1 and 3.3.2)

- (1) Row 1 (values of $\text{sqrt}_1(X)$) stores input values a .
- (2) Row 2 (values of $\text{sqrt}_2(X)$) stores output values $\sqrt{a} \geq 0$.

DEFINITION 3.3.3. Suppose $n \in \mathbb{N}_{\geq 1}$. Say that a $2 \times n$ matrix M **encodes a partial positive square root function** when

$$\forall 1 \leq x \leq n. 0 \leq M(2, x) \wedge M(2, x)^2 = M(1, x).$$

LEMMA 3.3.4. Suppose ς is a valuation (Definition 2.2.8(1)). Then the following are equivalent:

- (1) $\models_{\varsigma} \chi_{\text{sqrt}}$.
- (2) $\varsigma(\text{sqrt})$ encodes a partial positive square root function, as per Definition 3.3.3.

Proof. We examine χ_{sqrt} in Figure 4 and see that it correctly encodes the definition in Definition 3.5.1. □

REMARK 3.3.5. There are no pointers involved in Lemma 3.3.4, so we get a logical equivalence. Contrast with Lemma 3.2.7 and Examples 3.2.8 and 3.2.9 for pow.

REMARK 3.3.6. The point made by Definition 3.3.3, Figure 4, and Lemma 3.3.4 is not that positive square roots are particularly interesting.¹⁴ What matters is that actually *computing* \sqrt{a} is done by whoever provides ς (i.e. the ‘prover’). Intuitively, our logic is about *verification*. Thus, we assume a ς is provided, and we verify that it satisfies whatever validity predicates have been claimed of it.

3.4. Expressing Fibonacci

The Fibonacci function is a classic example in a certain kind of undergraduate course. We consider it in our system. It is a simple example, though nontrivial, and as we discuss in Remark 3.4.6 it raises some useful points:

DEFINITION 3.4.1. Recall that the Fibonacci sequence $\text{fib}(a)$ for $a \in \mathbb{N}_{\geq 0}$ is defined by:

$$\begin{aligned} \text{fib}(0) &= 0 \\ \text{fib}(1) &= 1 \\ \text{fib}(a + 2) &= \text{fib}(a) + \text{fib}(a + 1) \end{aligned}$$

DEFINITION 3.4.2. Assume a matrix variable symbol fib with arity $\text{arity}(\text{fib}) = 3$. We can think of this as a three-row matrix.

We will use our logic to express a validity predicate χ_{fib} that fib encodes the Fibonacci function, such that:

- (1) Row 1 (values of $\text{fib}_1(X)$) stores input values a .
- (2) Row 2 (values of $\text{fib}_2(X)$) stores result values $\text{fib}(a)$.
- (3) Row 3 (values of $\text{fib}_3(X)$) stores an index of a column in fib , corresponding to the first recursive call to fib in clause 3 of Definition 3.4.1.
- (4) Row 4 (values of $\text{fib}_3(X)$) stores an index of a column in fib , corresponding to the second recursive call to fib in clause 3 of Definition 3.4.1.

¹⁴They are, but not in ways that matter to us here.

$$\begin{array}{l}
\text{fib}_1(t) \text{ sugars to } a(t) \qquad \text{fib}_2(t) \text{ sugars to } \text{out}(t) \\
\text{fib}_3(t) \text{ sugars to } \text{rec1}(t) \qquad \text{fib}_4(t) \text{ sugars to } \text{rec2}(t) \\
\\
\text{clause1}(X) = (a(X) = 0) \wedge (\text{out}(X) = 1) \\
\text{clause2}(X) = (a(X) = 1) \wedge (\text{out}(X) = 1) \\
\text{clause3}(X) = \begin{array}{l} a(X) = a(\text{rec1}(X)) + 2 \\ a(\text{rec2}(X)) = a(\text{rec1}(X)) + 1 \\ \text{out}(X) = \text{out}(\text{rec1}(X)) + \text{out}(\text{rec2}(X)) \end{array} \wedge \\
\chi_{\text{fib}} = \begin{array}{l} 1 \leq \text{rec1} \leq \text{len}(\text{fib}) \wedge 1 \leq \text{rec2} \leq \text{len}(\text{fib}) \wedge \\ \forall X_{\text{fib}}. (\text{clause1}(X) \vee \text{clause2}(X) \vee \text{clause3}(X)) \end{array}
\end{array}$$

Fig. 5: Expressing the Fibonacci function $\text{fib}(a)$ (Definitions 3.4.1 and 3.4.4)

DEFINITION 3.4.3. Suppose $n \in \mathbb{N}_{\geq 1}$. Say that a $4 \times n$ matrix M **encodes a partial Fibonacci function** when

$$\forall 1 \leq x \leq n. M(2, x) = \text{fib}(M(1, x)).$$

DEFINITION 3.4.4. In Figure 5 we

- (1) define syntactic sugar,
- (2) use it to express the clauses from Definition 3.4.1, and
- (3) write a validity predicate χ_{fib} to express that fib encodes a partial Fibonacci function.

LEMMA 3.4.5. Suppose ς is a valuation (Definition 2.2.8(1)). Then

- (1) if $\models_{\varsigma} \chi_{\text{fib}}$,
- (2) then $\varsigma(\text{fib})$ encodes a partial Fibonacci function, as per Definition 3.4.3.

Proof. We examine the clauses in Figure 5 and see that they correctly encode the definition in Definition 3.4.1. The range checks $1 \leq \text{fib}_3 \leq \text{len}(\text{fib})$ and $1 \leq \text{fib}_4 \leq \text{len}(\text{fib})$ are there to exclude out-of-bounds pointer errors. \square

REMARK 3.4.6. There are a few interesting observations to make about Definition 3.4.1 and Figure 5.

- (1) It is not the case that $\varsigma(\text{fib})$ has to contain the entries of the Fibonacci sequence *in order*. Each column has to contain a value a , the number $\text{fib}(a)$, and (if $a \notin \{0, 1\}$) it contains pointers to two columns representing ‘recursive calls’. Where those recursive calls are located inside $\varsigma(\text{fib})$ does not really matter.
- (2) We put ‘recursive calls’ in scare quotes because these are not recursive calls to a computation.¹⁵ Definition 3.4.1 is famous in undergraduate courses because, if translated directly to code, it is very inefficient; each call to fib creates two sub-calls, thus giving the program exponential complexity.

A standard solution is to memoise the computation; store the results of previous calls and replace them with lookups if the function is called again on the same input.

Figure 5 is already *naturally memoised*. Indeed, it is nothing more than a lookup-table. As we noted in Remark 3.3.6, our logic is *verifying* (not computing!) a ς .¹⁶

¹⁵Well ... it depends on your point of view. They are recursive calls, but in a computation that is a verification of another computation that we assume has already been made.

¹⁶A valuation ς could contain repeated columns, corresponding to repeated calls to the same function on the same inputs, but this would be an inefficient prover; the verifier is still efficient on that (needlessly complex) input. Example 3.2.8 includes some basic examples with duplicated columns; they could be removed, but beyond padding out the matrix they do no harm.

$$\begin{array}{l}
\text{Ack}_1(t) \text{ sugars to } a(t) \quad \text{Ack}_2(t) \text{ sugars to } b(t) \\
\text{Ack}_3(t) \text{ sugars to } \text{out}(t) \quad \text{Ack}_4(t) \text{ sugars to } \text{rec1}(t) \quad \text{Ack}_5(t) \text{ sugars to } \text{rec2}(t) \\
\\
\text{clause1}(X) = (a(X) = 0) \wedge (\text{out}(X) = b(X) + 1) \\
\text{clause2}(X) = \begin{array}{l} a(X) = a(\text{rec1}(X)) + 1 \wedge \\ b(X) = 0 \quad \quad \quad \wedge \\ \text{out}(X) = \text{out}(\text{rec1}(X)) \end{array} \\
\text{clause3}(X) = \begin{array}{l} a(X) = a(\text{rec2}(X)) + 1 \wedge \\ b(X) = b(\text{rec1}(X)) + 1 \wedge \\ \text{out}(X) = \text{out}(\text{rec2}(X)) \wedge \\ a(\text{rec1}(X)) = a(\text{rec2}(X)) + 1 \wedge \\ b(\text{rec2}(X)) = \text{out}(\text{rec1}(X)) \end{array} \\
\chi_{\text{Ack}} = 1 \leq \text{rec1} \leq \text{len}(\text{Ack}) \wedge 1 \leq \text{rec2} \leq \text{len}(\text{Ack}) \wedge \\
\forall X_{\text{Ack}}. (\text{clause1}(X) \vee \text{clause2}(X) \vee \text{clause3}(X))
\end{array}$$

Fig. 6: Expressing Ackermann's function $\text{Ack}(a, b)$ (Definitions 3.5.1 and 3.5.4)

So the complexity of the program implicit in Definition 3.4.1 is actually linear (not exponential), because our semantics is in some sense inherently memoised.

REMARK 3.4.7 (Fast Fibonacci and algebraic extensions of \mathbb{Q}). Optimised methods for computing Fibonacci numbers exist. See for instance [this webpage \(permlink\)](#) and especially [this webpage \(permlink\)](#), which uses the Binet formula — a paper on this topic exists [Das18], however, the webpage includes significant additional optimisations.

This is interesting because The Binet formula uses $\sqrt{5}$, which is irrational and so is not in \mathbb{Q} , which is the field we use in this paper. So what this example illustrates is that \mathbb{Q} may not be enough: we may wish to talk about roots of polynomials that are not rationals, such as $x^2 = 5$, and if so, we might need to consider algebraic extensions, such as $\mathbb{Q}(\sqrt{5})$. Technically, this should not be a problem.

3.5. Expressing Ackermann's function

Ackermann's function is another classic example. We check how it comes out in our framework:

DEFINITION 3.5.1. Recall Ackermann's function $\text{Ack}(a, b)$ for $a, b \in \mathbb{N}_{\geq 0}$, defined by:

$$\begin{array}{l}
\text{Ack}(0, b) = b + 1 \\
\text{Ack}(a + 1, 0) = \text{Ack}(a, 1) \\
\text{Ack}(a + 1, b + 1) = \text{Ack}(a, \text{Ack}(a + 1, b))
\end{array}$$

DEFINITION 3.5.2. Assume a matrix variable symbol Ack with arity $\text{arity}(\text{Ack}) = 5$. We can think of this as a five-row matrix.

We will use our logic to express a validity predicate χ_{Ack} that Ack encodes a (partial) Ackermann function, such that:

- (1) Row 1 (values of $\text{Ack}_1(X)$) stores input values a .
- (2) Row 2 (values of $\text{Ack}_2(X)$) stores input values b .
- (3) Row 3 (values of $\text{Ack}_3(X)$) stores result values $\text{Ack}(a, b)$.
- (4) Row 4 (values of $\text{Ack}_4(X)$) stores an index of a column in the Ack matrix, corresponding to the first recursive call to Ack in clauses 2 and 3 of Definition 3.5.1.
- (5) Row 5 (the values of $\text{Ack}_5(X)$) stores an index of another column in the Ack matrix, corresponding to the second recursive call to Ack in clause 3 of Definition 3.5.1.

DEFINITION 3.5.3. Suppose $n \in \mathbb{N}_{\geq 1}$. Say that a $5 \times n$ matrix M **encodes a partial Ackermann's function** when

$$\forall 1 \leq x \leq n. M(3, x) = \text{Ack}(M(1, x), M(2, x)).$$

DEFINITION 3.5.4. In Figure 6 we

- (1) define syntactic sugar,
- (2) use it to express the clauses from Definition 3.5.1, and
- (3) write a validity predicate χ_{Ack} to express that Ack encodes a partial Ackermann function.

LEMMA 3.5.5. Suppose ς is a valuation (Definition 2.2.8(1)). Then

- (1) if $\models_{\varsigma} \chi_{\text{Ack}}$
- (2) then $\varsigma(\text{Ack})$ encodes a partial Ackermann function, as per Definition 3.5.3.

Proof. We examine the three clauses in Figure 6 and see that they correctly encode the definition in Definition 3.5.1. The range checks $1 \leq \text{Ack}_4 \leq \text{len}(\text{Ack})$ and $1 \leq \text{Ack}_5 \leq \text{len}(\text{Ack})$ are there to exclude out-of-bounds pointer errors. \square

3.6. Bitwise representation

Unsigned 64-bit integers — the familiar `uint64` datatype — are a standard datatype of practical interest. It is straightforward to represent them:

DEFINITION 3.6.1. We express that t is equal to zero or one (i.e. that t represents a single bit of binary data) using a little syntactic sugar:

$$\text{isBinary}(t) \quad \text{is sugar for} \quad t = 0 \vee t = 1.$$

Unpacking Figure 2, $\text{isBinary}(t)$ corresponds to a polynomial $t^2 * (t - 1)^2$.

DEFINITION 3.6.2. Assume a matrix variable symbol `uint64` with $\text{arity}(\text{uint64}) = 65$, which we can think of as representing a 65-row matrix.

- (1) Row 1 should store a whole number $0 \leq n < 2^{64}$, and
- (2) rows 2 to 65 should store its binary expansion (with smallest bit first).

Validity of `uint64` is simply

$$\chi_{\text{uint64}} = \forall X_{\text{uint64}}. (\text{uint64}_1(X) = \sum_{0 \leq (i \in \mathbb{N}_{\geq 0}) < 64} 2^i * \text{uint64}_{i+2}(X) \wedge \text{isBinary}(\text{uint64}_2(X)) \wedge \dots \wedge \text{isBinary}(\text{uint64}_{65}(X)))$$

This just asserts that for each column in `uint64`, elements 2 to 65 of that vector do indeed store bits representing the binary representation of element 1.

LEMMA 3.6.3. Suppose ς is a valuation (Definition 2.2.8(1)). Then the following are equivalent:

- (1) $\models_{\varsigma} \chi_{\text{uint64}}$ for χ_{uint64} as defined in Definition 3.6.2.
- (2) $\varsigma(\text{uint64})$ encodes a list of bitwise expansions, by which we mean precisely that

$$\forall 1 \leq x \leq \text{len}(\varsigma(\text{uint64})). \varsigma(\text{uint64})_1(x) = \sum_{0 \leq (i \in \mathbb{N}_{\geq 0}) < 64} 2^i * \varsigma(\text{uint64})_{i+2}(x).$$

Proof. We examine χ_{uint64} in Definition 3.6.2 and using Theorem 2.3.4 and basic arithmetic see that this is precisely what it encodes. Note that there are no range checks, pointers, or recursive calls involved; this is just straightforward arithmetic. Note also that 2^i is in the predicate above is just a number (not a power of X), so has degree zero in X . \square

REMARK 3.6.4. We get a decimal representation just by generalising `isBinary` to `isDecimal`:

$$\text{isDecimal}(t) \quad \text{is sugar for} \quad t = 0 \vee t = 1 \vee \dots \vee t = 9.$$

and use a matrix variable symbol $\text{uint64}^{\text{base } 10}$ with validity predicate

$$\text{clause}(X) = (\text{uint64}_1^{\text{base } 10}(X) = \sum_{0 \leq (i \in \mathbb{N}_{\geq 0}) < 64} 10^i * \text{uint64}_{i+2}^{\text{base } 10}(X) \wedge \text{isDecimal}(\text{uint64}_2^{\text{base } 10}(X)) \wedge \dots \wedge \text{isDecimal}(\text{uint64}_{65}^{\text{base } 10}(X))).$$

We can also nest representations and for example represent a number in the range 0 to $(2^{64})^{64}$ by taking its 64-digit expansion base 2^{64} .

3.7. Range check

REMARK 3.7.1. The clause for the range check $<$ in Figure 2 is very simple — just check that the relevant row in the matrix satisfies the required bound, and return 0 if it is satisfied and 1 if it is not. However, later on in Subsection 5.1 it will be convenient to express $<$ inside the language *without* $<$.

In particular, we see from our examples — such as those in Figures 3 and 6 — that we typically want to prove range checks that have an *upper and lower* bound, having this form:

$$1 \leq C_i \leq \text{len}(C).$$

We can translate predicates of this form into ones that does not mention $<$, as follows:

DEFINITION 3.7.2. Suppose $l, r \in \mathbb{Z}$ and $l \leq r$. Then define a polynomial $\text{range}_{l,r}(X)$ by

$$\text{range}_{l,r}(X) = (X = l) \vee (X = l+1) \vee \dots \vee (X = r-1) \vee (X = r).$$

($\text{range}_{l,r}$ is closely related to the *zeroes* polynomial which we define later in Definition 5.1.2. The difference is just that range is a predicate in our logic.)

REMARK 3.7.3. Range checks are special cases of *set membership* assertions. Succinct (and zero knowledge) set membership schemes are a field of research in their own right ([BCF⁺23] contains a recent survey). So: the schema in Definition 3.7.2 may not be optimal and in a practical implementation of these ideas we might wish to improve it further, but for proof-of-concept Definition 3.7.2 will serve our purposes perfectly well, and it has the benefit of being very simple. Lemma 3.7.4 says that it does what we need it to do:

LEMMA 3.7.4. *Suppose ς is a valuation. Then the following are equivalent:*

- (1) $\models_{\varsigma} 1 \leq C_i \leq \text{len}(C)$.
- (2) $\models_{\varsigma} \forall X_C. \text{range}_{1, \text{len}(C)}(C_i(X))$.

Proof. $\varsigma(C)$ is by Definition 2.2.8(1) an integer matrix, and the polynomial $[\text{range}_{l,r}]$ by construction in Definition 3.7.2 and Figure 2 has zeroes precisely at the integers between l and r inclusive, which by Notation 2.2.4 and Figure 2 is precisely what $1 \leq C_i \leq \text{len}(C)$ checks. \square

REMARK 3.7.5. If $\varsigma(C)$ is a very long matrix then $[\text{range}_{1, \text{len}(C)}(C_i(X))]$ might be a large (high-degree) polynomial. But, in the context of the *succinct proofs* that we will consider later in Subsection 5.1, this may be is a worthwhile tradeoff.

REMARK 3.7.6. We can also exploit uint64 from Subsection 3.6 to do a simple range check. Assume a matrix variable symbol range64 with $\text{arity}(\text{range64}) = 2$, and with validity condition

$$\text{clause}(X) = 1 \leq \text{range64}_2 \leq \text{len}(\text{uint}) \wedge (\text{range64}_1(X) = \text{uint}_2(\text{range64}_2(X))).$$

Intuitively, we can think of the above as follows:

- (1) $\text{range64}_1(X)$ holds some number n .
- (2) $\text{range64}_2(X)$ holds a pointer to a proof in uint64 that n has a 64-bit representation.
- (3) The pointers in range64_2 must be in-bound for the addressable range in uint ; i.e. they must be numbers between 1 and $\text{len}(\text{uint64})$.

The above can only happen if $0 \leq n < 2^{64}$, so this all amounts to doing range checks.

$$\begin{array}{lll}
\text{iter}_{f,1}(t) \text{ sugars to } a(t) & \text{iter}_{f,2}(t) \text{ sugars to } b(t) & \\
\text{iter}_{f,3}(t) \text{ sugars to } \text{out}(t) & \text{iter}_{f,4}(t) \text{ sugars to } \text{rec}(t) & \text{iter}_{f,5}(t) \text{ sugars to } \text{callf}(t)
\end{array}$$

$$\begin{array}{l}
\text{clause1}(X) = (a(X) = 0) \wedge (\text{out}(X) = b(X)) \\
\text{clause2}(X) = a(X) = a(\text{rec}(X)) + 1 \wedge \\
\quad b(X) = b(\text{rec}(X)) \wedge \\
\quad \text{out}(X) = f_2(\text{callf}(X)) \wedge \\
\quad f_1(\text{callf}(X)) = \text{out}(\text{rec}(X)) \\
\chi_{\text{iter}_f} = 1 \leq \text{rec} \leq \text{len}(\text{iter}) \wedge 1 \leq \text{callf} \leq \text{len}(f) \wedge \\
\quad \forall X_{\text{iter}_f}. (\text{clause1}(X) \vee \text{clause2}(X))
\end{array}$$

Fig. 7: Expressing $\text{iter}_f(a)(b)$; a -fold application of f to b (Definitions 3.8.1 and 3.8.2)

3.8. Iteration

DEFINITION 3.8.1. Recall that if $f : \mathbb{Z} \rightarrow \mathbb{Z}$ is a function and $a \in \mathbb{N}_{\geq 0}$, then the a -fold iteration

$\text{iter}_f(a) : \mathbb{Z} \rightarrow \mathbb{Z}$ of f is the function that inputs $b \in \mathbb{Z}$ and returns $\overbrace{(f \circ \dots \circ f)}^{a \text{ times}}(b)$.

This can be defined by:

$$\begin{array}{l}
\text{iter}_f(0)(b) = b \\
\text{iter}_f(a+1)(b) = f(\text{iter}_f(a)(b))
\end{array}$$

DEFINITION 3.8.2. Assume a binary propositional constant symbol f , with arity $\text{arity}(f) = 2$, along with a validity predicate χ_f which expresses that $f_3(X) = f(f_1(X), f_2(X))$. We can think of this intuitively as a matrix of columns all having the form $(a, b, f(a, b))$, for various values of a and b . There is no obstacle to having more rows, as we did for instance in Figure 6; we take three just for simplicity.

We now assume a binary propositional constant iter_f with arity $\text{arity}(\text{iter}_f) = 5$. In Figure 7 we use our logic to express that iter_f encodes an a -fold iteration of f on an input value b , such that:

- (1) Row 1 (values of $\text{iter}_{f,1}(X)$) stores the number of iterations a .
- (2) Row 2 (values of $\text{iter}_{f,2}(X)$) stores input values b .
- (3) Row 3 (values of $\text{iter}_{f,3}(X)$) stores result values $\text{iter}_f(a)(b)$.
- (4) Row 4 (values of $\text{iter}_{f,4}(X)$) stores pointers to recursive calls to iter_f , as required in clause 2 of Definition 3.8.1 above.
- (5) Row 5 (values of $\text{iter}_f(5, X)$) stores pointers to calls to f , corresponding to the call to f in clause 2 of Definition 3.8.1 above.

DEFINITION 3.8.3. Suppose $f : \mathbb{Z} \rightarrow \mathbb{Z}$ is a unary function and suppose $n \in \mathbb{N}_{\geq 1}$. Say that a $5 \times n$ matrix M encodes a partial iteration of f when

$$\forall 1 \leq x \leq n. M(3, x) = f^{M(1, x)}(M(2, x)).$$

LEMMA 3.8.4. Suppose ς is a valuation (Definition 2.2.8(1)). Then

- (1) if $\models_{\varsigma} \text{iter}_f \text{Valid}$
- (2) then $\varsigma(\text{iter}_f)$ encodes a partial iteration of f , as per Definition 3.8.3.

Proof. We examine the clauses in Figure 7 and using Theorem 2.3.4 see that they do indeed encode the definition in Definition 3.8.1. The bound checks $1 \leq \text{iter}_4 \leq \text{len}(\text{iter})$ and $1 \leq \text{iter}_5 \leq \text{len}(f)$ exclude out-of-bounds pointer errors. \square

REMARK 3.8.5. We see no inherent barrier to mutual inductive definitions. We just declare several matrix variable symbols and engineer pointers between them for the (mutual) inductive calls.

3.9. SK combinators

DEFINITION 3.9.1.

- (1) **Combinator expressions** are a formal syntax defined by:

$$t ::= S \mid K \mid tt.$$

So a combinator is either an S , a K , or an **application** of a combinator term to a combinator term.

- (2) A big-step evaluation relation for combinators is as follows:¹⁷

$$\frac{}{K \Downarrow K} \text{ (Ki)} \quad \frac{}{S \Downarrow S} \text{ (Si)} \quad \frac{s \Downarrow s'}{(Ks)t \Downarrow s'} \text{ (Ke)} \quad \frac{su \Downarrow s' \quad tu \Downarrow t' \quad s't' \Downarrow v}{((Ss)t)u \Downarrow v} \text{ (Se)}$$

REMARK 3.9.2. Combinator expressions and their evaluation form a Turing-complete model of computation (see [Bim20] or [Bar84, Chapter 7]).¹⁸

So, to specify combinators inside our logic is to build a Turing-complete model of computation inside it, which demonstrates that (at least in principle) it has a certain computational content.

We start with a trick that will make our encoding a little more compact:

DEFINITION 3.9.3.

- (1) The **Cantor pairing function** is a well-known way of bijecting $\mathbb{N}_{\geq 0} \times \mathbb{N}_{\geq 0}$ with $\mathbb{N}_{\geq 0}$. It has the characteristic that it can be expressed as a polynomial over \mathbb{Q} (indeed, this is the only quadratic polynomial pairing function) [Nat15].¹⁹

$$\langle x, y \rangle' = \frac{(x+y) * (x+y+1)}{2} + x = \frac{x^2 + 3 * x + 2 * x * y + y + y^2}{2}. \quad (1)$$

- (2) The pairing function $\langle x, y \rangle'$ above is not quite what we need, because we want to reserve 0 to represent the combinator S and 1 to represent the combinator K . Accordingly, we adjust it with a two-element offset:

$$\langle x, y \rangle = \langle x, y \rangle' + 2.$$

- (3) We then define a *Gödel encoding*²⁰ from the syntax of combinator expressions (Definition 3.9.1(1)) to \mathbb{N} , which is an injection defined by:

$$gdl(S) = 0 \quad gdl(K) = 1 \quad gdl(tt') = \langle gdl(t), gdl(t') \rangle.$$

It is routine to prove that this injection is actually a bijection between combinator expressions and the nonnegative natural numbers $\mathbb{N}_{\geq 0}$.

REMARK 3.9.4. In Equation (1) in Definition 3.9.3, we divide by 2. If we want a polynomial with coefficients over \mathbb{Z} we could just as well use $\langle x, y \rangle' * 2$, and retain integer coefficients throughout. We would lose bijectivity of the pairing function, but in the context of this prototype that would be fine too.

DEFINITION 3.9.5. Assume a matrix variable symbol evl with arity $arity(evl) = 8$, which we can think of as storing an 8-row matrix. In Figure 8, we

- (1) define syntactic sugar,
- (2) use it to express the clauses from Definition 3.9.1, and
- (3) write a validity predicate χ_{SK} to express that evl encodes a partial combinator big-step reduction relation.

¹⁷The ‘i’ in the name (Ki) and (Si) is short for ‘introduction’, and ‘e’ in the name (Ke) and (Se) is short for ‘elimination’. These are just names we gave the rules.

¹⁸A quite clear treatment is available online [here](#) (permalink).

¹⁹Clear and accessible accounts of the Cantor pairing function are also on Wikipedia [here](#) and [here](#).

²⁰By which we mean an injective map from the syntax of a language to natural numbers. Such maps are so called because Gödel used one such in his 1931 paper on incompleteness results.

$$\begin{array}{lll}
& 0 \text{ sugars to } S & 1 \text{ sugars to } K \\
\text{eval}_1(t) \text{ sugars to } & \text{lhs}(t) & \text{eval}_2(t) \text{ sugars to } \text{rhs}(t) & \text{eval}_3(t) \text{ sugars to } s(t) \\
\text{eval}_4(t) \text{ sugars to } & t(t) & \text{eval}_5(t) \text{ sugars to } u(t) & \\
\text{eval}_6(t) \text{ sugars to } & \text{rec1}(t) & \text{eval}_7(t) \text{ sugars to } \text{rec2}(t) & \text{eval}_8(t) \text{ sugars to } \text{rec3}(t)
\end{array}$$

$$\begin{array}{l}
\text{clause1}(X) = \text{lhs}(X) = S \wedge \text{rhs}(X) = S \\
\text{clause2}(X) = \text{lhs}(X) = K \wedge \text{rhs}(X) = K \\
\text{clause3}(X) = \text{lhs}(X) = \langle \langle K, \text{lhs}(s(X)) \rangle, t(X) \rangle \wedge \\
\quad \text{rhs}(X) = \text{rhs}(\text{rec1}(X)) \wedge \\
\quad \text{lhs}(\text{rec1}(X)) = s(X) \\
\text{clause4}(X) = \text{lhs}(X) = \langle \langle \langle S, s(X) \rangle, t(X) \rangle, u(X) \rangle \wedge \\
\quad \text{rhs}(X) = \text{rhs}(\text{rec3}(X)) \wedge \\
\quad \text{lhs}(\text{rec1}(X)) = \langle s(X), t(X) \rangle \wedge \\
\quad \text{lhs}(\text{rec2}(X)) = \langle s(X), u(X) \rangle \wedge \\
\quad \text{lhs}(\text{rec3}(X)) = \langle \text{rhs}(\text{rec1}(X)), \text{rhs}(\text{rec2}(X)) \rangle \\
\chi_{SK} = 1 \leq \text{rec1} \leq \text{len}(\text{evl}) \wedge 1 \leq \text{rec2} \leq \text{len}(\text{evl}) \wedge 1 \leq \text{rec3} \leq \text{len}(\text{evl}) \wedge \\
\quad \bigvee X_{\text{evl}}.(\text{clause1}(X) \vee \text{clause2}(X) \vee \text{clause3}(X) \vee \text{clause4}(X))
\end{array}$$

Fig. 8: Expressing big-step evaluation of combinator expressions (Definitions 3.9.1 and 3.9.5)

REMARK 3.9.6. We read through the rows in evl one by one:

- (1) Row 1 represents the left-hand (unreduced) term.
- (2) Row 2 represents the right-hand (reduced) term.
- (3) Row 3 represents a subterm s of the term on the left-hand side (as required in rules (\mathbf{Ke}) and (\mathbf{Se})).
- (4) Row 4 represents a subterm t (as required in rules (\mathbf{Ke}) and (\mathbf{Se})).
- (5) Row 5 represents a subterm u (as required in rule (\mathbf{Se})).
- (6) Rows 6, 7, and 8 represent pointers to proof-obligations above the line (= inductive function-calls) as may be required: none are required for (\mathbf{Ki}) and (\mathbf{Si}) ; one is required for (\mathbf{Ke}) ; and three are required for (\mathbf{Se}) .

DEFINITION 3.9.7. Suppose $n \in \mathbb{N}_{\geq 1}$. Say that an $8 \times n$ matrix M **encodes a partial big-step combinator reduction relation** when

$$\forall 1 \leq x \leq n. \text{gdl}^{-1}(M(1, x)) \Downarrow \text{gdl}^{-1}(M(2, x)).$$

Above, the \Downarrow relation is defined in Definition 3.9.1. The gdl function bijects combinator expressions with nonnegative natural numbers (Definition 3.9.3(3)).

LEMMA 3.9.8. *Suppose ς is a valuation (Definition 2.2.8(1)). Then*

- (1) *if $\models_{\varsigma} \chi_{SK}$,*
- (2) *then $\varsigma(\text{evl})$ encodes a partial combinator big-step reduction relation, as per Definition 3.9.7.*

Proof. We examine the clauses in Figure 8 and using Theorem 2.3.4 see that they do indeed encode the definition in Definition 3.9.1. This is a little bit fiddly because of the Gödel encoding of the combinator syntax, and the fact that there are four derivation rules, and one of them has three subderivations, so it is a challenge to not make a typo or miss out a bit of a rule — but conceptually this is a straightforward translation from one logical syntax into another. \square

REMARK 3.9.9. We use the Cantor pairing function to build a simple Gödel encoding that packs a combinator term into a single number. We might prefer instead to store a combinator term as a list or

as a tree of symbols. This is possible: it would add complexity to the representation above and thus to its validity predicate, but would not change the fundamental nature of what we are doing.

4. FROM RANGE CHECK TO GENERAL RECURSION

4.1. Simple functions obtainable directly from the range check

REMARK 4.1.1. A curious feature of our syntax in Figure 1 is the range check predicate $<$.

Our logic does not have a ‘runtime’ and a ‘compile time’ as such, but $<$ has the flavour of a static ‘compile time’ check, in the sense that (for example) given ς , $\llbracket vft < C_i \rrbracket_{\varsigma}$ computes a number rather than a polynomial in X .²¹

In the examples above, we use range check to check for out-of-bounds pointer errors (see Examples 3.2.8 and 3.2.9 for concrete examples). But what is the range check really doing, and what is its full power? In this section we will examine this operator in more detail. We start by using the range check to specify some simple predicates and functions:

4.1.1. Strictly positive numbers pos

DEFINITION 4.1.2 (Strictly positive numbers). Assume a matrix variable symbol pos with $\text{arity}(\text{pos}) = 1$ and validity predicate

$$\chi_{\text{pos}}(X) = (0 < \text{pos}_1).$$

LEMMA 4.1.3. Suppose ς is a valuation. Then the following are equivalent:

- (1) $\models_{\varsigma} \chi_{\text{pos}}$.
- (2) $\varsigma(\text{pos})$ is a 1-row matrix (i.e. a vector) of strictly positive integers in $\mathbb{N}_{>0}$.

Proof. This is exactly what the clause for range check in Figure 2 expresses, for $0 < \text{pos}_1$. \square

REMARK 4.1.4. We introduce a strict inequality $<$ rather than an inequality \leq in Figure 1 so that we can express being *strictly positive* pos . If we took \leq as primitive, it would be harder to express pos . This is because our logic does not include negation as primitive, so we cannot just express pos from a combination of \leq and \neq .

4.1.2. The sign function sign and its corollary functions

DEFINITION 4.1.5. We define functions

$$\begin{aligned} \text{sign} &: \mathbb{Q} \rightarrow \{-1, 0, 1\} \subseteq \mathbb{Q} \\ \text{isZero} &: \mathbb{Q} \rightarrow \{0, 1\} \\ \text{neg} &: \mathbb{Q} \rightarrow \{0, 1\} \\ \text{neq} &: (\mathbb{Q} \times \mathbb{Q}) \rightarrow \{0, 1\} \\ \text{leq} &: (\mathbb{Q} \times \mathbb{Q}) \rightarrow \{0, 1\} \end{aligned}$$

such that for $a, b \in \mathbb{Q}$:

$$\begin{aligned} \text{sign}(a) &= \begin{cases} 0 & \text{if } a = 0 \\ 1 & \text{if } a > 0 \\ -1 & \text{if } a < 0 \end{cases} \\ \text{isZero}(a) &= \begin{cases} 0 & \text{if } a = 0 \\ 1 & \text{if } a \neq 0 \end{cases} & \text{neg}(a) = \begin{cases} 1 & \text{if } a = 0 \\ 0 & \text{if } a \neq 0 \end{cases} \\ \text{neq}(a, b) &= \begin{cases} 0 & \text{if } a \neq b \\ 1 & \text{if } a = b \end{cases} & \text{leq}(a, b) = \begin{cases} 0 & \text{if } a \leq b \\ 1 & \text{if } a \not\leq b \end{cases} \end{aligned}$$

²¹So we can still say that our semantics is polynomial, even though $vft < C_i$ does not *look* like a polynomial expression: it evaluates to a number $\llbracket vft < C_i \rrbracket_{\varsigma}$, and a number is trivially a polynomial.

REMARK 4.1.6. $sign$ is the fundamental entity in Definition 4.1.5, and $isZero$, neg , neq , and leq follow from it, in the sense that we can derive everything using polynomial expressions using $sign$:

$$\begin{aligned} isZero(a) &= sign(a)^2 \\ neg(a) &= 1 - isZero(a) = 1 - sign(a)^2 \\ neq(a, b) &= neg(a - b) = 1 - (sign(a - b))^2 \\ leq(a, b) &= sign(a - b) * (sign(a - b) - 1)/2 \end{aligned}$$

We can use pos from Definition 4.1.2 to express $sign$, $isZero$, neg , neq , and leq from Definition 4.1.5:

DEFINITION 4.1.7. Assume matrix variable symbols $sign$, $isZero$, and neg all with arity 3, so that $arity(sign) = arity(isZero) = arity(neg) = 3$.

- (1) Sugar $sign_1(X)$ to $sign.in(X)$. This stores an input value a .
- (2) Sugar $sign_2(X)$ to $sign.out(X)$. This stores the corresponding output value $sign(a)$.
- (3) Sugar $sign_3(X)$ to $sign.ptr(X)$. This stores a pointer to a column in pos_1 (Definition 4.1.2) holding the absolute value of a .

We use similar sugar for $isZero$ and neg . The validity predicates for $sign$, $isZero$ and neg (using pos) are:

$$\begin{aligned} \chi_{sign}(X) &= 1 \leq sign.ptr \leq len(pos) \wedge \\ &\quad \forall X. ((sign.in(X) = 0 \wedge sign.out(X) = 0) \vee \\ &\quad (sign.in(X) = pos_1(sign.ptr(X)) \wedge sign.out(X) = 1) \vee \\ &\quad (((-1) * sign.in(X)) = pos_1(sign.ptr(X)) \wedge sign.out(X) = -1)) \\ \chi_{isZero}(X) &= 1 \leq isZero.ptr \leq len(pos) \wedge \\ &\quad \forall X. ((isZero.in(X) = 0 \wedge isZero.out(X) = 0) \vee \\ &\quad (isZero.in(X)^2 = pos_1(isZero.ptr(X)) \wedge isZero.out(X) = 1)) \\ \chi_{neg}(X) &= 1 \leq neg.ptr \leq len(pos) \wedge \\ &\quad \forall X. ((neg.in(X) = 0 \wedge neg.out(X) = 1) \vee \\ &\quad (neg.in(X)^2 = pos_1(neg.ptr(X)) \wedge neg.out(X) = 0)) \end{aligned}$$

Assume matrix variable symbols neq , and leq with arity 4, so that $arity(neq) = arity(leq) = 4$.

- (1) Sugar $neq_1(X)$ to $neq.a(X)$. This stores an input value a .
- (2) Sugar $neq_2(X)$ to $neq.b(X)$. This stores an input value b .
- (3) Sugar $neq_3(X)$ to $neq.out(X)$. This stores the corresponding output value $neq(a, b)$.
- (4) Sugar $neq_4(X)$ to $neq.ptr(X)$. This stores a pointer to a column in pos_1 (Definition 4.1.2).

We use similar sugar for leq . The validity predicates for neq and leq (using pos) are:

$$\begin{aligned} \chi_{neq}(X) &= 1 \leq neq.ptr \leq len(pos) \wedge \\ &\quad \forall X. (((neq.a(X) - neq.b(X)) = 0 \wedge neq.out(X) = 1) \vee \\ &\quad ((neq.a(X) - neq.b(X))^2 = pos_1(neq.ptr(X)) \wedge neq.out(X) = 0)) \\ \chi_{leq}(X) &= 1 \leq leq.ptr \leq len(pos) \wedge \\ &\quad \forall X. (((leq.a(X) - leq.b(X)) = 0 \wedge leq.out(X) = 0) \vee \\ &\quad ((leq.b(X) - leq.a(X)) = pos_1(leq.ptr(X)) \wedge leq.out(X) = 0) \vee \\ &\quad ((leq.a(X) - leq.b(X)) = pos_1(leq.ptr(X)) \wedge leq.out(X) = 1)) \end{aligned}$$

LEMMA 4.1.8. Suppose $f \in \{sign, isZero, neg, neq, leq\}$ is one of the functions from Definition 4.1.5. Then the corresponding validity predicate χ_f from Definition 4.1.7 correctly encodes f , in the formal sense that — taking the corresponding $f \in \{sign, isZero, neg, neq, leq\}$ — for every valuation ς , if

$\models_{\zeta} \chi_{\text{pos}}$ (Definition 4.1.2) and $\models_{\zeta} \chi_f$ then for every $1 \leq (x \in \mathbb{N}_{\geq 1}) \leq \text{len}(\zeta(f))$ we have

$$\begin{aligned} \zeta(\text{sign})(2, x) &= \text{sign}(\zeta(\text{sign})(1, x)) \\ \zeta(\text{isZero})(2, x) &= \text{isZero}(\zeta(\text{isZero})(1, x)) \\ \zeta(\text{neg})(2, x) &= \text{neg}(\zeta(\text{neg})(1, x)) \\ \zeta(\text{neq})(3, x) &= \text{neq}(\zeta(\text{neq})(1, x), \zeta(\text{neq})(2, x)) \\ \zeta(\text{leq})(3, x) &= \text{leq}(\zeta(\text{leq})(1, x), \zeta(\text{leq})(2, x)) \end{aligned}$$

Proof. The functions in Definition 4.1.5 are simple enough, and so are the validity predicates in Definition 4.1.7. We check the correspondence and use Theorem 2.3.4. \square

REMARK 4.1.9.

- (1) There is some apparent overlap between *isZero* from Definition 4.1.5 and the ‘is equal to zero’ function $\lambda x. [X = 0](x) = \lambda x. x^2$ which we can build from the syntax and denotation in Figures 1 and 2.

But these are different functions: with *isZero*, the output is either 0 or 1, whereas with $\lambda x. x^2$ the output may be any nonnegative value. For example:

$$\text{isZero}(2) = 1 \quad \text{whereas} \quad [2 = 0] = 4,$$

as per Figure 2.

Knowing that the output is precisely equal to 0 or 1 is a stronger property, and it matters because (for example) we can then easily get the ‘negation’ predicate (mapping 0 to 1 and any nonzero value to 0) as $1 - \text{isZero}(a)$. We cannot do this with $\lambda x. [X = 0](x) = \lambda x. x^2$.

- (2) *neg* is short for *negation*. It maps 0 (corresponding to ‘truth’) to 1, and any nonzero value (corresponding to ‘false’) to 0.

This is a negation, and we have:

- (a) We have *excluded middle*, since $a * \text{neg}(a) = 0$ for every $a \in \mathbb{Q}$.²²
- (b) $a + \text{neg}(a) \neq 0$ for every $a \in \mathbb{Q}$. This equals a if $a \neq 0$, and equals 1 if $a = 0$.²³
- (c) Double negation $\text{neg}(\text{neg}(a))$ maps 0 to 0 and any nonzero element to 1.

If we think of a truth-value $a \in \mathbb{Q}_{\geq 0}$ as being either 0 or a nonzero ‘error value’, then $\text{neg}(\text{neg}(a))$ throws away the precise error value and just retains the information whether a was true or false.

neg is not unique with the properties above; e.g. *neg'* mapping a to $2 * \text{neg}(a)$ has similar properties. This is as expected and it just reflects that there are many false (= nonzero) values.

4.1.3. Back from pos to the range check

REMARK 4.1.10 (From *pos* back to $<$). We saw above how, in the presence of the rest of the logic, the range checks $vft < t$ and $t < vft$ can express predicates for strictly positive numbers (Definition 4.1.2), and a *sign* function (Definition 4.1.5).

We take a moment to note that we can also go in the other direction, from *pos* to range checks. Let us for this Remark take a *pos* predicate as primitive in our syntax.

We can express the range check

$$vft < C_i \quad \text{as} \quad \forall X_C. \text{pos}(C_i(X) - vft),$$

and

$$C_i < vft \quad \text{as} \quad \forall X_C. \text{pos}(vft - C_i(X)).$$

Thus in some sense, $<$, *pos*, and also *sign* are all doing the same thing.

²²Excluded middle is the property ‘ ϕ -or-not- ϕ ’, which when filtered through our polynomial semantics from Figure 2 renders as $a * \text{neg}(a) = 0$.

²³This corresponds to ‘ ϕ -and-not- ϕ ’, which we expect to be false, i.e. nonzero.

REMARK 4.1.11. Continuing Remark 4.1.10, \lt is also clearly *different* from pos and $sign$, because $vft \lt C_i$ is variable-free and so has the flavour of a static check on the valuation ς , whereas $pos(x)$ and $sign(x)$ have the flavour of being predicates.

Yet, in the context of the rest of the system they have the same expressivity. What deeper structure is being manifested here?

We propose that at a deeper level, what these constructs do is introduce the power of a *knowledge modality*. This is a modal operator that internalises some notion of necessitation, truth, knowledge, or provability inside the logic itself [Ver24] — that something is not just true or false, but also known to be such, proved to be such, or necessarily such, etc.

A feature of the notion of ‘false’ in our logic is that it is modelled by being nonzero, and there are infinitely many nonzero numbers so we cannot explicitly enumerate over all the possible ways of being false within our logic.²⁴ What is significant about \lt , pos , and $sign$ is not so much what they detect (though this is of course relevant); what really matters is that they map the truth or falsity of what they measure, to a finite set of values ($\{0, 1\}$ or $\{0, 1, -1\}$), and this is where the effect of a knowledge modality comes from.

What makes \lt special, and better to use as a primitive than pos and $sign$, is that it still has a polynomial semantics — because (as we mentioned in Remark 4.1.1) $\llbracket vft \lt C_i \rrbracket_{\varsigma}$ returns a number, which is a polynomial. More on this in future work.

4.2. Representing arbitrary functions in terms

REMARK 4.2.1. The term language in Figure 1 does not accommodate arbitrary functions $f : \mathbb{Q}^n \rightarrow \mathbb{Q}$. This is by design, since Figure 2 presents a *polynomial* semantics. But it would be nice if we could have our cake and eat it too: use arbitrary functions in our terms, and *still* get the goodness of polynomial expressions.

This is impossible in the general case, but we do not need the general case: a simple polynomial approximation is sufficient. So, we will use a matrix variable symbol f , with a validity predicate that expresses that f approximates the desired function f .

4.2.1. Enriched syntax and its compilation back down to unenriched syntax. Suppose we have the following scenario:

- (1) Suppose we have some function $f : \mathbb{Q} \rightarrow \mathbb{Q}$. (For simplicity we assume f is unary, but functions with more arguments can also be accommodated.)
- (2) Suppose we have represented f in our logic with
 - a matrix variable symbol f of arity $arity(f) \geq 2$, and
 - a validity predicate χ_f , such that f_1 represents input values, and f_2 represents output values. In symbols, we write:

$$\models_{\varsigma} \chi_f \implies \forall l \leq (x \in \mathbb{N}_{\geq 0}) \leq len(\varsigma(f)). f_2(x) = f(f_1(x)).$$

Other rows may be present in f (see for example our implementation of $sign$ in Definition 4.1.7, which requires a third row of data). This is fine and will not affect our reasoning.

- (3) Suppose we have another matrix variable symbol P with $arity(P)$, and suppose P has a validity predicate χ_P that uses an **f -enriched term syntax**, which enriches the syntax from Figure 1 such that if t is an f -enriched term then $f(t)$ is an f -enriched term.

There are no other restrictions on χ_P ; it is just some predicate, but in the f -enriched syntax.

The denotation of the f -enriched term $f(t)$, is f applied to the denotation of t . That is, we enrich Figure 2 with a clause

$$\llbracket f(t) \rrbracket_{\varsigma}(x) = f(\llbracket t \rrbracket_{\varsigma}(x)). \quad (2)$$

²⁴This would not be solved by switching to a finite field. In practical terms, any finite field large enough to be cryptographically secure is also large enough to be practically unenumerable. This is why cryptographic assurances work! Thus, modelling ‘true’ by a single value and ‘false’ by many values is not a bug of our semantics. Far from it; it reflects an essential feature of the cryptographic notion of proof.

We want to transform χ_P into another validity predicate χ'_P such that

- χ'_P means the same thing as χ_P (in a sense we will make formal in Definition 4.2.3 and Proposition 4.2.4) but
- χ'_P is in the unenriched syntax, and it will replace calls to f with uses of the matrix variable symbol f .

Specifically, in Definition 4.2.3 we will show how to transform

- a matrix variable symbol P with arity $\text{arity}(P)$ and validity predicate χ_P that contains some innermost instance of $f(t)$,²⁵ into
- a matrix variable symbol P' with arity $\text{arity}(P') = \text{arity}(P)+1$ and a validity predicate $\chi_{P'}$ that does not contain this instance of $f(t)$.

Since syntax is finite, we can eliminate all instances of f from our validity predicate by repeating this transformation.

REMARK 4.2.2. The idea that motivates the transformation outlined in Definition 4.2.3 is straightforward: we replace calls to f with pointers to a matrix that stores a polynomial approximation with the same values at those calls. A polynomial can approximate a function at any finite number of points, so we know this can work; the rest is just a matter of getting the technicalities right.

DEFINITION 4.2.3 (The transformation). For clarity, we sugar $f_1(t)$ to $f.\text{in}(t)$ and $f_2(t)$ to $f.\text{out}(t)$.

- (1) Looking at the syntax in Figure 1, we see that the instance of $f(t)$ must occur inside some subpredicate $\phi = (t' = t'')$ where $f(t)$ appears in t' or t'' . We obtain $\chi_{P'}$ from χ_P by replacing ϕ in χ_P with

$$(f.\text{in}(P'_{n+1}(X)) = t) \wedge \phi[f(t) \mapsto f.\text{out}(P'_{n+1}(X))],$$

where $\phi[f(t) \mapsto f.\text{out}(P'_{n+1}(X))]$ denotes the predicate obtained by replacing any instances of $f(t)$ in ϕ (of which we assumed there is at least one) with $f.\text{out}(P'_{n+1}(X))$.

Using Theorem 2.3.4 we see that $\chi_{P'}$ expresses (subject to range checks which we will add below) that the value in $P'_{n+1}(X)$ points to a column in f that contains $(t, f(t))$.

- (2) We repeat the transformation above until there are no mentions of f left. The result is a matrix variable symbol $P^{(n)}$ of arity $\text{arity}(P^{(n)}) = \text{arity}(P) + n$ (where n is the number of times we applied our transformation), and with validity predicate $\chi_{P^{(n)}}$.
- (3) Now we just need to wrap this in range checks to prevent out-of-bounds pointer errors in the extra rows, so we set:

$$\chi'_P = \chi_{P^{(n)}} \wedge (1 \leq P^{(n)}_{i+1} \leq \text{len}(f)) \wedge \dots \wedge (1 \leq P^{(n)}_{i+n} \leq \text{len}(f)).$$

PROPOSITION 4.2.4. Suppose ς is a valuation such that $\models_{\varsigma} \chi_f$. Then the following are equivalent:

- (1) $\models_{\varsigma} \chi_P$ in the enriched syntax and denotation described above — in which $f(t)$ is a term if t is a term, and $\llbracket f(t) \rrbracket_{\varsigma}(x) = f(\llbracket t \rrbracket_{\varsigma}(x))$ as per equation 2.
- (2) $\models_{\varsigma} \chi'_P$ in the unenriched semantics.

Proof. The argument is already in Definition 4.2.3: calls to f are replaced by range-checked pointers to columns in f . We have assumed that $\models_{\varsigma} \chi_f$, and we have assumed that this implies that $\varsigma(f)_2(x) = f(\varsigma(f)_1(x))$ for every column x in $\varsigma(f)$, so that a call to f acting on some term in the enriched syntax is precisely equivalent to a lookup in $\varsigma(f)$ of a column starting with the denotation of that term. \square

REMARK 4.2.5. Each step of the transformation in Definition 4.2.3(1) removes one innermost instance of $f(t)$ and increases the arity of the matrix variable symbol by 1 (modulo optimisations, e.g if $f(t)$ appears more than once). So we can say that, roughly speaking,

²⁵‘Innermost’ means that t does not itself contain some subterm of the form $f(t')$.

each function-call to f costs an extra row in the validity matrix.

This is fine because it does not affect the width of the matrices, and so does not increase the degree of the interpolating polynomials.²⁶

Overall, the process described above is not much different from what we do in ordinary mathematics, when we define e.g. Kuratowski pairs as $(x, y) = \{\{x\}, \{x, y\}\}$ and then proceed to use (x, y) directly; or when we define and use functions in any modern programming language and expect that these may be inlined, optimised, or compiled to a lower-level representation. In principle everything will get unwound to some more primitive syntax, so we know we are safe, but the higher-level syntax is more usable.

In a practical implementation of this logic, we might expect to provide functions as primitive, and let the compiler translate them to a lower-level representation as described above.

4.2.2. A simple application: predicate complementation. We conclude by bringing together what we have learned, to express negation as a macro. In more technical terminology: we will show that our logic is *complemented*.

Recall from Figures 1 and 2 that our syntax and semantics do not include predicate negation $\neg\phi$. This is because we have one ‘true’ truth-value 0, and many ‘false’ truth-values $\mathbb{Q} \setminus \{0\}$.

However, in the presence of the rest of the logic, the *range-check* $<$ allows us to implement negation as a macro operation. We saw a strong indication of this in *neg* and *neg* from Subsection 4.1.2. This negation acts at the level of terms, but using the ideas in this Subsection we can fold it back into our logic.

Notation 4.2.6 and Theorem 4.2.7 are written in a syntax enriched with *neg*, as per Subsection 4.2.1:

NOTATION 4.2.6. Suppose ϕ is a predicate. Then we define $\sim\phi$ the **complement** of ϕ by²⁷

$$\sim\phi \quad \text{as shorthand for} \quad \text{neg}(\text{term}(\phi)) = 0.$$

As described in Proposition 4.2.4, $\sim\phi$ can be compiled down to an unenriched syntax that assumes a matrix variable symbol *neg* of arity 3 and a validity predicate χ_{neg} as per Definition 4.1.7. This licenses us to work directly with *neg*-enriched syntax, and we obtain Theorem 4.2.7:

THEOREM 4.2.7. *Suppose ζ is a valuation and $x \in \mathbb{Q}$ and ϕ is a predicate. Then*

$$x \models_{\zeta} \sim\phi \quad \text{if and only if} \quad x \not\models_{\zeta} \phi.$$

Proof. By chasing definitions. The fact that this works is in itself a mathematical result that needs to be checked, so we give full details:

$$\begin{array}{ll} x \models_{\zeta} \sim\phi \iff [\sim\phi]_{\zeta}(x) = 0 & \text{Figure 2} \\ \iff [\text{neg}(\text{term}(\phi)) = 0]_{\zeta}(x) = 0 & \text{Notation 4.2.6} \\ \iff ([\text{neg}(\text{term}(\phi))]_{\zeta} - 0)^2(x) = 0 & \text{Figure 2} \\ \iff [\text{neg}(\text{term}(\phi))]_{\zeta}(x) = 0 & \text{Fact} \\ \iff \text{neg}([\text{term}(\phi)]_{\zeta}(x)) = 0 & \text{Equation 2 from Subsection 4.2.1} \\ \iff [\text{term}(\phi)]_{\zeta}(x) \neq 0 & \text{Definition 4.1.5} \\ \iff [\phi]_{\zeta}(x) \neq 0 & \text{Figure 2} \\ \iff x \not\models_{\zeta} \phi & \text{Figure 2} \end{array}$$

The use of equation 2 from Subsection 4.2.1 above (i.e. treating *neg* as a primitive in our syntax) is justified by Proposition 4.2.4. \square

Theorems 4.2.7 and 2.3.4 taken together prove that our logic can translate the expressive power of full first-order logic — at least! — into polynomials. Each step in the path to this is arguably quite elementary, but the overall result seems surprising and powerful.

²⁶Tall but narrow matrices are cheap; see Remark 6.2.1(2). Thus, a transformation that increases arity (and thus the height of matrices) but does not affect their width, is free, to a first approximation.

²⁷This implementation is not necessarily optimal, but it is simple.

4.3. General recursion

We show how to implement a minimisation function, in the sense of general recursive functions [DN24], in our logic.²⁸ The construction below is rather fiddly, but this is worth doing because it demonstrates that our logic is expressive enough to verify general recursion.

We already had a hint of this, because we specified Ackermann's function in Subsection 3.5 and this is known as a canonical function that is recursive but not primitive recursive. But here, we isolate and implement a minimisation function in and of itself.

Recall the (standard) definition:

DEFINITION 4.3.1. Suppose $f : (\mathbb{N}_{\geq 1} \times \mathbb{N}^k) \rightarrow \mathbb{N}$.²⁹ Then define the **minimisation** $\mu(f) : \mathbb{N}^k \rightarrow \mathbb{N}$ by

$$\mu(f)(x_1, \dots, x_k) = \min\{n \in \mathbb{N}_{\geq 1} \mid f(n, x_1, \dots, x_k) = 0\}.$$

In words: $\mu(f)$ maps $(x_1, \dots, x_k) \in \mathbb{N}^k$ to the least zero value of $\lambda n.f(n, x_1, \dots, x_k)$.

If no such zero value exists, so that $\{n \in \mathbb{N}_{\geq 1} \mid f(n, x_1, \dots, x_k) = 0\} = \emptyset$, then the value of $\mu(f)(x_1, \dots, x_k)$ is not specified.³⁰

REMARK 4.3.2. We can think of μ as generalising a *while* loop: given some parameters $(x_1, \dots, x_k) \in \mathbb{N}^k$, μ searches $n \in \mathbb{N}_{\geq 1}$ and continues while the value of $f(n, x_1, \dots, x_k) \neq 0$. If $f(n, x_1, \dots, x_k) = 0$, then μ stops and returns that (first) value of n .

Now we give a slightly lower-level version of Definition 4.3.1. It is still abstract, but it is closer to what we would implement in our logic.

NOTATION 4.3.3. Suppose $U \subseteq \mathbb{N}_{\geq 1} \times \mathbb{N}$ and $b \in \mathbb{N}$. Then define

$$U_b = \{a \in \mathbb{N}_{\geq 1} \mid (a, b) \in U\}.$$

If U_b is an initial segment of $\mathbb{N}_{\geq 1}$ for every $b \in \mathbb{N}$, then call U **initial in its first component**.³¹

Recall that *sign* is defined in Definition 4.1.5, and note that if we restrict *sign* to \mathbb{N} then it maps to $\{0, 1\}$.

DEFINITION 4.3.4. Assume we are given the following data:

- (1) A function $f : (\mathbb{N}_{\geq 1} \times \mathbb{N}) \rightarrow \mathbb{N}$ (i.e. for simplicity we have set $k = 1$ in Definition 4.3.1).
- (2) A $U \subseteq \mathbb{N}_{\geq 1} \times \mathbb{N}$ that is initial in its first component (Notation 4.3.3).

We specify a pair of functions

$$\min_{f,U}, \text{acc}_{f,U} : U \rightarrow \mathbb{N}$$

($\min_{f,U}$ is short for 'minimal point' and $\text{acc}_{f,U}$ is short for 'accumulator') as follows:

$$\begin{aligned} 1 &\leq \min_{f,U}(a, b) \\ a \neq 1 &\implies \min_{f,U}(a, b) = \min_{f,U}(a-1, b) \\ &\quad f(\min_{f,U}(a, b), b) \leq f(a, b) \\ a = 1 &\implies \text{acc}_{f,U}(a, b) = \text{sign}(f(a, b) - f(\min_{f,U}(a, b), b)) \\ a \neq 1 &\implies \text{acc}_{f,U}(a, b) = \text{acc}_{f,U}(a-1, b) * \text{sign}(f(a, b) - f(\min_{f,U}(a, b), b)) \\ \min_{f,U}(a, b) \neq 1 &\implies \text{acc}_{f,U}(\min_{f,U}(a, b) - 1, b) = 1 \end{aligned}$$

We make sense of Definition 4.3.4 using a lemma:

LEMMA 4.3.5. Continuing the notation of Definition 4.3.4, we have the following properties, for each $b \in \mathbb{N}$:

²⁸The [Wikipedia page](#) gives a clear and accessible presentation.

²⁹We starting counting at 1 in the first argument for the technical reason that we will be most interested in applying this to identify columns in matrices, which we count from 1, not 0. There is no deeper mathematical significance here.

³⁰... or undefined.

³¹An initial segment $I \subseteq \mathbb{N}_{\geq 1}$ is such that if $a \in I$ and $a > 1$ then $a - 1 \in I$. Note that $\emptyset \subseteq \mathbb{N}_{\geq 1}$ is initial.

$$\begin{array}{ll}
\mu_{f,1}(X) \text{ sugars to ptr.min}(X) & \mu_{f,2}(X) \text{ sugars to ptr.f}(X) \\
f_1(\text{ptr.f}(X)) \text{ sugars to a}(X) & \\
f_2(\text{ptr.f}(X)) \text{ sugars to b}(X) & f_3(\text{ptr.f}(X)) \text{ sugars to f.ab}(X) \\
\mu_{f,3}(X) \text{ sugars to acc}(X) & \mu_{f,4}(X) \text{ sugars to ptr.prev}(X)
\end{array}$$

Write $t \neq t'$ as shorthand for $\text{sign}(t - t')^2 = 1$. Write $t \leq t'$ as shorthand for $\text{sign}(t - t' + 1) = 1$.

$$\begin{aligned}
\chi_{\mu_f} = & 1 \leq \text{ptr.min} \leq \text{len}(\text{mu}) \wedge 1 \leq \text{ptr.prev} \leq \text{len}(\text{mu}) \wedge 1 \leq \text{ptr.f} \leq \text{len}(f) \wedge \\
& \forall X_{\text{mu}} \cdot \left(\begin{aligned}
& (\text{a}(X) = 1 \vee \text{ptr.min}(\text{ptr.prev}(X)) = \text{ptr.min}(X)) \wedge \\
& (\text{f.ab}(\text{ptr.min}(X)) \leq \text{f.ab}(X)) \wedge \\
& \left(\begin{aligned}
& (\text{a}(X) = 1 \wedge \text{acc}(X) = \text{sign}(\text{f.ab}(X) - \text{f.ab}(\text{ptr.min}(X)))) \vee \\
& (\text{a}(X) \neq 1 \wedge \text{acc}(X) = \text{sign}(\text{f.ab}(X) - \text{f.ab}(\text{ptr.min}(X))) * \text{acc}(\text{ptr.prev}(X))) \right) \wedge \\
& (\text{a}(\text{ptr.min}(X)) = 1 \vee \text{acc}(\text{ptr.prev}(X)) = 1) \wedge \\
& (\text{a}(X) = 1 \vee \text{a}(\text{ptr.prev}(X)) = \text{a}(X) - 1) \wedge \\
& (\text{a}(X) = 1 \vee \text{b}(\text{ptr.prev}(X)) = \text{b}(X))
\end{aligned} \right)
\end{aligned} \right) \wedge
\end{aligned}$$

Fig. 9: Expressing a minimisation operator μ (Definition 4.3.6)

- (1) $\lambda a \in U_b. \min_{f,U}(a, b)$ is a constant function into $\mathbb{N}_{\geq 1}$.
Write $\mu_{f,U}(b) \in \mathbb{N}_{\geq 1}$ for its unique value.
- (2) $f(\mu_{f,U}(b), b) \leq f(a, b)$ for every $a \in U_b$.
Thus, $f(\mu_{f,U}(b), b)$ is a lower bound for $\{f(a, b) \mid a \in U_b\}$.
- (3) Furthermore, since $f(\mu_{f,U}(b), b) = f(\mu_{f,U}(b), b)$, also $f(\mu_{f,U}(b))$ is a greatest lower bound for this set. Thus,
 $\mu_{f,U}(b) \in \mathbb{N}_{\geq 1}$ is the index of a minimal value of $\lambda a \in U_b. f(n, b)$.
- (4) $\text{acc}_{f,U}(a, b) = \prod_{1 \leq i \leq a} \text{sign}(f(a, b) - \mu_{f,U}(b))$.
We observe by arithmetic that this is equal to 1 or 0, and furthermore $\text{acc}_{f,U}(a, b)$ is equal to 0 precisely when $f(i, b) = f(\mu_{f,U}(b), b)$ for some $1 \leq i \leq a$.
- (5) If $\mu_{f,U}(b) > 1$ then $\text{acc}_{f,U}(\mu_{f,U}(b) - 1, b) = 1$, which means that $\mu_{f,U}(b) \prec f(a, b)$ for every $1 \leq a < \mu_{f,U}(b)$. Thus,
 $\mu_{f,U}(b)$ is also least such that $\lambda a \in U_b. f(a, b)$ attains its minimal value.

Proof. Direct from the construction, as described in the statement of this Lemma. \square

We can now implement minimisation in our logic, just by translating Definition 4.3.4 into it:

DEFINITION 4.3.6. Suppose we are given a matrix variable symbol f whose first three rows $f_1, f_2,$ and f_3 are intended to represent the two input values a and b and one output value $f(a, b)$ of a binary function $f : (\mathbb{N}_{\geq 1} \times \mathbb{N}) \rightarrow \mathbb{N}$.

Assume a matrix variable symbol mu_f with arity $\text{arity}(\text{mu}) = 4$. In Figure 9, we define syntactic sugar and use it to express a validity predicate χ_{μ_f} , based on Definition 4.3.4.

REMARK 4.3.7. Note that in the predicate χ_{μ_f} in Figure 9 we use the function sign from Definition 4.1.5 directly in terms (its implementation in matrix semantics is in Definition 4.1.7). We can do this because as per Subsection 4.2, the function call can be compiled down to the purely polynomial term language. This is not a problem and our presentation in Figure 9 is more readable as a result.

A simple and natural bit of terminology will be useful in a moment:

DEFINITION 4.3.8. Suppose that:

- (1) $f : (\mathbb{N}_{\geq 1} \times \mathbb{N}) \rightarrow \mathbb{N}$.
- (2) f with arity $\text{arity}(f) \geq 3$ is a matrix variable symbol.
- (3) ς is a valuation.

Then say that ς **interprets** f when

$$\varsigma(f)_3(x) = f(\varsigma(f)_1(x), \varsigma(f)_2(x))$$

for every $1 \leq (x \in \mathbb{N}_{\geq 1}) \leq \text{len}(\varsigma(f))$.

PROPOSITION 4.3.9. *Suppose $f : (\mathbb{N}_{\geq 1} \times \mathbb{N}) \rightarrow \mathbb{N}$, and f with arity $\text{arity}(f) \geq 3$ is a matrix variable symbol, and suppose ς is a valuation that interprets f (Definition 4.3.8). Then*

- if $\models_{\varsigma} \chi_{\mu_f}$ (Figure 9),
- then $\llbracket \text{ptr.min}(X) \rrbracket_{\varsigma}(x) = \mu_{f,U}(\llbracket b(X) \rrbracket_{\varsigma}(x))$,

where we define $U \subseteq \mathbb{N}_{\geq 1} \times \mathbb{N}$ by

$$U = \{ \llbracket a(X) \rrbracket_{\varsigma}(x), \llbracket b(X) \rrbracket_{\varsigma}(x) \mid 1 \leq (x \in \mathbb{N}_{\geq 1}) \leq \text{len}(\varsigma(\text{mu})) \}$$

(this is just a fancy way of setting U to be the set of (a, b) pairs over which $\varsigma(\text{mu})$ minimises).

Proof. We examine Figure 9 and see through careful inspection that it translates Definition 4.3.4 into our logic: $\varsigma(\text{mu}_f)_1$ corresponds to $\text{min}_{f,U}$ and $\varsigma(\text{mu}_f)_2$ corresponds to $\text{acc}_{f,U}$. \square

REMARK 4.3.10. Proposition 4.3.9 does not describe the full minimisation outlined in Definition 4.3.1, but this is only because minimisation implicitly contains an unbounded search, whereas a valuation ς delivers *finite* matrices.

This is reasonable, because it is the prover’s job to run a program and — after a finite time — it must stop and present the ς it has generated. If the prover does not terminate then no ς is generated and there is nothing for the verifier to do. So in our context, minimisation means finding minimal values within a given run.

Thus Proposition 4.3.9 does the reasonable thing, which is to tell us that if we are given some $b \in \mathbb{N}$ and $a \in \mathbb{N}_{\geq 1}$ and a ς that satisfies the validation predicates and for which $\varsigma(\text{mu})$ contains a column which references $f(a, b)$ — if all the above holds, then $\varsigma(\text{mu})$ will correctly identify the first index $1 \leq i \leq a$ to yield the lowest value of $f(i, b)$ in the range $1 \leq i \leq a$.

4.4. Logic gates

We conclude with a small detour: how to encode a circuit of logic gates in our framework.

In one sense, considering logic gates is a waste: we already have predicates and terms, which are higher-level and (as we have seen) compile to polynomials. Yet it is still an interesting exercise to spell out how this might be done.

We will only implement a *nand* gate; this is known to be a *universal logic gate* from which all others may be derived (see [Kup24] for a clear presentation), and it will also be completely clear from the example of *nand* below how other logic gates could be directly implemented.

Recall from Remark 2.2.9(1) that we model 0 as ‘true’ and 1 as ‘false’, so the bits are flipped. *nand* should return 1 (false) if both of its inputs are 0 (true), and 0 (true) otherwise.

DEFINITION 4.4.1. It is straightforward to encode NAND as a simple polynomial:

$$\text{nand}(a, b) = (1 - a) * (1 - b).$$

Now we want to connect our individual logic gates into circuits.

We could try to nest our gates as functions, but we see from Remark 3.2.12(4) that this would result in polynomials of high degree; a more efficient method is available.

DEFINITION 4.4.2. Assume a matrix variable symbol *nand* with arity $\text{arity}(\text{nand}) = 5$. In Figure 10, we define syntactic sugar and use it to express a validity predicate χ_{nand} , based on Definition 4.4.1.

REMARK 4.4.3. We read through this carefully. Suppose we are given a valuation ς and consider column number x in $\varsigma(\text{nand})$, which is a 5-tuple of elements $(a, b, r, p_1, p_2) \in \mathbb{Z}^5$:

$$\begin{array}{ll}
\text{nand}_1(X) \text{ sugars to } \text{nand.a}(X) & \text{nand}_2(X) \text{ sugars to } \text{nand.b}(X) \\
\text{nand}_3(X) \text{ sugars to } \text{nand.out}(X) & \\
\text{nand}_4(X) \text{ sugars to } \text{a.ptr}(X) & \text{nand}_5(X) \text{ sugars to } \text{b.ptr}(X)
\end{array}$$

$$\begin{aligned}
\chi_{\text{nand}} = & 0 \leq \text{a.ptr} \leq \text{len}(\text{nand}) \wedge 0 \leq \text{b.ptr} \leq \text{len}(\text{nand}) \wedge \\
& \forall X_{\text{nand}}. ((\text{nand.out}(X) = (1 - \text{nand.a}(X)) * (1 - \text{nand.b}(X))) \wedge \\
& (\text{a.ptr}(X) = 0 \vee \text{nand.out}(\text{a.ptr}(X)) = \text{nand.a}(X)) \wedge \\
& (\text{b.ptr}(X) = 0 \vee \text{nand.out}(\text{b.ptr}(X)) = \text{nand.b}(X)) \wedge \\
& (\text{nand.a}(X) = 0 \vee \text{nand.a}(X) = 1) \wedge \\
& (\text{nand.b}(X) = 0 \vee \text{nand.b}(X) = 1))
\end{aligned}$$

Fig. 10: Expressing a circuit of NAND gates (Definition 4.4.2)

- (1) a represents the first argument to the NAND gate. This must be equal to either 0 or 1.³²
- (2) b represents the second argument to the NAND gate.
- (3) r represents the result. Note that χ_{nand} insists that $r = \text{nand}(a, b)$.
- (4) p_1 represents *either* a null pointer 0, or it is a pointer to the output of some other gate. Note that χ_{nand} insists that the value of that output is equal to a ; thus we ‘wire’ the output to the first input of this gate.
- (5) Similarly for p_2 .

Visibly this implements a circuit of NAND gates, where input wires are ones such that p_1 (or p_2) is zero, and output wires are outputs that are not pointed to.³³ A more explicit encoding would also be possible, but this will do.

REMARK 4.4.4.

- (1) Circuits with multiple types of gate (AND, OR, NOT) could be encoded, either as distinct matrix variable symbols with their own validity predicates, or by generalising Figure 10 directly with an additional row of data to describe what type of gate is stored.
- (2) The degree of the polynomial $[\chi_{\text{nand}}]_{\zeta}$ scales as $O(n^2)$, where n is $\text{len}(\zeta(\text{nand}))$. Our framework is not optimised for the special case of representing a circuit of simple logic gates, but if we specialise anyway then this does not obviously incur any serious overhead.

5. EFFICIENCY: SUCCINCTNESS AND BUILTIN FUNCTIONS

5.1. Succinctness

REMARK 5.1.1. A **succinct proof** in the cryptographic sense is when a *prover* does a computation and provides a certificate that can be checked by a *verifier* to verify (to a high degree of certainty) that the computation has been correctly carried out.³⁴

We will now illustrate how the maths in this paper leads to succinct proofs.

5.1.1. Some notation. It will be useful to give a name to the polynomial that is 0 on $\{1, \dots, n\}$:

³²We impose this condition explicitly in Figure 10 as $\text{nand.a}(X) = 0 \vee \text{nand.a}(X) = 1$ and similarly for nand.b ; however, we could also insist on $0 \leq \text{nand.a} \leq 1$. Because a valuation ζ returns an *integer* matrix, this would suffice. These two methods are however subtly different: if we care about zero knowledge then it may be that range checked values get exposed to the validator. See point 2 of the discussion in Subsection 5.2.

³³Note that in this system wires can be duplicated; the output of a gate can be wired to zero, one, or many outputs.

³⁴Note: a succinct proof in the cryptographic sense is not a formal proof in the logical sense.

DEFINITION 5.1.2. Suppose $n \in \mathbb{N}_{\geq 0}$. Define a polynomial $zeroes_n$ over X inductively on n as follows:

$$zeroes_0 = 1 \quad zeroes_{n+1} = (X - (n + 1)) * zeroes_n.$$

Thus for example:

$$zeroes_0 = 1 \quad zeroes_1 = 1 * (X - 1) \quad zeroes_3 = 1 * (X - 1) * (X - 2) * (X - 3).$$

NOTATION 5.1.3. Suppose M is an integer matrix of size $a \times b$ and recall from Notation 2.2.7(2) that M_i denotes the i th row of M (which is a vector of length b), and $intrplnt(M_i)$ (Definition 2.1.4) is a polynomial that interpolates M_i on $\{1, \dots, b\}$ (which means that $intrplnt(M_i)(j) = M_{i,j}$ for $1 \leq (j \in \mathbb{N}_{\geq 1}) \leq b$).

Then define the **polynomial interpolant of M**

$$intrplnt(M) = (intrplnt(M_1), \dots, intrplnt(M_a)).$$

Thus $intrplnt(M)$ is an a -tuple of polynomials such that that i th polynomial interpolates the i th row of M .

DEFINITION 5.1.4 (Tasks and solutions).

- (1) A **task** \mathcal{T} is a finite set of pairs (C, ϕ) such that for each $(C, \phi) \in \mathcal{T}$:
- (a) C is a matrix variable symbol.
 - (b) ϕ is a quantifier-free predicate.

We may abuse notation and write

- $C \in \mathcal{T}$ for “there exists a ϕ such that $(C, \phi) \in \mathcal{T}$ ”, and
- $\phi \in \mathcal{T}$ for “there exists a C such that $(C, \phi) \in \mathcal{T}$ ”, respectively.

- (2) A **solution** to a task consists of a pair (ς, h) , where:

- (a) ς is a valuation such that

$$\forall (C, \phi) \in \mathcal{T}. \models_{\varsigma} \forall X_C. \phi. \quad (3)$$

Recall that the $\models_{\varsigma} \forall X_C. \phi$ notation is from Definition 2.2.8(4), and it makes sense because X is not free in $\forall X_C. \phi$.

- (b) h is a map that assigns to each $(C, \phi) \in \mathcal{T}$ a **quotient polynomial** $h_{C,\phi} \in \mathbb{Q}[X]$ such that

$$[\phi]_{\varsigma} = h_{C,\phi} * zeroes_{len(\varsigma(C))}.$$

Note that this is a polynomial equality in $\mathbb{Q}[X]$, not a numerical equality in \mathbb{Q} . Recall that $len(\varsigma(C))$ is the number of columns in the matrix $\varsigma(C)$; the number of rows is always *arity*(C), but the number of columns depends on the choice of ς .

5.1.2. Applying Schwartz-Zippel. This Subsection does not solve the problem of succinct proofs for tasks, but it will set the scene for the next Subsection.³⁵

REMARK 5.1.5. Suppose that a *prover* wants to convince a *verifier* that it knows a solution (ς, h) to a task \mathcal{T} (Definition 5.1.4). The prover could send (ς, h) to the verifier and invite it to check $\models_{\varsigma} \forall X_C. \phi$ for every $(C, \phi) \in \mathcal{T}$ but this would defeat the purpose of having a prover and a verifier, since the verifier is just duplicating the prover’s work *and* the network has to communicate all of ς , which is the prover’s data.

Now note that it would suffice for the verifier to check for each $(C, \phi) \in \mathcal{T}$ the polynomial equality

$$[\phi]_{\varsigma} = h_{C,\phi} * zeroes_{len(\varsigma(C))}$$

because this implies that $[\phi]_{\varsigma}(i) = 0$ for $1 \leq (i \in N) \leq len(\varsigma(C))$ and $\models_{\varsigma} \forall X_C. \phi$ follows.

So, we have replaced checking a quantification over $1, \dots, len(\varsigma(C))$ with checking a polynomial equality involving $zeroes_{len(\varsigma(C))}$. How is this an improvement?

³⁵If you are familiar with this material then remember that some other readers might not be, and this paper is for them too; so please either bear with me or just skip to the next Subsection.

We start with something very simple, which is just to apply the Schwartz-Zippel lemma:

LEMMA 5.1.6. *Suppose \mathcal{T} is a task and suppose (ς, h) is a claimed solution to \mathcal{T} . We proceed as follows:*

- (1) For each $(C, \phi) \in \mathcal{T}$, choose a random $q \in \mathbb{Q}$, and
- (2) check that

$$[\phi]_{\varsigma}(q) = h_{C, \phi}(q) * zeroes_{len(\varsigma(C))}(q).$$

If are these equalities hold, then with a high degree of probability (ς, h) is indeed a solution to \mathcal{T} .

Proof. By elementary properties of polynomials, if $[\phi]_{\varsigma}$ and $h_{C, \phi} * zeroes_{len(\varsigma(C))}$ are not equal polynomials then they agree on at most $d \in \mathbb{N}$ points, where d is the highest power of X in either polynomial.

Thus if we choose $q \in \mathbb{Q}$ at random, it is almost certain to *not* be one of those points. This is the basis of the **Schwartz-Zippel lemma**; we can check polynomial equality to a high degree of probability, by checking equality at a random point [Tha22, Lemma 3.3, Subsection 3.4]. So, if we evaluate our two polynomials at a random point and get the same answer, then they are almost certainly equal. By the discussion in Remark 5.1.5 above, $\models_{\varsigma} \forall X_C. \phi$ is proven (to a high degree of probability). \square

REMARK 5.1.7. The scheme above is not what a cryptographer would call a succinct proof system. Lemma 5.1.6 does save the verifier *some* computation, since for each $(C, \phi) \in \mathcal{T}$ the verifier needs to evaluate $[\phi]_{\varsigma}$, $h_{C, \phi}$, and $zeroes_{len(\varsigma(C))}$ at one point q instead of at every $1 \leq (x \in N_{\geq 1}) \leq len(\varsigma(C))$.

However, the prover still needs to communicate the full solution (ς, h) to the verifier, and the verifier still needs to directly evaluate large polynomials (albeit at a single point). There are ways to make this more efficient, as we discuss next.

5.1.3. Checking solutions using inner product arguments. For concreteness we consider our scheme for the case of the validity predicate for the power function from Subsection 3.2. This is a simple example, but it illustrates the issues involved and it will be clear how to generalise to the arbitrary case.

Recall the validity predicate χ_{pow} for pow from Figure 3. Using Lemma 3.7.4 and Definition 3.7.2, we can express this as the following simple task:

$$\mathcal{T} = \{(\text{pow}, \phi_1), (\text{pow}, \phi_2)\}$$

where

$$\phi_1 = \text{range}_{1, len(\varsigma(\text{pow}))}(\text{rec}(X)) \text{ and } \phi_2 = \text{clause1}(X) \mathbf{V} \text{clause2}(X).$$

Above, $\text{range}_{1, len(\varsigma(\text{pow}))}$ is from Definition 3.7.2, and clause_1 and clause_2 and rec are from Figure 3.

The prover computes a solution (ς, h) for this task. If the prover is honest and correct then by Lemma 3.2.7 $\varsigma(\text{pow})$ encodes a partial power function (Definition 3.2.6).

The verifier should now choose some very large prime p — one that that is larger than any coefficient or power mentioned in \mathcal{T} or its solution (ς, h) . Bearing in mind that ς and h are physically constrained by the prover's memory, a prime close to 2^{256} should suffice. The verifier communicates this p to the prover.

The prover then homomorphically maps the equalities

$$[\phi_i]_{\varsigma} = h_{\text{pow}, \phi_i} * zeroes_{len(\varsigma(\text{pow}))} \quad \text{for } i \in \{1, 2\}$$

into the finite field $\mathbb{F}(p)$ (which is isomorphic to $\mathbb{Z}/\mathbb{Z}p$). The prover and verifier will perform the succinct check in $\mathbb{F}(p)$.³⁶

³⁶Note that we can do this *even if* our original logical assertion used conjunction, and *even if* we have done other things, such as the Gödel encoding of combinator syntax mentioned in Definition 3.9.3(3). We just have a polynomial in $\mathbb{F}(p)$ and we want to check it.

So now we have a polynomial equality over $\mathbb{F}(p)$. Note that ϕ_1 and ϕ_2 are fixed, because they define the problem to be solved (namely: computing an exponential). The part that varies is $\varsigma(\text{pow})$, and evaluating (the interpolants to) its rows; namely a (row 1 of $\varsigma(\text{pow})$), b (row 2), out (row 3), and rec (row 4).

This is made mildly more complicated because we evaluate not only at some $x \in \mathbb{F}(p)$, but also at values derived from x . To see how, we unpack our example of Figure 3 and note that to check

$$[\phi_i]_{\varsigma}(x) = h_{\text{pow},\phi_i}(x) * \text{zeroes}_{\text{len}(\varsigma(\text{pow}))}(x) \pmod{p}$$

it suffices to evaluate the following quantities — which the reader can check are the component polynomials used in Figure 3 — mod p :

$$a(x), \text{out}(x), \text{rec}(x) = r, a(r), b(r), \text{out}(x), \text{out}(r), h_{\text{pow},\phi_i}(x), \\ \text{range}_{1,\text{len}(\varsigma(\text{pow}))}(x) \text{ and } \text{zeroes}_{\text{len}(\varsigma(\text{pow}))}(x). \quad (4)$$

Above, the value of $\text{rec}(x)$ is re-used, so for brevity we give it the name r .

Succinctly proving the evaluations should be straightforward by (for example) an *inner product argument* [BCC+16]³⁷ — except for the values of $\text{range}_{1,\text{len}(\varsigma(\text{pow}))}(x)$ and $\text{zeroes}_{\text{len}(\varsigma(\text{pow}))}(x)$, which the verifier would need to compute itself.³⁸

Once the prover has proven to the verifier that it knows polynomials a , out , rec , b , and h_{pow,ϕ_i} with suitable evaluations, the verifier can just perform a few \mathbb{F}_p -additions and -multiplications as per Example 3.2.11, as follows:

$$[\phi_1]_{\varsigma}(x) = \text{range}_{1,\text{len}(\varsigma(\text{pow}))}(x) \\ [\phi_2]_{\varsigma}(x) = (a(x)^2 + (\text{out}(x)-1)^2) * \\ ((a(x)-a(r_x))^2 + (b(x)-(b(r_x)+1))^2 + (\text{out}(x) - a(x)*\text{out}(r_x))^2)$$

We count fifteen additions or multiplications above, so this is very easy. Then, with two more multiplications in \mathbb{F}_p , the verifier checks that $[\phi_i]_{\varsigma}(x) = h_{\text{pow},\phi_i}(x) * \text{zeroes}_{\text{len}(\varsigma(\text{pow}))}(x)$ for $i \in \{1, 2\}$.

EXAMPLE 5.1.8. Suppose a prover wants to prove to a verifier that $10^2 = 100$. It can proceed as follows:

(1) The prover publishes the predicate

$$\phi_1 \wedge \phi_2 \wedge (a(1) = 10 \wedge b(1) = 2 \wedge \text{out}(1) = 100)$$

and computes a ς that makes this predicate valid.

(2) The prover and verifier perform the verification ceremony as described above. The verifier is assured that the prover has a $\varsigma(\text{pow})$ such that that χ_{pow} , and furthermore the first column of $\varsigma(\text{pow})$ asserts that $10^2 = 100$.

REMARK 5.1.9. Our scheme is sound and complete by construction, in the sense that by Theorem 2.3.4 we generate polynomial equalities that are valid if and only if the logical assertion is true (up to a high degree of certainty in the sense of Schwartz-Zippel), and to prove these equalities valid, it suffices to provide succinct proofs of their component parts as outlined above.

Still, the above is proof-of-concept; it remains to make it optimal. If these ideas were applied in a real implementation, we would want to add more advanced techniques, to increase efficiency or to attain a zero-knowledge proof (in which the verifier has no information at all about the solution ς).

Fortunately such techniques are available off-the-shelf. A common practice to achieve zero-knowledge is to apply recursive proof composition [BCCT13] on an existing SNARK. Similarly, since

³⁷An precise yet accessible survey, with references, is [here](#).

³⁸The difference is that *range* and *zeroes* are known polynomials; they are not arbitrary ones provided by the prover. If the verifier lacks the computational resources to perform these computations then the task could be passed to a trusted service. Note that this is a one-off cost, and so can be amortised over multiple clauses.

the complexity of the verifier in an IPA scheme is linear in time, it is also possible to add another layer of recursion using a SNARK of sublinear verification time. Halo2 [BGH19] leverages recursive proof composition techniques to amortise verification costs by constructing an accumulation scheme on a proving system based on the IPA. A KZG-based method should also be possible [KZG10] but describing it is more complex. Investigating this is future work.

REMARK 5.1.10. Our translation from FOL to polynomials works for all FOL predicates, but the schema for succinct proof above works only on (the translation of) predicates with a single universal quantifier at top level (so a fragment of Π_1^0 in the arithmetical hierarchy; but note Remark 6.1.1(6)). This is all we need for our examples.

We suspect it is all we will ever need, in the sense that we suspect (but have not proved) that the effects of nested quantifiers could be emulated using techniques similar to the encoding of arbitrary functions described in Subsection 4.2. Checking this, or generalising the schema, is future work.

5.2. Built-in functions

Having considered cryptographic notions of efficiency in the previous Subsection, we now make a few observations about making things run quickly on native silicon. In this case, we may prefer to check some validity properties directly:

- (1) We gave a polynomial circuit to check that a matrix expresses bitwise expansion of 64-bit unsigned integers in Subsection 3.6. But bitwise operations may be rapid on underlying hardware — not least because this is how numbers are represented anyway. In practice, rather than check a validity predicate, a verifier might recognise that this is a bitwise expansion operation and prefer to just traverse the matrix and check its correctness directly.

In a monolithic system, where everything has to run in an abstract machine built in arithmetic circuits, this might be a bit problematic because it would involve stepping outside the abstraction provided. Here, the system is very modular, so this may be less of a problem.

- (2) The range check primitive $<$ in Figure 1 can be checked rapidly in silicon. If we are just using $<$ to range check that pointers are in bound for the matrices they point to, then information leakage would be minimal: all a validator would have is a set of pointers, but without knowing how the prover has laid out its data in its matrices, this does not impart much useful information. For extra security, the numbers could always be obfuscated. Concretely, we would do this by providing pos from Definition 4.1.2 as a built-in matrix variable symbol of infinite length — calls to the validity check of pos would just get passed directly to the underlying silicon. As per Remark 4.1.10, just providing this would provide all the power of $<$.

So we see that if we just care about speed, then efficient checking in silicon for a small set of special built-in validity predicates, like range checks and bitwise operations, should be fine. If we want a full zero-knowledge treatment, such that the verifier never learns the precise witnesses used, this would not be possible.

But even here, we might (depending on circumstances) be happy to accept a small amount of information leakage, if this buys us enough efficiency. In particular, range checks for pointers could be done outside of a zero knowledge wrapper and information leakage may be acceptable, if all the verifier has is a bunch of pointers but no information about what they point to or how they connect. This seems plausible, but working through how to do it in practice is future work.

6. CONCLUSIONS AND FURTHER WORK

6.1. Future work and other observations

REMARK 6.1.1. There are plenty of directions to go from here. For example:

- (1) We have set up a shallow translation of logic to polynomials, and logical judgements to polynomial equalities. An obvious next step is to engineer a practical proving system based on these ideas.

- (2) We include an existential quantifier \exists in our syntax and semantics, but in practice a more efficient technique might be to use *Skolem functions* [AZ20]. A Skolem function is just a way to replace an existential quantifier with a function symbol; e.g. $\forall x.\exists y.x = y$ becomes $\exists f.\forall x.x = f(x)$. The f can then be compiled down to a matrix variable symbol in our base syntax, using the techniques in Subsection 4.2.

Thus mathematically, one way to view our matrix variable symbols is as Skolem functions.

- (3) Our polynomials are univariate, and correspondingly our first-order logic has just one variable symbol X (and arbitrarily many matrix variable symbols). It would be natural to generalise to the multivariate case — thus permitting X , but also X' and Y , and so on, in Figure 1. This would naturally give a translation to multivariate first-order logic, just by setting $[X'] = X'$ for each variable symbols in Figure 2. Nothing in the maths in Figure 2 would be affected.

This might be convenient and useful for applying multivariate arithmetisation techniques, but we do not think it would increase expressivity. It seems likely that we could emulate the multivariate case using Skolem functions (as discussed above) and (functions representing) ϵ -terms [AZ20]. Looking into this generalisation is future work.

- (4) We can consider extending the logic with constructs based on polynomial division. For instance, define $P \setminus Q$ to be P with any roots it shares with Q divided out. This would model non-implication $\phi \not\vdash \psi$ (which can also be written as $\phi \wedge \neg\psi$).

- (5) We could finesse the logic to make it resource-sensitive.

The validity judgement of our logic is clean and simple

$$x \models_{\zeta} \phi \iff [\phi]_{\zeta}(x) = 0,$$

as per Figure 2. This just says that x is a root of $[\phi]_{\zeta}$.

However, a polynomial is a *multiset* of roots, which suggests a validity judgement of the form

$$\models_{\zeta} (\Phi \vdash \psi) \iff [\psi]_{\zeta} \mid \prod_{\phi \in \Phi} [\phi]_{\zeta}.$$

This polynomial divisibility assertion is also extremely natural.

Note that this would change the logic; it would become resource-sensitive, in the sense of linear logic. Whether this is a feature or a bug depends on what we want to achieve; see e.g. Remark 3.2.12 which comments on the efficiencies of being able to remove repeated roots.

- (6) How powerful is our logic, really? In fact, extremely powerful.

The syntax in Figure 1 has a term-former term that maps predicates back down to terms. This reflects the fact that in our semantics, terms map to polynomials, and so do predicates (predicates map to *non-negative* polynomials, but a non-negative polynomial is still just a polynomial).

This is unusual and it makes the logic impredicative. We have called our logic ‘first-order logic’, and this was not untrue since it comes with a specific intended model in which the variable X ranges over numbers. But what that left unsaid at the start, for simplicity, was that our logic *also* has an in-built encoding and decoding of predicates as terms; with term going from predicates to terms, and $t = 0$ going from terms to predicates (cf. Remark 2.3.6).

So this is FOL, but not *just* FOL, and the full expressivity of the syntax in Figure 1, with its intended semantics in Figure 2 is a topic for future research.

- (7) Tree-structured data is used pervasively in declarative programming. In light of this paper, it would be interesting to give labelled trees additive and multiplicative structure sufficient to do polynomial arithmetic directly over trees (something along these lines has been done [Tar16]) so that, instead of working as we do in this paper with a ring of polynomials over rationals ($\mathbb{Q}[X]$), we could work with a ring of polynomials over trees: $\text{Tree}[X]$. Integers can already emulate trees via a Gödel encoding, but if we had polynomials with coefficients that already *are* trees, then we might be able to avoid some encoding and decoding. This is speculative but would be interesting to consider for future work.

6.2. More on efficiency

We discussed efficiency and succinctness in Remarks 3.2.12 and 3.4.6(2) and Section 5. We make a few more general observations, based on the following question:

How big do our polynomials get?

REMARK 6.2.1.

- (1) When we succinctly check a polynomial equality

$$\llbracket \phi \rrbracket_{\zeta}(q) = h_{C,\phi}(q) * \text{zeroes}_{\text{len}(\zeta(C))}(q),$$

lower-degree polynomials are better because *a*) they are easier to evaluate and *b*) they have fewer roots (so that a succinct check is less likely to give a false positive).

So we can ask what contributes to the degree of the polynomial that is $\llbracket \phi \rrbracket_{\zeta}$. We discussed this in the context of a simple but paradigmatic example in Remark 3.2.12.

One important point is that \wedge maps to addition, which does not increase degree — we can say: *conjunction is free*. Thus, succinctly checking a conjunction of validity predicates is no more expensive than checking the most expensive (= highest degree) clause.

Thus, a program consisting of a large number of matrix variable symbols with relatively simple, low-degree validity predicates, is better than a program with a smaller number of matrix variable symbols with more complex validity predicates. This fits in nicely with a modular programming philosophy, where we try to factor our system into a many relatively simple parts. The reader will know that factoring a system into smaller pieces, where possible, is generally accepted as good practice *anyway* — but here it has concrete mathematical implications: we get lower degree polynomials.

- (2) Having matrices with lots of rows is cheap, because rows do not directly increase the degree of polynomials. Long interpolation vectors cost, because (as we noted in Definition 2.1.4(2) and Remark 3.2.12(5)) a vector of length n requires in general a degree $n-1$ interpolating polynomial. So we want to design our validation predicates to create tall but narrow matrices.
- (3) So our question becomes:

What leads to wide matrices?

Broadly speaking, this is bounded by how many *distinct* function calls are made overall ('distinct', because the system is naturally memoised; see Remark 3.4.6(2)).

Counting such calls is a familiar task; for example, exponentiation a^b can be computed in $O(\ln(b))$ distinct calls (as discussed in Remark 3.2.14). So after all this discussion, our conclusion is fairly straightforward:

- (a) computation and control flow are free, and
- (b) the cost of verification scales with the size of the *longest deduplicated derivation chain*.

Broadly speaking, this corresponds to the inherent complexity of proving something, so we can say that our translation is efficient in the sense that it converts a logical problem into an arithmetic one of roughly equal (and thus not much larger) complexity.

6.3. Other comments

REMARK 6.3.1 (Logic programming). Logic programming can be understood as the engineering of predicates and derivation rules to make the search for derivation-trees computationally efficient (an old but to-the-point research survey is in [MNPS89]).

Our examples in Section 3 show how we can encode computation using derivation-trees, as is standard in mathematically structured programming. A so-called *validity predicate* χ_C expresses when a matrix $\zeta(C)$ expresses a valid derivation-tree, and thus a valid computation. The reader can think of $\zeta(C)$ as being a kind of 'generalised computation trace'; just note that derivation trees are not necessarily linear.

Given that this paper uses derivation-trees, and so does logic programming, is there a connection? Yes, and no.

For us in this paper, *finding* a valid derivation-tree / providing a suitable ζ is for the prover. Many methods for generating derivation-trees exist — including logic-programming systems, but also rewrite systems, or just by some suitable evaluation strategy. What is true is that logic programming is one way to efficiently construct derivation-trees, and our semantics consumes them, so there is a natural match here.

REMARK 6.3.2 (Why matrices?). Why do we need matrices variable symbols in Figure 2? Why not just encode all our information into natural numbers, as we do e.g. when we use a Cantor pairing function to Gödel encode combinator terms in Definition 3.9.3? Why not just do this or something like it throughout?

We could try, but a key issue is our use of *pointers*. The reader can see in the examples in Section 3 how we use entries in our matrices to point to columns in matrices (we make an effort to really spell this out in Examples 3.2.8 and 3.2.9). If we Gödel encode all structure in natural numbers, then this would be harder to do because we would have to unpack those pointers from the encoding before using them. This is not necessarily impossible, but it would add layers of complexity which for now we prefer to do without.

To put this another way: we use derivation-trees a lot in our semantics, and trees are graphs, and graphs are labelled nodes with edges. There is a natural structural map from here to matrices, and thence to interpolating polynomials and succinct proofs in the cryptographic sense. Other design decisions may be possible, but this paper follows a(n in retrospect) natural structural path through the mathematics.

REMARK 6.3.3 (Why \mathbb{Q} ?). We give predicates semantics as nonnegative polynomials in $\mathbb{Q}[X]$, interpret truth as the number 0, and map $\mathbf{\Lambda}$ to $+$. This may seem counterintuitive: yes if $x, y \geq 0$ then $x + y = 0$ if and on only if $x = 0 \wedge y = 0$ — but we lose this property if we work in a p -element finite field $\mathbb{F}(p)$, as is common in cryptography. How is this consistent with our semantics for $\mathbf{\Lambda}$ as addition?

The point is that once we have transformed our logical judgements into a polynomial equalities over $\mathbb{Q}[X]$ as per Figure 2 or Lemma 5.1.6, all we have is a polynomial equality. We are free to verify this by the method of our choice, including mapping to a prime field (so long as the prime p is chosen sensibly and large enough that Schwartz-Zippel works), and use whatever cryptographic apparatus we wish. There is no contradiction between that and our use of \mathbb{Q} .

REMARK 6.3.4 (FOL truth vs. FOL derivability). This paper maps FOL judgements directly to polynomial equalities (Figure 2), such that valid (true) judgements map to valid (true) equalities.

Another approach to encoding FOL is to map its syntax and derivation-trees to polynomials with a circuit that identifies the (encodings of) well-formed derivation trees. That is also a good approach, but it encodes *derivability*, not *validity*. To see how these differ, consider the FOL predicate $0 = 0$. This is valid, since 0 *is* equal to 0; and it is derivable by the equality right-derivation rule. As per Figure 2 $\llbracket 0 = 0 \rrbracket_{\zeta} = 0$, which represents ‘true’. Thus, $\llbracket 0 = 0 \rrbracket_{\zeta}$ is true because it literally *is* true; indeed no reasoning or circuitry is required in this case; it is just a fact! An arithmetisation of FOL that uses derivability would encode a (simple) derivation tree like this

$$\frac{}{\vdash 0 = 0} (=R)$$

along with a circuit expressing that this is well-formed. This also makes sense, but it is a different approach.

It is the difference between asserting ‘ $\forall x. \exists y. x = y$ is true in some finite model’ and ‘I have a valid derivation-tree of $\forall x. \exists y. x = y$ ’. The former is a (true) assertion about concrete behaviour in finite models (finite, because ζ is unbounded, but finite), whereas the latter is an assertion about derivability.

For computation, finite models are what matters most, so our design choice is natural. We labour this point because derivation-trees still feature in this paper (they are a common structure), but they arise in the spirit of being generalised computation traces; the embedding of the FOL logic remains semantic and direct as discussed above. An ‘infinite models’ version of the ideas in this paper, possibly working through explicit consideration of FOL derivation-trees, is possible future work.

REMARK 6.3.5 (Rapid coprocessors). We could use the ideas in this paper for fast and efficient prototyping of a correct-by-construction *coprocessor* for an existing system: to create some function, specified and arithmetised using the techniques in this paper, for the specific purpose of being called from some other zero-knowledge abstract machine.

We have seen how the compositional nature of our semantics lends itself — by design — to specifying functional programs, which require no external state and which are correct by construction.³⁹ Functional components are modular and compositional, and therefore portable and well-suited to being plugged together.

This compositionality could be useful and may mean that the barrier to extracting practical value from these ideas may be relatively low.

Furthermore, pure speed is not the only metric of success: shorter development time, greater modularity, readability, maintainability, portability, and amenability to formal verification of correctness, are also valid criteria, amongst others. High-level specifications tend to perform well by these metrics, so a compositional shallow translation directly to polynomials is interesting.

6.4. Final remarks

We have mapped logic to polynomials, and logical judgements to polynomial equalities. At a high level, the mapping is simple:

- (1) Truth maps to zero, and falsity maps to one.
- (2) \wedge and \vee map to $+$, and \forall and \exists map to $*$ (see Figure 2 and the discussion in Remark 3.2.12).

Simple facts of arithmetic — notably that a sum of two nonnegative numbers is zero if and only if both numbers are zero (Lemma 2.3.2) — give us a soundness and completeness result (Theorem 2.3.4).

It is perhaps surprising that from this elementary arithmetic beginning, we manage to pull out so much logical and computational content.

Using matrix variable symbols and some simple logic, we specify a library of exemplar functions (Subsection 3), including the Turing-complete SK-combinator system.⁴⁰ Note that the valuation ς may have arbitrary size; its size and the size of the validity predicate are two orthogonal parameters, so that even a fairly simple and compact validity predicate (like the one in Figure 8) can express the correctness of an arbitrarily complex computation trace as encoded in ς . Furthermore, computations corresponds to derivations (in the style of mathematically structured programming), which are trees: they are not restricted to being a linear trace. Finally, we note that the range check primitive acts in the context of our logic as a truth modality (see Remark 4.1.11) and using this we can encode even more interesting structure (see Section 4).

Our semantics maps logical assertions to polynomial equalities (Definition 2.2.8(3)), which can be checked succinctly and/or in zero knowledge (Subsection 5.1.3). The semantics is clean, compositional, and modular, and overheads seem to be pleasingly low — the cost of verifying a derivation scales (roughly speaking) with its deduplicated size. We get a nice mapping from derivation-trees to matrices, and modularity in the specification corresponds to relatively lower degree polynomials. Practical application is plausible both in and of itself, and as a coprocessor for other systems.

Because this system is based on logic, implementations based on it are likely to be amenable to tools from computer science such as formal verification, correctness-by-construction, and type systems. It is true that not everything in this paper is elementary, but even here it is noteworthy that the complexities that arise come from *the applications*, and not from the framework; in other words, our logic and its polynomial translation do not seem to make a thing any harder than it inherently *already is*. For instance: a \wedge turns into a $+$ and a \forall turns into $*$, thus one connective = one arithmetic operation — it is hard to imagine a translation being more efficient or direct than that!

³⁹Correct by construction with respect to the specification. If the specification is buggy then the program constructed from it will be too. But it will faithfully satisfy the (incorrect) specification.

⁴⁰SK combinators are not user-friendly, but they are one paradigmatic example of a Turing-complete system.

Finally, logics other than first-order logic exist, so implicit in the act of mapping of *this* logic to polynomials is an implication that we could do the same for *others* — and conversely, there is an implication that it may be fruitful for logicians to study novel logics based on polynomial semantics motivated by succinct proofs.

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