# **Cirrus**: Performant and Accountable Distributed SNARK

Wenhao Wang *Yale University*

Fangyan Shi *Tsinghua University*

Dani Vilardell *Cornell University*

Fan Zhang *Yale University*

*Abstract*—As Succinct Non-interactive Arguments of Knowledge (SNARKs) gain traction for large-scale applications, *distributed proof generation* is a promising technique to horizontally scale up the performance. In such protocols, the workload to generate SNARK proofs is distributed among a set of workers, potentially with the help of a coordinator. Desiderata include linear worker time (in the size of their sub-tasks), low coordination overhead, low communication complexity, and *accountability* (the coordinator can identify malicious workers). State-of-the-art schemes, however, do not achieve these properties.

In this paper, we introduced **Cirrus**, the first accountable distributed proof generation protocol with linear computation complexity for all parties. **Cirrus** is based on HyperPlonk (EUROCRYPT'23) and therefore supports a universal trusted setup. **Cirrus** is horizontally scalable: proving statements about a circuit of size  $O(MT)$  takes  $O(T)$  time with M workers. The per-machine communication cost of **Cirrus** is low, which is only logarithmic in the size of each sub-circuit. **Cirrus** is also accountable, and the verification overhead of the coordinator is efficient. We further devised a *load balancing* technique to make the workload of the coordinator independent of the size of each sub-circuit.

We implemented an end-to-end prototype of **Cirrus** and evaluated its performance on modestly powerful machines. Our results confirm the horizontal scalability of **Cirrus**, and the proof generation time for circuits with  $2^{25}$  gates is roughly 40s using 32 8-core machines. We also compared **Cirrus** with Hekaton (CCS'24), and **Cirrus** is faster when proving PLONKfriendly circuits such as Pedersen hash.

## 1. Introduction

Succinct Non-interactive Arguments of Knowledge (SNARKs) [\[1\]](#page-13-0), [\[2\]](#page-13-1), [\[3\]](#page-13-2), [\[4\]](#page-13-3), [\[5\]](#page-13-4), [\[6\]](#page-13-5), [\[7\]](#page-13-6), [\[8\]](#page-13-7), [\[9\]](#page-13-8), [\[10\]](#page-13-9), [\[11\]](#page-13-10) have emerged as a breakthrough in cryptographic tools, enabling efficient, non-interactive verification of computations. The primitive has proven practical for various applications, such as privacy-preserving payments (e.g., Zcash [\[12\]](#page-13-11)), scalability solutions for decentralized consensus (e.g., zkRollups [\[13\]](#page-13-12)), and blockchain interoperability (e.g., zkBridges [\[14\]](#page-13-13)). However, for more ambitious applications, such as verifiable machine learning (zkML) and verifiable virtual machines (zkVM), the computational demands of proof generation remain a substantial bottleneck.

A recent line of works [\[14\]](#page-13-13), [\[15\]](#page-13-14), [\[16\]](#page-13-15), [\[17\]](#page-13-16), [\[18\]](#page-13-17) proposed to horizontally scale up SNARK generation with *distributed SNARK generation protocols*. Distributed SNARK generation protocols allow multiple *workers* to collaborate on different parts of a general-purpose circuit, creating a final proof that is still succinct and sound. By splitting up the proof task across workers, distributed proof generation not only speeds up computation but also reduces memory usage, making it feasible to prove statements about large circuits that cannot fit in the memory of a single server.

An ideal distributed proof scheme should satisfy several properties. First, it should scale horizontally with *low overhead* (e.g., a worker's computation should be linear in the size of its sub-task). Second, to support deployment in a decentralized setting where provers may be malicious, it should achieve *accountability*, allowing detection of malfeasance and identification of offenders. Accountability is necessary for applications such as prover markets, for instance [\[19\]](#page-13-18), [\[20\]](#page-13-19), [\[21\]](#page-13-20), which aim to connect users and untrusted workers with spare computation power. Third, to support diverse applications, it should avoid per-circuit trusted setup because it is infeasible to require each application to perform its independent setup ceremony [\[22\]](#page-13-21).

While prior works reduced overhead and achieved partial accountability, none achieved all three properties. Pianist [\[16\]](#page-13-15) is the first distributed proof generation scheme based on PLONK. It achieved a robust quasilinear prover time protocol for general circuits but did not support identifying malicious activity. Some following works [\[18\]](#page-13-17) improved the prover complexity to linear time but still lacked accountability. Hekaton [\[17\]](#page-13-16) introduced an accountable protocol with quasilinear prover time, but it relies on Mirage [\[23\]](#page-13-22), a SNARK requiring a circuit-specific setup, making it costly to use in general-purpose circuits. Neither of them achieves linear worker time.

We present Cirrus, the first distributed proof generation scheme that is low overhead, accountable, and supports universal trusted setup. To be precise, Cirrus achieves linear time complexity on both the worker and coordinator sides, all while ensuring minimal communication overhead. Cirrus incorporates a mechanism that allows the coordinator to not only detect the existence of malicious behavior but also pinpoint the specific worker at fault and enable the implementation of incentive-based systems to enhance security and trustworthiness. By addressing the dual challenges of computational efficiency and accountability, Cirrus paves the way for more scalable and secure SNARK applications across a broader range of decentralized systems.

## 1.1. Technical Overview

We start by distributing HyperPlonk [\[8\]](#page-13-7) because HyperPlonk has a prover time linear in the circuit size (from which we achieve linear worker prover time) and supports a universal trusted setup. To distribute the SNARK generation process to different nodes, we follow the idea in [\[16\]](#page-13-15). We split a circuit  $C$  of  $N$  gates into  $M$  sub-circuits, where each sub-circuit has size T. Following this setting, M *workers* and one *coordinator* generate the proof together.

Distributing HyperPlonk. To successfully distribute HyperPlonk, we need to find a way to distribute multilinear KZG, sum-check protocol (SumCheck), zero test protocol (ZeroTest), and permutation test protocol (PermTest), since they are the essential building blocks of HyperPlonk. Distributing multilinear KZG is straightforward, yet naively distributing SumCheck using techniques in [\[14\]](#page-13-13) would lead to the following problem: at the end of SumCheck we will have to open a multivariate polynomial of degree  $d$ , where  $d$  is a constant greater than 1, but with techniques in [\[14\]](#page-13-13) and multilinear KZG we can only commit and open multivariate polynomials with degree at most 1. We overcome this challenge using the observation from [\[6\]](#page-13-5): each constant-degree multivariate polynomial can be written as a function of a constant number of multilinear polynomials. Following this observation, we can carefully separate each multivariate polynomial into multilinear polynomials, therefore making it possible to use distributed multilinear KZG to open polynomials at the end of distributed SumCheck. With distributed multivariate Sumcheck constructed, we can derive the constructions of distributed ZeroTest, distributed PermTest, and finally, distributed HyperPlonk.

Minimizing computation time of the coordinator. The coordinator's computation time depends on the number of workers; thus, it can be a bottleneck when the number of workers is large. However, naively distributing HyperPlonk, the coordinator will end up with a computation time of  $O(M \log T)$ . This is because the proofs of SumCheck and multivariate KZG components are of size  $O(\log T)$  for each worker prover, and the coordinator will have to add up M field or group element vectors of length  $O(\log T)$ . To mitigate this problem, Cirrus distributes the load of aggregating  $O(M)$  vectors distributed to  $M/\log T$  worker provers (called leaders), where each leader is responsible for adding the vectors of  $\log T$  provers. In this way, the total runtime of this step for the coordinator is reduced to  $O(M)$ . We note that the computation overhead for each leader is  $O(\log^2 T)$ , which is dominated by  $O(T)$ .

Optimistic accountability. Up to this point, our protocol can only work when all workers are honest. To make our protocol accountable even assuming an arbitrary number of corrupted worker nodes, we need to enable the coordinator to tell which provers are malicious apart when the generated proof is not correct. To achieve this, we have the coordinator verify all the computations done by the individual workers, as well as distinguish when it was the worker or the leader the one that provided the incorrect proof to the coordinator. However, verifying the computation results of each worker every time is computationally expensive. For example, the verification steps in distributed PermTest introduce a computation workload linear in the size of the entire circuit to the runtime of the coordinator. To solve this problem, we leverage the soundness property of Cirrus (i.e., the verifier has an overwhelming probability of detecting an incorrect final proof) and have the coordinator verify the final proof. If the proof is valid, the protocol is in the optimistic path and no further checks are required—by the soundness property, no worker is malicious with overwhelming probability. Otherwise, the protocol switches to the pessimistic path, and the coordinator checks the transcripts from each prover separately to identify the offenders.

# 1.2. Implementation and Evaluation

We implement Cirrus and evaluate its end-to-end performance. Our implementation is based on the HyperPlonk library [\[24\]](#page-13-23). To benchmark, we sampled random PLONK circuits with random witnesses and distributed the SNARK generation to up to  $32 \text{ AWS } \text{\text{t}}3.2 \text{x} \text{large machines (8 vC-}$ PUs and 32 GB memory). Our experiments demonstrate that Cirrus enables the proof of PLONK circuits containing  $2^{25}$ gates in just 39 seconds, utilizing a network of 32 machines with modest computation power. With vanilla HyperPlonk, we can only prove circuits containing  $2^{22}$  gates (in more than 110 seconds) using these machines; our results show that Cirrus achieves horizontal scalability. We also evaluate the computation time of the coordinator and show that with our load balancing technique, the computation time of the prover is indeed independent of the size of each sub-circuit.

Furthermore, we compared Cirrus with Hekaton, another accountable distributed SNARK scheme for general circuits. Our comparison shows that for PLONK-friendly tasks such as Pedersen hash, Cirrus is substantially faster than Hekaton by up to  $3 \times$ . We note that for other tasks, such as SHA-256 hash, we cannot outperform Hekaton since the circuit representation of SHA-256 is far less efficient using PLONK gates than using R1CS.

## Our Contribution

Here we summarize our contribution:

- We formally define *accountability* for distributed SNARK generation schemes. In an accountable distributed SNARK protocol, the coordinator can detect malicious node(s) liable for incorrect proof. This notion is particularly useful for deployment in a decentralized setting, like ZKP markets [\[19\]](#page-13-18), where some nodes may be malicious.
- In Section [4,](#page-4-0) we introduce Cirrus, the first accountable distributed SNARK generation scheme that achieves the following properties: (1) truly (optimistically) linear prover time in the size of each sub-circuit for all workers,

<span id="page-2-1"></span>TABLE 1: Comparison between Cirrus and existing distributed SNARK generation protocols. Comm. denotes the communication cost in total. V Time denotes the verifier time. N is the number of gates (or constraints) in the whole circuit, and M is the number of workers.  $T = N/M$  is the size of each sub-circuit. For Setup, "Circuit-specific" denotes that each different circuit requires its own trusted setup, "Universal" denotes that one set of trusted setup parameters works for circuits up to a certain size, and "Transparent" denotes that no trusted setup is required.

Scheme	Worker Time	Coordinator Time	Comm.	Time	Accountable	Setup
Cirrus (Ours)	O(T)	O(M)	$\overline{O(M \log T)}$	$O(\log N)$		Universal
Hekaton [17]	$O(T \log T)$	$O( S +M\log M)$	O(M)	O(1)		Circuit-specific
HyperPianist [18]	O(T)	$O(M \log T)$	$O(M \log T)$	$O(\log N)$		Universal
Pianist $[16]$	$O(T \log T)$	$O(M \log M)$	O(M)	O(1)	$\sim$	Universal
DeVirgo $[14]$	O(T)	N/A	O(N)	$O(\log^2 N)$		Transparant
<b>DIZK</b> [15]	$O(T \log^2 T)$	N/A	O(N)	O(1)		Circuit-specific

(2) truly (optimistically) linear coordinator time in the number of workers, and (3) has a universal trusted setup. Moreover, Cirrus can reuse existing setup parameters of HyperPlonk.

• In Section [5,](#page-10-0) we implement Cirrus and perform experiments to demonstrate its high performance. With our implementation, we can prove circuits of  $2^{25}$  gates in less than 40 seconds using  $32$  AWS  $\text{t}3.2 \text{x}$  Large machines.

# 2. Related work

In this section, we review prior works on distributed proof generation schemes, focusing on asymptotical performance (prover time, verifier time, communications) and accountability. We summarize the comparison in Table [1.](#page-2-1)

DIZK. Wu et. al. [\[15\]](#page-13-14) introduced a distributed approach to zkSNARK provers, focusing on optimizing key operations such as Fast Fourier Transforms (FFTs) and multi-scalar multiplications. Their system scales Groth16 by distributing the computation across multiple machines, and can handle much larger circuits using a cluster of machines. However, a significant limitation is that the communication cost of each machine is linear in the size of the *full circuit* (as opposed to the size of a worker's sub-circuit) due to the distributed FFT algorithm. Another limitation of DIZK is that it uses a circuit-specific setup instead of a universal setup.

DeVirgo. Introduced in zkBridge [\[14\]](#page-13-13), DeVirgo is a distributed variant of Virgo [\[7\]](#page-13-6). Authors of DeVirgo proposed a way to distribute the SumCheck protocol [\[25\]](#page-13-24) and the FRI low-degree test across multiple machines. With  $n$  machines, the proof generation time is reduced by  $1/n$ . The protocol only supports data-parallel circuits (circuits with repeated sub-circuits). Despite these improvements in scalability over DIZK, DeVirgo incurs a per-worker communication cost linear in the size of the full circuit due to its reliance on the FRI low-degree test.

Pianist. [\[16\]](#page-13-15) introduces a distributed proving algorithm for the PLONK SNARK that works for general circuits [\[3\]](#page-13-2), aiming to reduce communication overhead in distributed proving systems. The core innovation in Pianist is the use of bivariate polynomial commitments, which allows for decomposing PLONK's global permutation check (which is responsible for ensuring the correctness of circuit wiring) into local permutation checks for each prover. Pianist achieves only partial accountability (only for data-parallel circuits) and their paper does not formally define accountability. Pianist is also the first distributed SNARK scheme to achieve constant per-node communication. Despite these advances, the prover time of each worker remains *quasi-linear* in the circuit size. In comparison, our protocol achieves linear worker prover time (in the size of the sub-circuit) and stronger accountability for general circuits (not just dataparallel circuits).

Mangrove. [\[26\]](#page-13-25) presents a framework for dividing PLONK into segments of proofs and using folding schemes to aggregate them. While Mangrove shows promising theoretical results with estimated performance comparable to leading SNARKs, it has not been fully implemented or evaluated, especially when it comes to distributing the evaluation of the segments. Additionally, if applied to distributed proving, its techniques would require an inter-worker communication complexity that is linear in the circuit size. In comparison, Cirrus achieves an amortized communication complexity logarithmic in the size of each sub-circuit. Since Mangrove is not implemented, its concrete performance is unknown.

Hekaton. [\[17\]](#page-13-16) proposes a "distribute-and-aggregate" framework to achieve accountable distributed SNARK generation. Specifically, Hekaton leverages memory-checking techniques, where a coordinator constructs a global memory based on the value of the shared wires among circuits. Then, the provers perform consistency checks on their memory access. In this way, the coordinator can detect malicious behavior, and therefore, Hekaton is an accountable scheme. However, like Pianist, the workers' prover time of Hekaton is quasi-linear instead of truly linear in the size of the sub-circuit. Another drawback of Hekaton is that it requires a circuit-specific setup. Moreover, the coordinator's work scales linearly with the global memory size, which could be a potential bottleneck when the number of shared wires among circuits is large. In comparison, the per-worker prover time of Cirrus is "truly" linear in the size of the subcircuit, and the coordinator's workload is independent of the number of shared wires among sub-circuits.

<span id="page-2-0"></span><sup>1.</sup> Pianist is accountable when using independent sub-circuits (e.g. multiple instances of the same circuit).

HyperPianist. [\[18\]](#page-13-17) is a concurrent work with similar distributed permutation test and zero test techniques with multilinear polynomials, while they adopt a different distributed polynomial commitment scheme. However, their protocol is not accountable. There has not been a proof of completeness or soundness or a thorough performance evaluation.

SNARK aggregation schemes. A series of works [\[27\]](#page-13-26), [\[28\]](#page-13-27), [\[29\]](#page-13-28) focuses on SNARK aggregation schemes, where the system uses cryptographic techniques to aggregate proofs of sub-circuits. However, these schemes can only aggregate the proofs when the sub-circuits do not have shared wires or inputs, and cannot be directly used to construct distributed SNARK generation schemes for general circuits.

Collaborative ZK-SNARKs. A recent line of work on *collaborative ZK-SNARKs* [\[30\]](#page-13-29), [\[31\]](#page-13-30), [\[32\]](#page-14-0), [\[33\]](#page-14-1), [\[34\]](#page-14-2) addresses the privacy problem when generating proofs with witnesses from multiple parties using multi-party computation. All these schemes require preprocessing among all servers for each proof, which requires total communication that is linear in the size of the full circuit. Therefore, Collaborative ZK-SNARKs are not as efficient, though they achieve privacy, which is a non-goal for distributed proof generation schemes.

## 3. Preliminaries

In this section, we introduce the key notation, definitions, and foundational tools essential for the completeness and soundness of our protocol. For the interest of space, we move canonical definitions of SNARKs, polynomial interactive oracle proofs (Poly-IOPs), and polynomial commitment schemes (PCS's) to Appendix [A.](#page-14-3) We also refer readers to Appendix [B](#page-15-0) for details of the HyperPlonk protocol.

#### Notation.

- Let  $\lambda$  be the security parameter. If for all  $c > 0$ ,  $f(\lambda)$ is asymptotically smaller than  $\lambda^{-c}$ , then  $f(\lambda)$  is called *negligible* and is denoted negl( $\lambda$ ).
- Let  $\mathbb F$  be a finite field of size  $\Omega(2^{\lambda})$ .
- Let  $B_{\mu} := \{0,1\}^{\mu}$  denote the  $\mu$ -dimension boolean hypercube.
- Let  $\mathbb{F}_{\mu}^{\leq d}[X]$  denote the set of  $\mu$ -variate polynomials where the degree of each variable is not greater than  $d$ . A multivariate polynomial g is *multilinear* if the degree of the polynomial in each variable is at most 1. Each element  $f \in \mathbb{F}_{\mu}^{\leq d}[X]$  would satisfy  $f(x) = h(g_1(x), \dots, g_c(x))$ where h has total degree  $O(d)$  and can be evaluated with an arithmetic circuit of  $O(d)$  gates, and each  $g_i$  is multilinear.
- Let  $\chi_w(x) := \prod_{i=1}^{\mu} (w_i x_i + (1 w_i)(1 x_i))$  be a multilinear Lagrange polynomial over  $B_{\mu}$ .
- Let  $[x] := \sum_{i=1}^{\mu} 2^{i-1} \cdot x_i$ . Let  $\langle v \rangle_m$  be the *m*-bit representation of  $v \in [0, 2<sup>m</sup> - 1]$ .

Useful tools. The following tools are frequently used in the paper.

**Lemma 1** (Multilinear Extension). *For a function*  $g : B<sub>\mu</sub> \to$ F<sub>r</sub>, there is a unique multilinear polynomial  $\tilde{q}$  such that  $\tilde{g}(\boldsymbol{x}) = g(\boldsymbol{x})$  for all  $\boldsymbol{x} \in B_{\mu}$ . The function  $\tilde{g}$  is said to *be the multilinear extension (MLE) of g.*  $\tilde{g}$  *can be expressed as*

$$
\tilde{g}(\boldsymbol{x}) = \sum_{\boldsymbol{a} \in B_{\mu}} f(\boldsymbol{a}) \cdot \chi_{\boldsymbol{a}}(\boldsymbol{x}).
$$

**Lemma 2** (Schwartz-Zippel Lemma). Let  $g : \mathbb{F}^{\ell} \to \mathbb{F}$  be a *nonzero* ℓ*-variate polynomial of degree at most* d*. Then for all*  $\emptyset \neq S \subseteq \mathbb{F}$ ,

$$
\Pr_{\boldsymbol{x}\leftarrow S^{\ell}}[g(\boldsymbol{x})=0]\leq \frac{d}{|S|}.
$$

#### 3.1. Multilinear KZG PCS.

We now present the multilinear KZG PCS which is developed upon the scheme in [\[6\]](#page-13-5), [\[35\]](#page-14-4) for the multilinear polynomials as follows:

- KeyGen $(1^{\lambda}, \mu, d)$ : Generate crs =  $(\tau_1, \cdots, \tau_{\mu}, (U_{\mathbf{b}})$  :=  $(g^{\chi_b(\tau)})_{b \in B_\mu}$ , where  $\tau_1, \cdots, \tau_\mu$  are secrets.
- Commit $(f, \textsf{crs})$ : Suppose  $f(x) = \sum_{\boldsymbol{b} \in B_{\mu}} f_{\boldsymbol{b}} \cdot \chi_{\boldsymbol{b}}(\boldsymbol{x})$ . P computes com :=  $\prod_{\boldsymbol{b}\in B_{\mu}}U_{\boldsymbol{b}}^{f_{\boldsymbol{b}}}.$
- Open $(f, a, \text{crs})$ :  $P$  has  $y := f(a)$  locally stored. Then it evaluates the multilinear degree-1 polynomials  $q_i(x)$  such that  $f(\boldsymbol{x}) - y = \sum_{i \in [\mu]} (x_i - a_i) q_i(\boldsymbol{x})$ . Then it computes  $\pi_i = g^{q_i(\tau)}$ , and takes  $\pi$  as proof of the opening.
- Verify(com,  $\pi$ ,  $a$ ,  $y$ , crs): The verifier checks if  $e(\mathsf{com}/g^y, g) = \prod_{i \in [\mu]} e(\pi_i, g^{\tau_i - a_i}).$

It is shown in [\[6\]](#page-13-5) that to commit a  $\mu$ -variate multilinear polynomial f, the cost of the prover is  $O(2^{\mu})$  group operations. To open f, the cost of the prover is  $O(2^{\mu})$  group operations, and the proof size is  $O(\mu)$  group elements. The cost of the verifier is  $O(\mu)$  bilinear mappings and  $O(\mu)$ group operations.

#### 3.2. Accountability

Definition 1 (Accountable Collaborative Proving System). *For a circuit* C *with* M *workers and one designated coordinator, an* accountable *distributed SNARK generation scheme is a protocol that ensures completeness, knowledge soundness, and succinctness, along with the following key property:*

• *Accountability. For any number of malicious worker provers who produce incorrect proofs, the system enables the coordinator to reliably and accurately identify* all *such malicious participants.*

In an environment where each prover except the coordinator may sabotage the distributed proving process by intentionally sending incorrect information such as wrong proofs, an *accountable* distributed SNARK generation system enables the coordinator  $P_0$  to either (1) generate a valid proof or (2) detect *all* malicious provers. In [\[16\]](#page-13-15) the authors proposed such a scheme for circuits comprising independent sub-circuits and left the construction of accountable distributed SNARK generation schemes that work for single circuits as an open problem.

# <span id="page-4-0"></span>4. **Cirrus**: Accountable and Efficient Distributed SNARK

In this section, we formally describe Cirrus, a distributed SNARK generation scheme with linear prover time where the coordinator can make accountable any malicious behavior. We start by giving the construction to distribute Hyper-Plonk for a general circuit. We first present a distributed multilinear KZG PCS, and how to construct a distributed multivariate SumCheck protocol that is compatible with our distributed PCS. With the distributed multivariate SumCheck protocol, we show how to construct the distributed Hyper-Plonk accordingly.

The distributed HyperPlonk protocol is complete and sound, but it still suffers from the following drawbacks: (1) the coordinator cannot find accountable the malicious prover(s) if the final proof is incorrect, and (2) the coordinator needs to perform  $O(M \log T)$  group and field operations, which is not truly linear in  $M$  and  $T$ . To tackle the first challenge, we propose a verification protocol that allows the coordinator to detect any malicious prover. The core technique in the verification protocol is that the coordinator can evaluate the permutation products of each circuit segment. Note that the additional verification steps would introduce a large overhead for the coordinator if the verification is performed on the fly. We overcome this with an alternative protocol: the coordinator first verifies the final proof to check if there exist malicious nodes. The coordinator will only run the complete check if the verification fails. In this way, the optimistic runtime overhead of the coordinator can be reduced to the verifier time of our protocol. To tackle the second challenge, we delegate part of the work of the coordinator to multiple nodes to reduce the runtime of the coordinator and show how this technique can work along with the optimistic verification. This is done while keeping all the coordinator's computation time linear.

#### 4.1. Distributedly Computable HyperPlonk

Our protocol is built on top of HyperPlonk [\[8\]](#page-13-7), an adaptation of PLONK to the boolean hypercube, using multilinear polynomial commitments to remove the need for FFT computation and achieving linear prover time. We first define polynomials necessary for the protocol.

Defining polynomials. Before running the distributed SNARK, we assume that a circuit  $C$  has been divided into  $M = 2^{\xi}$  different sub-circuits. This can be done by just dividing the PLONK trace  $t$  of the circuit evenly into  $M$ chunks  $\{t^{(b)}\}_{b \in B_{\xi}}$ , where the gates within the same chunk are in the same circuit segment. Then each circuit segment has its own public inputs, addition and multiplication gate selectors, and wiring permutations. Let  $\nu^{(b)}$  be the length of the binary index to represent public inputs for  $P_b$ , i.e.  $2^{\nu^{(b)}} = \ell_x^{(b)}$ . Let  $\mu$  be the length of the binary index to

represent the gates for all workers, i.e.  $2^{\mu} = |\mathcal{C}|/M$ . Let  $s^{(b)}$ represent the gate selection vector for  $P_b$ . Note that there is a wiring permutation between different circuit segments. Let  $s^{(b)}$ ,  $\sigma^{(b)}$ :  $B_{\mu+2} \rightarrow B_{\mu+2}$  and  $\rho^{(b)}$ :  $B_{\mu+2} \rightarrow B_{\xi}$ be the mappings of the circuit wiring for  $\mathcal{P}_b$ , such that  $\{(\sigma^{(\bm{b})}(\bm{c}), \bar{\rho^{(\bm{b})}}(\bm{c})) \; : \; \bm{c} \; \in \; B_{\mu}, \bm{b} \; \in \; B_{\xi}\} \; = \; B_{\mu+2} \times B_{\xi}.$ Specifically, the permutations indicate that  $t_i^{(b)} = t_{\sigma^{(b)}(\langle i \rangle)}^{\rho^{(b)}(\langle i \rangle)}$  $\sigma^{(\bm{b})}(\langle i\rangle)$ for all  $i \in [2^{\mu}], \mathbf{b} \in B_{\xi}$ .

A worker prover  $P_b$  interpolates the following polynomials:

• Two multilinear polynomials  $S_{\text{add}}^{(b)}, S_{\text{mult}}^{(b)} \in \mathbb{F}_{\mu}^{\leq 1}[X]$  such that for all  $i \in [N^{(b)}]$ 

$$
\begin{cases} S^{(\pmb{b})}_{\text{add}}(\langle i \rangle_{\mu}) = s^{(\pmb{b})}_{i} \\ S^{(\pmb{b})}_{\text{mult}}(\langle i \rangle_{\mu}) = 1 - s^{(\pmb{b})}_{i} \end{cases}
$$

• A multilinear polynomial  $F^{(b)} \in \mathbb{F}_{\mu+2}^{\leq 1}$  such that

$$
\begin{cases}\nF^{(b)}(0,0,\langle i \rangle_{\mu}) = t_{3i+1}^{(b)} & i \in [0,N^{(b)}-1] \\
F^{(b)}(0,1,\langle i \rangle_{\mu}) = t_{3i+2}^{(b)} & i \in [0,N^{(b)}-1] \\
F^{(b)}(1,0,\langle i \rangle_{\mu}) = t_{3i+3}^{(b)} & i \in [0,N^{(b)}-1] \\
F^{(b)}(1,\cdots,1,\langle i \rangle_{\nu}) = x_{i+1}^{(b)} & i \in [0,\ell^{(b)}_x-1]\n\end{cases}
$$

• A multilinear polynomial  $I^{(b)} \in \mathbb{F}_{n(b)}^{\leq 1}$  $\sum_{\nu^{(b)}}^{\leq 1}$  [X] such that for all  $i \in [0, \ell_x^{(b)} - 1]$  we have  $I^{(b)}(\langle i \rangle_{\nu^{(b)}}) = x_{i+1}^{(b)}$ .

Distributed multilinear KZG PCS. First, we introduce the distributed multilinear KZG PCS. This protocol enables us to commit and open a multilinear polynomial distributedly. For a multilinear polynomial  $f(x, y) =$  $\sum_{b \in B_{\xi}} f^{(b)}(x) \chi_b(y) \in \mathbb{F}_{\mu+\xi}^{\leq 1}[X]$ , the PCS protocol is described as follows:

- KeyGen: Generate crs =  $(g, (g^{\tau_i})_{i \in [\mu + \xi]}, (U_{c,b} :=$  $g^{\chi_c(\tau_1,\cdots,\tau_\mu)\cdot \chi_b(\tau_{\mu+1},\cdots,\tau_{\mu+\xi})}\Big)_{c\in B_\mu,b\in B_\xi}$  where  $\tau_1, \cdots, \tau_{\mu+\xi}$  are secrets. Note that we can derive  $V_{\mathbf{b}} := g^{\chi_{\mathbf{b}}(\tau_{\mu+1},\cdots,\tau_{\mu+\xi})}$  with crs, which will be useful to  $\mathcal{P}_0$ , since  $\sum_{\mathbf{c} \in B_{\mu}} \chi_{\mathbf{c}} \equiv 1$ .
- Commit $(f, \text{crs})$ : each  $\mathcal{P}_b$  computes com $_b$  :=  $\prod_{\mathbf{c}\in B_{\mu}} U_{\mathbf{c},\mathbf{b}}^{(b)}(c)$  and sends com<sub>b</sub> to  $\mathcal{P}_0$ . Then  $\mathcal{P}_0$ computes com : $= \prod_{\boldsymbol{b} \in B_\xi}$  com $_{\boldsymbol{b}}.$
- Open $(f, \boldsymbol{\alpha}, \boldsymbol{\beta}, \mathsf{crs})$ :
	- 1) Each prover  $P_b$  computes  $z^{(b)} := f^{(b)}(\alpha)$  and  $f^{(b)}(\bm{x}) - f^{(b)}(\bm{\alpha}) := \sum_{i \in [\mu]} q_i^{(b)}(\bm{x}) (x_i - \alpha_i)$ . Then it computes  $\pi_i^{(b)} = g_i^{q_i^{(b)}(\tau_1,\cdots,\tau_\mu) \cdot \chi_b(\tau_{\mu+1},\cdots,\tau_{\mu+\xi})}$  with crs, and sends  $\pi^{(b)}$  and  $z^{(b)}$  to  $\mathcal{P}_0$ .
	- 2)  $\mathcal{P}_0$  computes  $z = f(\boldsymbol{\alpha}, \boldsymbol{\beta}) = \sum_{\boldsymbol{b} \in B_{\xi}} z^{(\boldsymbol{b})} \cdot \chi_{\boldsymbol{b}}(\boldsymbol{\beta}).$   $\mathcal{P}_0$ decomposes

$$
f(\boldsymbol{\alpha},\boldsymbol{y})-f(\boldsymbol{\alpha},\boldsymbol{\beta})=\sum_{j\in[\xi]}q_j(\boldsymbol{y})(y_j-\beta_j).
$$

Then it computes  $\pi_{\mu+j} = g^{q_j(\tau_{\mu+1},\dots,\tau_{\mu+\xi})}$  with  $V_b$ .  $\mathcal{P}_0$  also computes  $\pi_i = \prod_{\boldsymbol{b} \in B_{\xi}} \pi_i^{(\boldsymbol{b})}$  for all  $i \in [\mu]$ . 3) Then  $\mathcal{P}_0$  sends  $\boldsymbol{\pi} := (\pi_1, \cdots, \pi_{\mu+\xi})$  and z to  $\mathcal{V}$ .

• Verify(com, π, α, β, z, crs): The verifier checks if 
$$
e(\text{com}/g^z, g) = \prod_{i \in [\mu]} e(\pi_i, g^{\tau_i - \alpha_i})
$$
.  
\n
$$
\Pi_{j \in [\xi]} e(\pi_{\mu+j}, g^{\tau_{\mu+j} - \beta_j}).
$$

Here we summarize the complexity of the distributed multilinear KZG PCS:

- $P_i$  Time:  $O(2^{\mu})$
- $\mathcal{P}_0$  Time:  $O(2^{\xi} \cdot \mu)$
- Verifier Time:  $O(\mu + \xi)$
- Communication:  $O(\mu + \xi)$ G

Distributed multivariate SumCheck Poly-IOP. Here we describe the distributed multivariate SumCheck Poly-IOP. Suppose each prover  $P_b$  has a multivariate polynomial  $f^{(b)} \in \mathbb{F}_{\mu}[\boldsymbol{X}]$ . The provers want to show to the verifier that a multivariate polynomial  $f(x) := h(g_1(x), \dots, g_c(x)) \in \mathbb{F}_{\mu + \xi}[X]$  satisfies  $\sum_{x \in B_{\mu + \xi}} f(x) = v$ , where each  $g_i$  is multilinear and  $h$  can be evaluated using a arithmetic circuit with  $O(d)$  gates. Suppose each prover  $P_b$  has access to  $g_i^{(\boldsymbol{b})}$  for  $i \in [c]$ , where  $g_i^{(\boldsymbol{b})}(\boldsymbol{y}) = g_i(\boldsymbol{y}, \boldsymbol{b})$ . We further define  $f^{(b)}(y) = f(y, b) = h(g_1^{(b)}(y), \cdots, g_c^{(b)}(y)).$  The protocol is described as follows:

- 1) For each  $i \in |\mu|$ : a) Let  $\alpha_{i-1} = (\alpha_1, \cdots, \alpha_{i-1})$ b) Each  $P_b$  computes and sends to  $P_0$  the polynomial  $r_i^{(\boldsymbol{b})}(x) := \sum_{\boldsymbol{w}\in B_{\mu-i}} f^{(\boldsymbol{b})}(\boldsymbol{\alpha}_{i-1},x,\boldsymbol{w}).$ 
	- c) Then  $P_0$  adds

$$
r_i := \sum_{\boldsymbol{b} \in B_{\xi}} r_i^{(\boldsymbol{b})} = \sum_{\boldsymbol{b} \in B_{\xi}, \boldsymbol{w} \in B_{\mu - i}} f^{(\boldsymbol{b})}(\boldsymbol{\alpha}_{i-1}, x, \boldsymbol{w})
$$

$$
= \sum_{\boldsymbol{w} \in B_{\mu - i}} \sum_{\boldsymbol{b} \in B_{\xi}} f(\boldsymbol{\alpha}_{i-1}, x, \boldsymbol{w}, \boldsymbol{b})
$$

and sends the oracle of  $r_i$  to  $\mathcal V$ .

- d) V checks if  $v = r_i(0) + r_i(1)$ , and samples and sends to  $\mathcal{P}_0$  a random  $\alpha_i \leftarrow \mathbb{F}$ . Then  $\mathcal{V}$  sets  $v := r_i(\alpha_i)$ .
- 2) Let  $\boldsymbol{\alpha} = (\alpha_1, \cdots, \alpha_\mu)$ .
- 3)  $\mathcal{P}_0$  receives  $g_i^{(b)}(\alpha)$  for  $i \in [c]$  from each  $\mathcal{P}_b$ .  $\mathcal{P}_0$  first construct

$$
\tilde{g}_i(\boldsymbol{y}) :=\hspace{-0.5mm}g(\boldsymbol{\alpha}, \boldsymbol{y}) = \sum_{\boldsymbol{b} \in B_{\mu}} g_i(\boldsymbol{\alpha}, \boldsymbol{b}) \chi_{\boldsymbol{b}}(\boldsymbol{y}) \\ = \sum_{\boldsymbol{b} \in B_{\mu}} g_i^{(\boldsymbol{b})}(\boldsymbol{\alpha}) \chi_{\boldsymbol{b}}(\boldsymbol{y})
$$

Then  $\mathcal{P}_0$  has  $\tilde{f}(\mathbf{y}) = f(\boldsymbol{\alpha}, \mathbf{y}) = h(\tilde{g}_1(\mathbf{y}), \cdots, \tilde{g}_c(\mathbf{y})).$ 4)  $\mathcal{P}_0$  and V perform Multivariate SumCheck on  $\tilde{f}$  with target value v. Denote the challenge as  $\beta$ . Note that in the final round, the verifier queries  $g_i(\alpha, \beta)$  for  $i \in [c]$ and calculates  $f(\alpha, \beta)$  itself.

We give an example of how to perform SumCheck with  $f^{(b)} = f_1^{(b)} \cdot f_2^{(b)}$ , to illustrate the sum-check protocol on  $h(f_1, f_2, \dots, f_k)$ . In Step 3,  $\mathcal{P}_b$  opens  $f^{(b)}$ , and compute  $g(\bm{y}):=\sum_{\bm{b}\in B_\xi}(v^{(\bm{b})}_1\!\cdot\!\chi_{\bm{b}}(\bm{y}))\!\cdot\!\sum_{\bm{b}\in B_\xi}(v^{(\bm{b})}_2\!\cdot\!\chi_{\bm{b}}(\bm{y}))$ . In Step 4, V and the provers open  $f_1(\alpha, \beta) \cdot f_2(\alpha, \beta)$  at the end.

We summarize the complexity of the distributed multivariate SumCheck Poly-IOP as follows:

- $P_i$  Time:  $O(2^{\mu} \cdot d \log^2 d)$
- $\mathcal{P}_0$  Time:  $O(2^{\xi} \cdot d \log^2 d + \mu \cdot 2^{\xi} \cdot d)$
- Verifier Time:  $O((\mu + \xi) \cdot d)$
- Communication:  $O((\mu + \xi) \cdot d)\mathbb{F}$

Distributed multivariate ZeroTest Poly-IOP. Suppose each prover  $P_b$  has a multivariate polynomial  $f^{(b)}(x) \in$  $\mathbb{F}_{\mu}^{\leq d}[\hat{\boldsymbol{X}}]$ . The provers wants to show to the verifier that  $f^{(b)}(x) = 0$  for all  $b \in B_{\xi}$  and  $x \in B_{\mu}$ . The protocol is described as follows:

- 1) V samples and sends to  $P_0$  two random vectors  $r \leftarrow \mathbb{F}^{\mu}$ and  $\mathbf{r}_0 \leftarrow \mathbb{F}^{\xi}$ .
- 2)  $\mathcal{P}_0$  sends r to each  $\mathcal{P}_b$ . Note that when we apply the Fiat-Shamir heuristic to make the argument non-interactive, communication in this step is no longer needed.
- 3) Each  $\mathcal{P}_b$  sets  $\tilde{f}^{(b)}(x) := f^{(b)}(x) \cdot \chi_r(x) \cdot \chi_{r_0}(b)$ .
- 4) The provers and  $V$  run distributed multivariate SumCheck protocol on  $\tilde{f}^{(b)}(x)$  with target value 0.

To improve the efficiency of the protocol, the verifier has oracle to  $f(x, y) := \sum_{\boldsymbol{b} \in B_{\xi}} f^{(\boldsymbol{b})}(\boldsymbol{x}) \cdot \chi_{\boldsymbol{b}}(\boldsymbol{y})$  and knows  $\chi_{\bm r}(\bm x)\cdot \chi_{\bm r_0}(\bm y)$ . In Step 4 of SumCheck,  $\mathcal P_0$  will evaluate  $\chi_{\mathbf{r}_0}(\mathbf{y})$  over the boolean hypercube  $B_\xi$  using dynamic pro-gramming techniques [\[6\]](#page-13-5) and then add them up. Since  $V$  has oracle access to  $f$  and can efficiently evaluate  $\chi_{\bm r}(\bm x)\!\cdot\!\chi_{\bm r_0}(\bm y)$ at a random point in  $O(\mu + \xi)$  time, the protocol is succinct.

We present the proof of soundness and completeness for the ZeroTest protocol as an example, as the proofs for the distributed protocols follow analogous arguments found in the classic protocols.

<span id="page-5-0"></span>Proposition 1. *The Distributed Multivariate* ZeroTest *Poly-IOP presented above is complete and sound.*

*Proof.* Let  $F(x, y) := \sum_{\boldsymbol{b} \in B_{\xi}, \boldsymbol{c} \in B_{\mu}} f^{(\boldsymbol{b})}(\boldsymbol{c}) \cdot \chi_{\boldsymbol{c}}(\boldsymbol{x}) \cdot \chi_{\boldsymbol{b}}(\boldsymbol{y}).$ If  $f^{(b)}(c)$  is identically zero for all  $b \in B_{\xi}$  and  $c \in B_{\mu}$ , F is identically zero, therefore,  $F(\mathbf{r}, \mathbf{r}_0) = 0$  and the SumCheck will always pass. On the other hand, if  $F$  is not identically zero, by Schwartz-Zippel lemma  $F(r, r_0) = 0$  holds w.p. at most  $(\mu + \xi)d/|\mathbb{F}|$ , which is negligible.  $\Box$ 

Distributed multivariate PermTest Poly-IOP. Let  $\sigma^{(b)}$  :  $B_{\mu} \rightarrow B_{\mu}$  and  $\rho^{(b)} : B_{\mu} \rightarrow B_{\xi}$  be two mappings such that  $\{(\sigma^{(\boldsymbol{b})}(\boldsymbol{c}),\rho^{(\boldsymbol{b})}(\boldsymbol{c})) : \boldsymbol{c}\in B_{\mu}, \boldsymbol{b}\in B_{\xi}\}=B_{\mu}\times B_{\xi}.$ 

Each prover  $P_b$  has a multivariate polynomial  $f^{(b)} \in$  $\mathbb{F}_{\mu}^{\leq d}[X]$ . The provers want to show to the verifier that  $f^{(\boldsymbol{b})}(\boldsymbol{c}) = f^{(\rho^{(\boldsymbol{b})}(\boldsymbol{c}))}(\sigma^{(\boldsymbol{b})}(\boldsymbol{c}))$  for all  $\boldsymbol{b} \in B_{\xi}$  and  $\boldsymbol{c} \in B_{\mu}$ . Here we introduce the protocol in the more complicated case where  $\rho^{(b)}(c)$  is not identically b for all  $c \in B_\mu$ .

We define the multivariate polynomials  $s, s_{\mu}^{(b)}, s_{\xi}^{(b)} \in$  $\mathbb{F}_{\mu}^{\leq 1}[X]$  where  $s(x) := [x]$ ,  $s_{\mu}^{(b)}(x) := [\sigma^{(b)}(x)]$  and  $s_{\varepsilon}^{(\boldsymbol{b})}$  $\zeta_{\xi}^{(b)}(x) := [\rho^{(b)}(x)].$  The protocol then goes as follows:

- 1) V samples and sends to the coordinator  $\gamma_{\mu}, \gamma_{\xi}, \delta \leftarrow \mathbb{F}$ .
- 2) Let  $f_1^{(b)}(x) := f^{(b)}(x) + \gamma_{\mu} \cdot s(x) + \gamma_{\xi} \cdot [b] + \delta$  and  $f_2^{(\bm{b})}(\bm{x})\ :=\ f^{(\bm{b})}(\bm{x})+\gamma_{\mu}\cdot s_{\mu}^{(\bm{b})}(\bm{x})+\gamma_{\xi}\cdot s_{\xi}^{(\bm{b})}(\bm{x})+\delta.$ Each prover  $P_b$  builds a multilinear polynomial  $z^{(b)} \in$  $\mathbb{F}_{\mu+1}^{\leq 1}[\boldsymbol{X}]$  such that for all  $\boldsymbol{x} \in B_{\mu}$

$$
\begin{cases} z^{(b)}(0,\textit{\textbf{x}})=f_1^{(b)}(\textit{\textbf{x}})/f_2^{(b)}(\textit{\textbf{x}}) \\ z^{(b)}(1,\textit{\textbf{x}})=z^{(b)}(\textit{\textbf{x}},0)\cdot z^{(b)}(\textit{\textbf{x}},1), \ \textit{\textbf{x}}\neq\textbf{1} \\ z^{(b)}(1,\textbf{1})=0 \end{cases}
$$

Let  $w_1^{(b)}(x) := z^{(b)}(1,x) - z^{(b)}(x,0) \cdot z^{(b)}(x,1)$  and  $w^{(\bm{b})}_{2}(\bm{x}) := f^{(\bm{b})}_{2}(\bm{x}) \cdot z^{(\bm{b})}(0,\bm{x}) - f^{(\bm{b})}_{1}(\bm{x}).$ 

3) Each  $P_b$  sends  $z^{(b)} := z^{(b)}(1, 1, \dots, 1, 0)$  to  $P_0$ . We have  $\prod_{b \in B_{\mu}} z^{(b)} = 1$ . Then  $\mathcal{P}_0$  interpolates a multilinear polynomial  $z \in \mathbb{F}_{\xi+1}^{\leq 1}[\boldsymbol{X}]$  such that

$$
\begin{cases} z(0, y) = z^{(y)} \\ z(1, y) = z(y, 0) \cdot z(y, 1) \end{cases}
$$

- Let  $w_3(y) := z(1, y) z(y, 0) \cdot z(y, 1)$  and  $w_4^{(b)}(x) :=$  $z(0,\bm{b}) - z^{(\bm{b})}(\bm{x},0) \cdot \chi_{(1,1,\cdots,1,0)}(\bm{x},0).$
- 4) The provers and  $V$  run distributed multivariate ZeroTest on  $\{w_1^{(b)}\}, \{w_2^{(b)}\}$  and  $\{w_4^{(b)}\}.$   $\mathcal{P}_0$  and  $\mathcal{V}$  run multivariate ZeroTest on  $w_3$ .

Distributed HyperPlonk Poly-IOP. We construct a distributedly computable HyperPlonk Poly-IOP as follows:

- Input Constraint:  $\mathcal V$  ensures that  $F^{(b)}(1,\dots,1,\boldsymbol{x})$   $I^{(b)}(x) = 0$  for all  $x \in B_{\nu}$  and  $b \in B_{\xi}$  with distributed multilinear ZeroTest.
- Output Constraint:  $V$  queries  $F^{(1,\cdots,1)}(1,0,\langle N^{(b)}-1\rangle_\mu)$ and checks if it is zero.
- Gate Constraint: Define a multivariate polynomial

 $G^{(\bm{b})}(\bm{x}) := S^{(\bm{b})}_{\mathsf{add}}(\bm{x}) (F^{(\bm{b})}(0,0,\bm{x}) + F^{(\bm{b})}(0,1,\bm{x})) +$  $S_{\mathsf{mult}}^{(\bm{b})}(\bm{x}) (F^{(\bm{b})}(0,0,\bm{x}) \cdot F^{(\bm{b})}(0,1,\bm{x})) - F^{(\bm{b})}(1,0,\bm{x}).$ 

V checks that  $G^{(b)}(x) = 0$  for all  $x \in B_{\mu}$  and  $b \in B_{\xi}$ with distributed multivariate ZeroTest.

• Wiring Constraint:  $V$  verifies that  $F^{(b)}(x) =$  $F^{(\rho^{(\mathbf{b})}(\bm{x}))}(\sigma^{(\mathbf{b})}(\bm{x}))$  for all  $\bm{x}$   $\in$   $B_{\mu+2}$  and  $\bm{b}$   $\in$   $B_{\xi}$ with distributed multivariate PermTest.

Batch openings. The batch opening is a technique introduced in [\[3\]](#page-13-2), [\[8\]](#page-13-7) that reduces prover and verifier time. It is especially useful when the practical bottleneck for the prover and the verifier is polynomial opening and verification, instead of field operations. Here we introduce the distributed batch opening protocol.

Assume there are  $2^{\rho}$  distributed polynomials  ${f_{\boldsymbol{v}}^{(\boldsymbol{b})}}_{\boldsymbol{v}\in B_{\rho},\boldsymbol{b}\in B_{\xi}}$ , and  $2^{\rho}$  evaluation points  $\{(\alpha_{\boldsymbol{v}},\beta_{\boldsymbol{v}})\}_{\boldsymbol{v}\in B_{\rho}}$ , with opening values  $\{z_v\}_{v \in B_\rho}$ . Then the distributed batch opening and verification protocol is described as follows:

- The verifier V samples and sends to  $\mathcal{P}_0$   $t \stackrel{\$}{\leftarrow} \mathbb{F}^{\rho}$ .
- V computes the target sum  $s := \sum_{v \in B_\rho} \chi_v(t) \cdot z_v$ .
- Each worker prover  $P_b$  defines a multilinear polynomial  $g^{(\boldsymbol{b})}(\boldsymbol{v},\boldsymbol{c}):=\chi_{\boldsymbol{v}}(\boldsymbol{t})\cdot f^{(\boldsymbol{b})}_{\boldsymbol{v}}(\boldsymbol{c})$  for all  $\boldsymbol{c}\in B_{\mu}$  and  $\boldsymbol{v}\in B_{\rho}$ .
- Each worker prover  $P_b$  defines a multilinear polynomial  $h^{(\mathbf{b})}(\mathbf{v},\mathbf{c}) := \chi_{\mathbf{c}}(\mathbf{\alpha}_{\mathbf{v}}) \cdot \chi_{\mathbf{b}}(\mathbf{\beta}_{\mathbf{v}})$  for all  $\mathbf{c} \in B_{\mu}$  and  $\mathbf{v} \in B_{\rho}$ .
- The provers and the verifier runs a distributed multivariate SumCheck protocol for  $\{g^{(b)} \cdot h^{(b)}\}$  with target s.

Proposition 2. *The Distributed Batch Opening protocol previously presented is sound and accountable, if the distributed* SumCheck *and KZG PCS are accountable.*

*Proof.* The soundness of the protocol depends on the soundness of the distributed SumCheck, which we prove in Propo-sition [4.](#page-9-0) This means that  $s = \sum_{b \in B_{\xi}, v \in B_{\rho}, c \in b_{\mu}} g^{(b)}(v, c)$ .  $h^{(b)}(\boldsymbol{v}, \boldsymbol{c})$ , which is the same equality that the soundness of the Batch Opening protocol in HyperPlonk relies on. Since HyperPlonk is sound, so is ours.

Given that the protocol is sound, the coordinator  $P_0$  will be able to detect when a worker is malicious. As shown in Proposition [4,](#page-9-0) the SumCheck is accountable, and  $h^{(b)}(v, c)$ can be computed efficiently by the coordinator; therefore, this protocol is also accountable. П

#### 4.2. Making Malicious Nodes Accountable

The distributed Poly-IOP and PCS schemes presented previously cannot enable the coordinator to find which node is malicious if the final proof is incorrect. In this part, we discuss how to make malicious nodes accountable.

Accountable distributed multivariate PermTest Poly-IOP. One challenge in constructing such systems for single circuits is that, in distributed multivariate PermTest, the values  $z^{(b)}$  of each circuit segment are generated by each prover and cannot be verified by the coordinator. To deal with this challenge, we let the coordinator calculate  $z^{(b)}$  after evaluating the circuit, and then each prover is required to open their polynomial commitment of  $z^{(b)}$  at  $(1, 1, \dots, 1, 0)$ to the coordinator during PermTest. More specifically, in PermTest, each prover  $P_b$  needs to construct two polynomials  $f_1^{(b)}$  and  $f_2^{(b)}$  from the committed polynomial  $f^{(b)}$ . And  $z^{(b)}$  is evaluated using  $z^{(b)}(x) = \prod_{x \in B_{\mu}} f_1^{(b)}(x) / f_2^{(b)}(x)$ . If  $f^{(b)} \in \mathbb{F}_{\mu}^{\leq d}[X]$ , then the evaluation of  $f^{(b)}$  over  $B_{\mu}$  can be efficiently evaluated in  $O(d \cdot 2^{\mu})$  time. Once  $f^{(b)}$  over  $B_{\mu}$  is evaluated,  $z^{(b)}$  can be evaluated in  $O(2^{\mu})$  time, given that  $s, s_{\mu}^{(b)}, s_{\varepsilon}^{(b)}$  $\zeta$ <sup>(*o*)</sup> has been preprocessed by the coordinator.

However, it would still be a challenge to the coordinator to evaluate and store  $f^{(b)}$  over  $B_{\mu}$  for all  $b \in B_{\xi}$ , and we come up with a way to circumvent this problem. We observe that the only place where PermTest is called in the distributed SNARK protocol is during Wiring Constraint, where  $F^{(b)}$ , the polynomial interpolated with the transcript of each prover, is the  $f^{(b)}$  in the PermTest. Recall that at the beginning of the distributed proof generation, the coordinator and all provers have to evaluate the entire circuit in plaintext, and therefore  $P_0$  would have all evaluations of  $F^{(b)}$  over  $B_{\mu}$  for all  $b \in B_{\xi}$ . Therefore,  $\mathcal{P}_0$  can store  $\{F^{(b)}(\boldsymbol{x}) : \boldsymbol{x} \in B_{\mu}, \boldsymbol{b} \in B_{\xi}\}\$  during the circuit evaluation, and use them to calculate  $z^b$ .

Once  $P_0$  has  $z^b$  locally computed, it is able to verify the  $z^{(b)}(1,1,\dots,1,0)$  computed by  $\mathcal{P}_b$ . To do so, prover  $\mathcal{P}_b$ will send the proof of the opening of  $z^{(b)}$  at  $(1, 1, \dots, 1, 0)$ to  $P_0$ , and  $P_0$  will verify the correctness of the multivariate polynomial opening. The extra work of  $P_0$  this step is  $O(\mu \cdot$  $(2^{\xi})$  bilinear mapping. The extra work of each other prover is  $O(\mu)$  field operation and group exponentiation. The extra amortized communication is  $O(\mu)\mathbb{G}$ .

We summarize the technique in the following modification of distributed multivariate PermTest protocol: We assume that  $\mathcal{P}_0$  has  $f^{(b)}(\boldsymbol{x}), s(\boldsymbol{x}), s_{\mu}^{(b)}(\boldsymbol{x}), s_{\xi}^{(b)}$  $\zeta^{(0)}(x)$  locally ready for all  $\mathbf{b} \in B_{\xi}, \mathbf{x} \in B_{\mu}$ .

Once again we consider the multivariate polynomials  $(s, s_{\mu}^{(b)}, s_{\xi}^{(b)})^{\top} \in \mathbb{F}_{\mu}^{\leq 1}[X]$  where  $s(x) := [x], \ s_{\mu}^{(b)}(x) :=$  $[\sigma^{(b)}(x)]$  and  $s^{(b)}_s$  $\mathcal{L}_{\xi}^{(\bm{b})}(\bm{x}) := [\rho^{(\bm{b})}(\bm{x})].$  The modified protocol steps are highlighted in blue.

1) V samples and sends to the coordinator  $\gamma_{\mu}, \gamma_{\xi}, \delta \leftarrow \mathbb{F}$ . 2) Let  $f_1^{(b)}(x) := f^{(b)}(x) + \gamma_{\mu} \cdot s(x) + \gamma_{\xi} \cdot [b] + \delta$  and  $f_{2}^{(\bm{b})}(\bm{x})\ :=\ f^{(\bm{b})}(\bm{x})+\gamma_\mu\cdot s_\mu^{(\bm{b})}(\bm{x})+\gamma_\xi\cdot s_\xi^{(\bm{b})}(\bm{x})+\delta.$ Each prover  $P_b$  builds a multilinear polynomial  $z^{(b)} \in$  $\mathbb{F}_{\mu+1}^{\leq 1}[\bm{X}]$  such that for all  $\bm{x} \in B_{\mu}$ 

$$
\begin{cases} z^{(b)}(0,\textit{\textbf{x}})=f_1^{(b)}(\textit{\textbf{x}})/f_2^{(b)}(\textit{\textbf{x}}) \\ z^{(b)}(1,\textit{\textbf{x}})=z^{(b)}(\textit{\textbf{x}},0)\cdot z^{(b)}(\textit{\textbf{x}},1), \ \textit{\textbf{x}}\neq \textbf{1} \\ z^{(b)}(1,\textbf{1})=0 \end{cases}
$$

Let  $w_1^{(b)}(x) := z^{(b)}(1,x) - z^{(b)}(x,0) \cdot z^{(b)}(x,1)$  and  $w_2^{(\bm{b})}(\bm{x}) := f_2^{(\bm{b})}(\bm{x}) \cdot z^{(\bm{b})}(0,\bm{x}) - f_1^{(\bm{b})}(\bm{x}).$ 

3)  $\mathcal{P}_0$  locally evaluates

$$
z^{(\bm{b})}:=\prod_{\bm{x}\in B_{\bm{\mu}}}f_1^{(\bm{b})}(\bm{x})/f_2^{(\bm{b})}(\bm{x})
$$

for all  $\mathbf{b} \in B_{\xi}$  in  $O(|\mathcal{C}|)$  field operations. Then each prover  $\mathcal{P}_{\mathbf{b}}$  sends com<sub>z(b)</sub> to  $\mathcal{P}_{0}$ , and opens  $z^{(\mathbf{b})}(1,1,\cdots,1,0)$ , with the target value  $z^{(b)}$ . If the opening fails,  $P_b$  is malicious.

4) We have  $\prod_{b \in B_\mu} z^{(b)} = 1$ . Then  $\mathcal{P}_0$  interpolates a multilinear polynomial  $z \in B_{\xi+1}^{\leq 1}$  such that

$$
\begin{cases} z(0, y) = z^{(y)} \\ z(1, y) = z(y, 0) \cdot z(y, 1) \end{cases}
$$

Let  $w_3(y) := z(1, y) - z(y, 0) \cdot z(y, 1)$  and  $w_4^{(b)}(x) :=$ Let  $w_3(y) := z(1, y) - z(y, 0) \cdot z(y, 1)$  and  $w_4$ <br>  $z(0, b) - z^{(b)}(x, 0) \cdot \chi_{(1,1,\dots,1,0)}(x, 0).$ 

5) The provers and V run distributed multivariate ZeroTest<br>
(b) can  $\{w^{(b)}\}$  and  $\{w^{(b)}\}$  R and V run multivariate on  $\{w_1^{(0)}\}, \{w_2^{(0)}\}$  and  $\{w_4^{(0)}\}.$   $\mathcal{P}_0$  and  $\mathcal{V}$  run multivariate ZeroTest on  $w_3$ .

Accountable distributed multivariate SumCheck and multilinear KZG PCS. Recall that in the distributed SumCheck and distributed multilinear KZG PCS protocol, the coordinator need to receive intermediate results from each prover  $P_b$  and compute the complete proof depending on these intermediate results. Specifically, the coordinator needs to process the SumCheck result of  $f^{(b)}$  from each node in distributed multilinear SumCheck protocol, and needs to process the separate sub-proofs from each nodes during the polynomial opening in the distributed KZG PCS. To make the malicious nodes accountable, we note that the coordinator can verify the intermediate results submitted by the nodes.

In the distributed KZG PCS, the coordinator can detect malicious prover as follows:

- KeyGen, Commit $(f, \text{crs})$ , Verify(com,  $\pi, \alpha, \beta, z, \text{crs}$ ): Same as the previous protocol.
- Open $(f, \boldsymbol{\alpha}, \boldsymbol{\beta}, \mathsf{crs})$ : 1) Each prover  $P_b$  computes  $z^{(b)} := f^{(b)}(\alpha)$  and  $f^{(b)}(\bm{x}) - f^{(b)}(\bm{\alpha}) := \sum_{i \in [\mu]} q_i^{(b)}(\bm{x}) (x_i - \alpha_i)$ . Then it computes  $\pi_i^{(b)} = g_i^{q_i^{(b)}(\tau_1,\dots,\tau_\mu) \cdot \chi_b(\tau_{\mu+1},\dots,\tau_{\mu+\xi})}$  with crs, and sends  $\pi^{(b)}$  and  $z^{(b)}$  to  $\mathcal{P}_0$ .
	- 2)  $P_0$  checks if  $e(\text{com}_{f^{(b)}}, g) \cdot e(g^{-z^{(b)}}, V_b) =$  $\prod_{i\in[\mu]}e(\pi_i^{(\mathbf{b})},g^{\tau_i-\alpha_i}).$  If this check fails,  $\mathcal{P}_{\mathbf{b}}$  is dishonest.
	- 3)  $\mathcal{P}_0$  computes  $z = f(\boldsymbol{\alpha}, \boldsymbol{\beta}) = \sum_{\boldsymbol{b} \in B_{\xi}} z^{(\boldsymbol{b})} \cdot \chi_{\boldsymbol{b}}(\boldsymbol{\beta}).$   $\mathcal{P}_0$ decomposes

$$
f(\boldsymbol{\alpha},\boldsymbol{y})-f(\boldsymbol{\alpha},\boldsymbol{\beta})=\sum_{j\in[\xi]}q_j(\boldsymbol{y})(y_j-\beta_j).
$$

Then it computes  $\pi_{\mu+j} = g^{q_j(\tau_{\mu+1},\dots,\tau_{\mu+\xi})}$  with  $V_b$ .  $\mathcal{P}_0$  also computes  $\pi_i = \prod_{\boldsymbol{b} \in B_{\xi}} \pi_i^{(\boldsymbol{b})}$  for all  $i \in [\mu]$ . 4) Then  $\mathcal{P}_0$  sends  $\boldsymbol{\pi} := (\pi_1, \dots, \pi_{\mu+\xi})$  and z to V.

In the distributed SumCheck protocol, the coordinator runs full verification for the intermediate results of each node. Recall that each prover  $P_b$  has a multivariate polynomial  $f^{(b)} \in \mathbb{F}_{\mu}^{\leq d}[X]$ , and that the provers want to show the verifier that  $\sum_{b \in B_{\xi}, c \in B_{\mu}} f^{(b)}(c) = v$ . Now the coordinator has distributed target  $v^{(b)}$  for each prover  $P_b$ , and they satisfy  $\sum_{b \in B_{\xi}} v^{(b)} = v$ . Here we assume that  $\mathcal{P}_0$  has the commitment of com  $f^{(b)}$ . The accountable distributed multilinear SumCheck protocol is described as follows:

1) For each  $i \in [\mu]$ :

- a) Each  $P_b$  computes and sends to  $P_0$  the polynomial  $r_i^{(\bm{b})}(x) := \sum_{\bm{w} \in B_{\mu-i}} f^{(\bm{b})}(\alpha_1, \cdots, \alpha_{i-1}, x, \bm{w})$ .
- b) For each  $\mathbf{b} \in B_{\xi}$ ,  $\mathcal{P}_0$  checks if  $v^{(\mathbf{b})} = r_i^{(\mathbf{b})}(0)$  +  $r_i^{(b)}(1)$ . If the check fails,  $P_b$  is malicious. Then  $P_0$ sets  $v^{(b)} := r_i^{(b)}(\alpha_i)$ .
- 2)  $\mathcal{P}_0$  receives  $\delta_b := f^{(b)}(\alpha)$  from each  $\mathcal{P}_b$ .  $\mathcal{P}_b$  opens  $f^{(b)}$ at  $\alpha$ , and  $\mathcal{P}_0$  verifies the proof of the opening. If the verification fails,  $P_b$  is malicious.
- 3)  $\mathcal{P}_0$  has a multivariate polynomial  $g(\mathbf{y}) := f(\alpha, \mathbf{y}) =$  $\mathcal{P}_0$  has a multivariate polynomial  $g(\mathbf{y}) := f(\boldsymbol{\alpha}, \mathbf{y}) = \sum_{\mathbf{b} \in B_{\xi}} \delta_{\mathbf{b}} \cdot \chi_{\mathbf{b}}(\mathbf{y}).$
- 4)  $P_0$  and V perform SumCheck on g with target value v. Note that in the final round, the verifier queries  $f(\alpha, \beta)$ .

Accountable distributed multivariate ZeroTest Poly-**IOP.** Suppose each prover  $P_b$  has a multivariate polynomial  $f^{(b)}(x) \in \mathbb{F}_{\mu}^{\leq d}[X]$ . The provers wants to show to the verifier that  $f^{(b)}(x) = 0$  for all  $b \in B_{\xi}$  and  $x \in B_{\mu}$ . The protocol is described as follows:

- 1) V samples and sends to  $P_0$  two random vectors  $r \leftarrow \mathbb{F}^{\mu}$ and  $\mathbf{r}_0 \leftarrow \mathbb{F}^{\xi}$ .
- 
- 2)  $\mathcal{P}_0$  sends r to each  $\mathcal{P}_b$ .<br>3) Each  $\mathcal{P}_b$  sets  $\tilde{f}^{(b)}(x) := f^{(b)}(x) \cdot \chi_r(x) \cdot \chi_{r_0}(b)$ .
- 4) The provers and  $V$  run accountable distributed multivariate SumCheck protocol on  $\tilde{f}^{(b)}(x)$  with target value 0 for each prover.
- 5) The coordinator verifies the SumCheck protocol. If the verification fails, the prover constructs  $\tilde{f}^{(b)}(x)$  for each  $\mathbf{b} \in B_{\xi}$  and check if the computation of  $\mathcal{P}_{\mathbf{b}}$  is correct.

We observe that the coordinator does not need to verify all the computations on the fly. Instead, our coordinator could just run the verifier's program after all worker nodes have computed all intermediate results. The soundness of the verification is based on the soundness of the verifier. If the verification fails, at least one of the worker provers is malicious, and the prover will re-run the program of each node and check all the communication to determine the malicious nodes. Following this observation, we can *defer* the check whether anyone is malicious, which is carried out by the coordinator, to the end of each accountable protocol, which is a technique we refer to as *optimistic check*.

There are two advantages of performing optimistic checks compared with performing checks in the middle of each protocol. On one hand, since the verifier check of each protocol is succinct, if no one is malicious, the protocol will have minimal overhead to the vanilla Cirrus protocol without accountability. On the other hand, if some workers are malicious since the coordinator keeps track of all the communication, it can still find out exactly who is malicious by running the checks based on the communication and therefore preserves the accountability property.

## 4.3. Load Balancing and Optimistic Accountability

By far, we have achieved linear prover time for each worker. However, we still have a coordinator time of  $O(M \log T)$ , T being the size of each sub-circuit, and M being the number of worker nodes. In this part, we discuss how to eliminate the  $(\log T)$  term while maintaining the accountability property.

We note that the coordinator's work of summing up group or field elements in distributed SumCheck and distributed KZG can be distributed. However, naively distributing this step (e.g., having each node add one element) could introduce a larger round complexity. Instead, only a subset of nodes is required for computing. We demonstrate this idea with the following example. Suppose each node has A elements, and the coordinator would finally need to get the sum of all elements. We divide the  $M$  nodes into  $k$ groups and select a leader of each group. In the first round, the leader in each group adds up all  $MA/k$  elements in its group. In the second round, the coordinator adds up  $k$ elements from the leaders. The cost of the leader of each group is  $O(MA/k)$ , and the cost of the coordinator is  $O(k)$ .

In our case, when choosing  $k = log(T)$  we end up with a  $O(M)$  coordinator cost and the same worker cost as before for the leaders, as their previous cost dominates this extra computation.

In the following paragraphs, we introduce the modified protocols with load balancing and optimistic accountability. The modified protocol steps are highlighted in purple.

Optimisitic accountability for distributed KZG PCS. For a multilinear polynomial  $f(x,y) = \sum_{\boldsymbol{b} \in B_\xi} f^{(\boldsymbol{b})}(x) \chi_{\boldsymbol{b}}(\boldsymbol{y}),$ the PCS protocol is described as follows:

- KeyGen, Commit(f, crs), Verify(com,  $\pi, \alpha, \beta, z$ , crs): Same as the previous protocol.
- Open $(f, \boldsymbol{\alpha}, \boldsymbol{\beta}, \mathsf{crs})$ :
	- 1) Divide nodes into groups of size  $log T$ . The coordinator randomly select one leader out of each group.
	- 2) Each prover  $P_b$  computes  $z^{(b)} := f^{(b)}(\alpha)$  and  $f^{(b)}(\bm{x}) - f^{(b)}(\bm{\alpha}) := \sum_{i \in [\mu]} q_i^{(b)}(\bm{x}) (x_i - \alpha_i)$ . Then it computes  $\pi_i^{(b)} = g^{q_i^{(b)}(\tau_1,\cdots,\tau_\mu) \cdot \chi_b(\tau_{\mu+1},\cdots,\tau_{\mu+\xi})}$  using the crs and  $s^{(b)}$  the signature of  $(\pi^{(b)}, z^{(b)})$ .  $\mathcal{P}_b$  sends  $(\pi^{(b)}, z^{(b)}, s^{(b)})$  to both leader of its group and  $\mathcal{P}_0$ .
	- 3) The leader of each group sums up the  $\pi^{(b)}$  it receives, and sends the result together with all the communication to the coordinator. In the case where the leader does not receive  $\pi^{(b)}$  from a specific node, it communicates with the coordinator for result.
	- 4)  $\mathcal{P}_0$  computes  $z = f(\boldsymbol{\alpha}, \boldsymbol{\beta}) = \sum_{\boldsymbol{b} \in B_{\xi}} z^{(\boldsymbol{b})} \cdot \chi_{\boldsymbol{b}}(\boldsymbol{\beta}).$   $\mathcal{P}_0$ decomposes

$$
f(\boldsymbol{\alpha}, \boldsymbol{y}) - f(\boldsymbol{\alpha}, \boldsymbol{\beta}) = \sum_{j \in [\xi]} q_j(\boldsymbol{y}) (y_j - \beta_j).
$$

Then it computes  $\pi_{\mu+j} = g^{q_j(\tau_{\mu+1},\dots,\tau_{\mu+\xi})}$  with  $V_b$ .

5)  $\mathcal{P}_0$  sets  $\boldsymbol{\pi}$  :=  $(\pi_1, \cdots, \pi_{\mu+\xi})$ .  $\mathcal{P}_0$  checks if Verify(com,  $\pi$ ,  $\alpha$ ,  $\beta$ , z, crs) passes. If the check fails,  $P_0$  aborts and checks which provers are malicious. In this case, signatures are used to ensure the integrity of the messages when the leader works as a relayer. Otherwise,  $P_0$  sends  $\pi$  and z to  $\mathcal{V}$ .

<span id="page-8-0"></span>Proposition 3. *The accountable Distributed KZG PCS previously presented is sound and accountable.*

*Proof.* To prove soundness by contradiction, assume that a group of malicious provers can produce a proof  $\pi' \neq \pi$  such that Verify(com,  $\pi^{\overline{I}}$ ,  $\alpha$ ,  $\beta$ ,  $z$ , crs) passes. This would mean that the soundness of the classic KZG protocol is broken, because in the classic protocol, a single prover has all the information that the worker provers have. This contradiction confirms that the optimistic protocol is sound.

Regarding accountability, if the protocol fails, we can identify the malicious provers by verifying the following equality:  $e(\text{com}/g^{z^{(b)}}, g) = \prod_{i \in [\mu]} e(\pi_i^{(b)}, g^{\tau_i - \alpha_i}).$  Since this is the standard commitment verification for the multilinear polynomial generated by the sub-circuit that  $P_b$  is responsible for, we can assume with high probability that it is sound, and that the coordinator will be able to identify any incorrect proofs between the ones provided. If none of the verifications fail, it implies that the leader either did not correctly sum the  $\pi^{(b)}$  or received  $\pi'^{(b)} \neq \pi^{(b)}$ . In the latter case, the leader will send the signed  $(\pi^{(b)}, z^{(b)})$ to demonstrate that prover  $b$  was malicious, which can be identified using the same method as the coordinator. If the leader cannot provide such proof, then the master will assume that the malicious node is the leader itself.  $\Box$ 

#### Optimistic accountability for distributed SumCheck.

Assume we have a secure signature scheme  $\sigma$ , then using the previously presented accountable and distributed KZG PCS we can build an optimistic accountable distributed SumCheck as follows:

- 1) For each  $i \in [\mu]$ :
	- a) Let  $r_i := 0$  for  $\mathcal{P}_0$ . b) For  $\mathbf{b} \in B_{\xi}$ ,  $\mathcal{P}_{\mathbf{b}}$  computes  $r_i^{(\mathbf{b})}(x)$  :=  $w\in B_{\mu-i}\smallint^{(\bm b)}(\alpha_1,\cdots,\alpha_{i-1},x,\bm w).$  $\mathcal{P}_{\mathbf{b}}$  sends  $r_i^{(\mathbf{b})}$  and its signature  $s^{(\mathbf{b})}$  to both the leader

of its group and  $P_0$ .

- c) The leader of each group sums up the  $r_i^{(b)}$  it receives, and sends the result together with all communication to the coordinator. In the case where the leader does not receive  $r_i^{(b)}$  from a specific worker, it communicates with the coordinator for the result.
- d)  $\mathcal{P}_0$  adds up the sum of  $r_i^{(b)}$  from the leaders and gets  $r_i$ .  $\mathcal{P}_0$  then checks if  $v = r_i(0) + r_i(1)$ . If such check does not pass,  $P_0$  aborts and checks which provers are malicious.  $\mathcal{P}_0$  sends the oracle of  $r_i$  to  $\mathcal{V}$ .
- e) V checks if  $v = r_i(0) + r_i(1)$ , and samples and sends a random  $\alpha_i \leftarrow \mathbb{F}$  to  $\mathcal{P}_0$ . Then  $\mathcal{V}$  sets  $v := r_i(\alpha_i)$ .  $\mathcal{P}_0$  sends  $\alpha_i$  to each  $\mathcal{P}_b$ .
- 2)  $\mathcal{P}_0$  receives  $v^{(b)} := f^{(b)}(\alpha)$  from each  $\mathcal{P}_b$ .  $\mathcal{P}_0$  has a multivariate polynomial  $g(\mathbf{y}) := f(\boldsymbol{\alpha}, \mathbf{y}) = \sum_{\mathbf{b} \in B_{\xi}} v^{(\mathbf{b})}$ .  $χ_b(y)$ .
- 3)  $\mathcal{P}_0$  and V perform SumCheck on g with target value v. In the final round,  $\mathcal{P}_0$  and the worker provers open  $f(\alpha, \beta)$ . If the opening fails,  $P_0$  aborts and checks which provers are malicious. Otherwise, V and  $\mathcal{P}_0$  open  $f(\alpha, \beta)$ .

<span id="page-9-0"></span>Proposition 4. *The accountable Distributed* SumCheck *protocol previously presented is sound and accountable.*

*Proof.* We will again prove soundness by reducing it to the classic SumCheck protocol. From the verifier's perspective, in step 1 of the protocol, they receive the univariate polynomials

$$
r_i(x) := \sum_{\boldsymbol{b}\in B_{\xi}} \sum_{\boldsymbol{w}\in B_{\mu-i}} f^{(\boldsymbol{b})}(\alpha_1, \cdots, \alpha_{i-1}, x, \boldsymbol{w})
$$
  
= 
$$
\sum_{\boldsymbol{w}\in B_{\mu-i}} \sum_{\boldsymbol{b}\in B_{\xi}} f(\alpha_1, \cdots, \alpha_{i-1}, x, \boldsymbol{w}, \boldsymbol{b}).
$$

This represents the first  $\mu$  steps of a classic SumCheck protocol for the polynomial  $f$ . In step 3, they perform the last  $\varepsilon$  steps of the SumCheck protocol by computing

the values  $r_{i+\mu}(x) := \sum_{\mathbf{w} \in B_{\xi-i}} f(\mathbf{\alpha}, x, \mathbf{w})$ . Since, from the verifier's perspective, this protocol mirrors the classic SumCheck protocol, we can ensure its soundness.

Since the protocol is sound, the coordinator can detect when a malicious worker or leader has submitted incorrect proof. Accountability is ensured in these scenarios through the following mechanism:

- If a worker submits an incorrect proof to both the leader and the coordinator, the coordinator can identify the worker by recomputing all  $r_i^{(b)}$  values and cross-checking them with the signed values provided during the protocol.
- If the leader submits an incorrect addition, the coordinator can detect the leader's error by recomputing the additions based on the values supplied by the workers.
- If the previous verification fails because a worker submitted inconsistent signed values to the leader and the coordinator, the leader can show that they received a different value than the one available to the coordinator by presenting the worker's signed value as evidence.
- Lastly, if both the worker and the leader engage in dishonest actions, they will be flagged as malicious. In this case, the leader will be able to show a signed value different from what the worker provided to the coordinator but will be unable to present a signed value that matches the leader's stated total.

Since all the possible cases are covered, the coordinator will always be able to identify the malicious node, ensuring accountability in the protocol. П

Optimistic accountability for distributed ZeroTest. Suppose each prover  $P_b$  has a multivariate polynomial  $f^{(\boldsymbol{b})}(\boldsymbol{x}) \in \mathbb{F}_{\mu}^{\leq d}[\boldsymbol{X}]$ . The provers wants to show to the verifier that  $f^{(b)}(x) = 0$  for all  $b \in B_{\xi}$  and  $x \in B_{\mu}$ . The protocol is described as follows:

- 1) V samples and sends to  $P_0$  two random vectors  $r \leftarrow \mathbb{F}^{\mu}$ and  $r_0 \leftarrow \mathbb{F}^{\xi}$ .
- 2)  $\mathcal{P}_0$  sends r to each  $\mathcal{P}_b$ .
- 3) Each  $\mathcal{P}_b$  sets  $\tilde{f}^{(b)}(x) := f^{(b)}(x) \cdot \chi_r(x) \cdot \chi_{r_0}(b)$ .
- 4) The provers and  $V$  run accountable distributed multivariate SumCheck protocol on  $\tilde{f}^{(b)}(x)$  with target value 0 for each prover.
- 5) The coordinator verifies the SumCheck protocol. If the verification fails,  $\mathcal{P}_0$  aborts and constructs  $\tilde{f}^{(b)}(x)$  for each  $\mathbf{b} \in B_{\xi}$  to check if the computation of  $\mathcal{P}_{\mathbf{b}}$  is correct.

Proposition 5. *The accountable Distributed* ZeroTest *protocol previously presented is sound and accountable.*

*Proof.* Note that this protocol does not change the communication with the verifier if all parties are honest. As discussed in Proposition [1,](#page-5-0) the protocol is sound.

By the accountability of distributed multivariate SumCheck protocol (Proposition [4\)](#page-9-0), this protocol is also accountable.  $\Box$ 

Optimistic accountability for distributed PermTest. Let  $\sigma^{(b)}: B_{\mu} \to B_{\mu}$  and  $\rho^{(b)}: B_{\mu} \to B_{\xi}$  be two mappings such that  $\{(\sigma^{(\mathbf{b})}(\mathbf{c}), \rho^{(\mathbf{b})}(\mathbf{c})) : \mathbf{c} \in B_{\mu}, \mathbf{b} \in B_{\xi}\} = B_{\mu} \times B_{\xi}.$ 

Each prover  $P_b$  has a multivariate polynomial  $f^{(b)} \in$  $\mathbb{F}_{\mu}^{\leq d}[X]$ . The provers want to show to the verifier that  $f^{(\boldsymbol{b})}(\boldsymbol{c}) = f^{(\rho^{(\boldsymbol{b})}(\boldsymbol{c}))}(\sigma^{(\boldsymbol{b})}(\boldsymbol{c}))$  for all  $\boldsymbol{b} \in B_{\xi}$  and  $\boldsymbol{c} \in B_{\mu}$ . Note that  $\rho^{(b)}(c)$  is not identically b for all  $c \in B_{\mu}$ .

- 1) Define the multivariate polynomials  $s, s_{\mu}^{(b)}, s_{\xi}^{(b)} \in$  $\mathbb{F}_{\mu}^{\leq 1}[X]$  where  $s(x) := [x]$ ,  $s_{\mu}^{(b)}(x) := [\sigma^{(b)}(x)]$  and  $s_{\xi}^{(\boldsymbol{b})}(\boldsymbol{x}):=[\rho^{(\boldsymbol{b})}(\boldsymbol{x})].$
- 2)  $\hat{V}$  samples and sends to the coordinator  $\gamma_{\mu}, \gamma_{\xi}, \delta \leftarrow \mathbb{F}$ .
- 3) Let  $f_1^{(b)}(x) := f^{(b)}(x) + \gamma_{\mu} \cdot s(x) + \gamma_{\xi} \cdot [b] + \delta$  and  $f_2^{(\bm{b})}(\bm{x})\ :=\ f^{(\bm{b})}(\bm{x})+\gamma_{\mu}\cdot s_{\mu}^{(\bm{b})}(\bm{x})+\gamma_{\xi}\cdot s_{\xi}^{(\bm{b})}(\bm{x})+\delta.$ Each prover  $P_b$  builds a multilinear polynomial  $z^{(b)} \in$  $\mathbb{F}_{\mu+1}^{\leq 1}[\bm{X}]$  such that for all  $\bm{x} \in B_{\mu}$

$$
\begin{cases} z^{(b)}(0,\mathbf{x}) = f_1^{(b)}(\mathbf{x})/f_2^{(b)}(\mathbf{x}) \\ z^{(b)}(1,\mathbf{x}) = z^{(b)}(\mathbf{x},0) \cdot z^{(b)}(\mathbf{x},1), \ \mathbf{x} \neq \mathbf{1} \\ z^{(b)}(1,\mathbf{1}) = 0 \end{cases}
$$

Let  $w_1^{(b)}(x) := z^{(b)}(1,x) - z^{(b)}(x,0) \cdot z^{(b)}(x,1)$  and  $w_2^{(\bm{b})}(\bm{x}) := f_2^{(\bm{b})}(\bm{x}) \cdot z^{(\bm{b})}(0,\bm{x}) - f_1^{(\bm{b})}(\bm{x}).$ Then each worker  $\mathcal{P}_b$  sends  $z^{(b)} := z^{(b)}(1, 1, \dots, 1, 0)$ to  $\mathcal{P}_0$ .

4)  $\mathcal{P}_0$  checks if  $\prod_{b \in B_\xi} z^{(b)} = 1$ . If not,  $\mathcal{P}_0$  aborts and checks which provers are malicious. Otherwise,  $P_0$  continues as follows.

Then  $\mathcal{P}_0$  interpolates a multilinear polynomial  $z \in B_{\xi+1}^{\leq 1}$ such that

$$
\begin{cases} z(0, y) = z^{(y)} \\ z(1, y) = z(y, 0) \cdot z(y, 1) \end{cases}
$$

Let  $w_3(y) := z(1, y) - z(y, 0) \cdot z(y, 1)$  and  $w_4^{(b)}(x) :=$  $z(0,\bm{b}) - z^{(\bm{b})}(\bm{x},0) \cdot \chi_{(1,1,\cdots,1,0)}(\bm{x},0).$ 

5)  $\mathcal{P}_0$  runs optimistically accountable distributed multivariate ZeroTest on  $\{w_1^{(b)}\}, \{w_2^{(b)}\}$  and  $\{w_4^{(b)}\}$  with the worker provers. If the test fails,  $P_0$  aborts and checks which provers are malicious. Otherwise, the  $\mathcal{P}_0$  and  $\mathcal V$ run distributed multivariate ZeroTest on  $\{w_1^{(b)}\}, \{w_2^{(b)}\}$ and  $\{w_4^{(b)}\}$ .  $\mathcal{P}_0$  and  $\mathcal{V}$  run multivariate ZeroTest on  $w_3$ .

<span id="page-10-1"></span>Proposition 6. *The accountable Distributed PermTest protocol previously presented is sound and accountable.*

*Proof.* The test  $W_1$  ensures with high probability (w.h.p.) that

$$
w_1^{(b)}(\bm{x}) = z^{(b)}(1,\bm{x}) - z^{(b)}(\bm{x},0) \cdot z^{(b)}(\bm{x},1) = 0, \forall \bm{x} \in B_{\mu}.
$$

Similarly,  $W_2$  ensures w.h.p. that

$$
w_2^{(b)}(\mathbf{x}) = f_2^{(b)}(\mathbf{x}) \cdot z^{(b)}(0, \mathbf{x}) - f_1^{(b)}(\mathbf{x}) = 0, \forall \mathbf{x} \in B_{\mu}.
$$

As long as  $z^{(b)}$  is a multilinear polynomial and  $z^{(b)}(1) = 0$ , these two constraints uniquely determine  $z^{(b)}$ . However, we observe that we do not need to check that  $z^{(b)}(1) = 0$ because we are interested in the value  $z^{(b)}(1,1,\ldots,1,0)$ 

for  $P_0$ . Regardless of the value of  $z^{(b)}(1)$ , the value  $z^{(b)}(1,1,\ldots,1,0)$  remains unaffected. Indeed, we have

$$
z^{(b)}(1,1,\ldots,1,0) = z^{(b)}(1,\ldots,1,0,0) \cdot z^{(b)}(1,\ldots,1,0,1)
$$
  
= 
$$
\cdots = \prod_{x \in B_{\mu}} z^{(b)}(0,x) = \prod_{x \in B_{\mu}} \frac{f_1^{(b)}(x)}{f_2^{(b)}(x)}.
$$

which does not depend on the value assigned to  $z^{(b)}(1)$ . Therefore, we only need to ensure that  $z^{(b)}$  is a multilinear polynomial, which is guaranteed by construction via the Polynomial Commitment Scheme (PCS). This means that as long as the  $W_1$  and  $W_2$  tests pass, w.h.p. the committed polynomial  $z^{(b)}$  is correctly computed. Next, we need to ensure that the value  $z^{(b)}$  provided by Prover<sub>b</sub> is indeed  $z^{(b)}(1,1,\ldots,1,0)$ . This is achieved by performing the  $W_4$ ZeroTest. The soundness comes from the fact that we are using the same commitment as in the previous zero tests, which ensures that the polynomial  $z^{(b)}$  was correctly computed. This demonstrates that if any of the provers Prover<sub>b</sub> is malicious, then w.h.p. at least one of the tests  $W_1$ ,  $W_2$ , or  $W_4$  will fail, and thus the previous optimistic protocol is sound. Due to the accountability property of ZeroTest and the fact that the coordinator has all the values needed to reconstruct all the  $z^{(b)}$  values from scratch and compare them to the provided values, we can ensure that this protocol is accountable. П

The **Cirrus** distributed SNARK. Cirrus, the accountable and efficiently computable distributed SNARK, is structured as follows:

- Input Constraint:  $F^{(b)}(1,\dots,1,x) I^{(b)}(x) = 0$  for all  $x \in B_{\nu}$  and  $b \in B_{\xi}$  with accountable distributed multilinear ZeroTest.
- Output Constraint:  $V$  queries  $F^{(1,\cdots,1)}(1,0,\langle N^{(b)}-1\rangle_\mu)$ and checks if it is zero.
- Gate Constraint: Define a multivariate polynomial

 $G^{(\bm{b})}(\bm{x}) := S^{(\bm{b})}_{\mathsf{add}}(\bm{x}) (F^{(\bm{b})}(0,0,\bm{x}) + F^{(\bm{b})}(0,1,\bm{x})) +$  $S^{(\bm{b})}_{\mathsf{mult}}(\bm{x}) (F^{(\bm{b})}(0,0,\bm{x})\cdot F^{(\bm{b})}(0,1,\bm{x})) - F^{(\bm{b})}(1,0,\bm{x}).$ 

 $G^{(b)}(x) = 0$  for all  $x \in B_{\mu}$  and  $b \in B_{\xi}$  with accountable distributed multivariate ZeroTest.

Wiring Constraint: Check if  $\hspace{.1cm}^{(\bm{b})}(\bm{x}) \hspace{.2cm} =$  $F^{(\rho^{(\mathbf{b})}(\bm{x}))}(\sigma^{(\mathbf{b})}(\bm{x}))$  for all  $\bm{x}$   $\in$   $B_{\mu}$  and  $\bm{b}$   $\in$   $B_{\xi}$ with accountable distributed multivariate PermTest.

## <span id="page-10-0"></span>5. Implementation and Evaluation

#### 5.1. Implementation Details

We implement Cirrus with optimistic accountability and without load balancing. Our implementation is based on HyperPlonk [\[8\]](#page-13-7) and adds 5, 000+ lines of Rust.

**Cirrus** is developer-friendly. An advantage of Cirrus is that it can automatically distribute the workload without requiring the developers to specify how to partite the circuit. In Hekaton, however, developers must partition the circuit into sub-circuits and carefully manage shared wires.

Universal v.s. per-circuit trusted setup. Inherited from HyperPlonk, the trusted setup in Cirrus is *universal*, which is a significant advantage in practice compared to schemes that require per-circuit trust setup (such as Hekaton) because a trustworthy setup ceremony is expensive to organize [\[22\]](#page-13-21).

## 5.2. Evaluation Results

To assess the practicality of running Cirrus in a decentralized environment (e.g., by individual volunteers), we decide to benchmark Cirrus on hardware comparable to commodity PCs. In contrast, related works such as Hekaton and Pianist are evaluated on much more powerful HPC servers (e.g., with 128 cores and 512 GB memory). Our cluster consists of 32 AWS t3.2xlarge machines in North Virginia, each with 8 vCPUs and 32 GB of memory; the average network latency across nodes in our setup was measured at 306 microseconds.

We found that the best setting for Cirrus is one worker per core (i.e., running 8 workers per machine), and we use this configuration across all experiments.

End-to-end proof generation time. We first evaluate the end-to-end proof generation time of Cirrus. As a baseline, we run multi-threaded HyperPlonk on the same worker machine to generate proofs for random circuits of varying sizes. Then, we run Cirrus with up to 32 workers (each with 8 cores) for random circuits generated in the same way. Figure [1](#page-11-0) shows the end-to-end proof generation time of Cirrus, as a function of total circuit size and the number of workers. The line for HyperPlonk stops at  $2^{22}$  gates when it runs out of memory (32 GB). In comparison, Cirrus can support larger circuits with more machines. We stopped at  $2^{25}$  with 8 AWS machines since the horizontal scalability is clear.

Coordinator's computation time. A feature of Cirrus is that the coordinator's computation is lightweight thanks to load balancing. We measure the coordinator's computation time in Fig. [2.](#page-11-1) Without load balancing, the computation time of the coordinator increases as the sub-circuit size increases. With our load balancing technique, the computation time of the coordinator is independent of the size of the sub-circuits. Overall, the coordinator's computation is lightweight and well under 1 second for less than 1, 000 workers.

Memory usage. We record how each worker's memory usage varies with the worker's sub-circuit size in Table [2a.](#page-12-0) In Cirrus, worker memory consumption is similar to that of Pianist and Hekaton for sub-circuits of the same sizes.

The memory usage of the coordinator is reported in Table [2b.](#page-12-0) We observe that the memory usage of the coordinator only depends on the size of the full circuit in our experimental setting.

Comparison with Hekaton. To provide context for Cirrus's performance, we compared it with Hekaton, which reportedly outperforms Pianist by  $3\times$ , making it the fastest

<span id="page-11-0"></span>

Figure 1: End-to-end proof generation time of Cirrus when all workers are honest with different total cores and circuit sizes. C denotes Cirrus; H denotes HyperPlonk.

<span id="page-11-1"></span>

Figure 2: Computation time of the coordinator with varying number of workers and sub-circuit sizes.

distributed SNARK scheme to the best of our knowledge. We obtained the Hekaton source code from the authors and evaluated it on our worker machines.

The comparison to Hekaton is not straightforward because Hekaton works with R1CS circuits instead of PLONK circuits. A given function may translate to drastically different numbers of R1CS constraints and PLONK gates. We use  $\alpha$  to denote the ratio of the number of PLONK gates to the number of R1CS constraints to express a given program. For certain tasks, such as MinRoot [\[36\]](#page-14-5), Pedersen hash function [\[12\]](#page-13-11) and MiMC hash function [\[37\]](#page-14-6), the number of PLONK gates can be 75% less than the number of R1CS constraints [\[8\]](#page-13-7), [\[38\]](#page-14-7) (i.e.,  $\alpha = 0.25$ ); HyperPlonk reported that Zexe's recursive circuit and a ZK-Rollup block of 50 private transactions have  $\alpha$  as small as  $2^{-5}$ . For other tasks, including SHA-256 hash, the number of PLONK gates can

<span id="page-12-0"></span>TABLE 2: Memory usage of workers and the coordinator.

Sub-circuit Size	Memory	<b>Full Circuit Size</b>	Memory
$2^{16}$	383 MB	$2^{19}$	200 MB
$2^{17}$	765 MB	$2^{20}$	396 MB
$2^{18}$	1.4 GB	$2^{21}$	799 MB
$2^{19}$	$2.8$ GB	$2^{22}$	$1.5$ GB
$2^{20}$	5.6 GB	$2^{23}$	$3.0$ GB
$2^{21}$	11.3 GB	$2^{24}$	$6.0$ GB
22	22.6 GB	2 <sup>25</sup>	12.1 GB
$\sim$ $\sim$	$\sim$	$\sim$ $\sim$ $\sim$	$\sim$ $\sim$ $\sim$

(a) Memory usage of each worker with varying subcircuit sizes.

(b) Memory usage of the coordinator with varying full circuit sizes.

<span id="page-12-1"></span>

Figure 3: Cirrus and Hekaton Comparison with  $\alpha = 0.25$ . C denotes Cirrus, and He denotes Hekaton. The numbers in the parentheses denote the total number of working cores.

be  $2.25\times$  more than the number of R1CS constraints [\[39\]](#page-14-8).

To perform a fair comparison with Hekaton, we use hash functions as the workload and report the results with  $\alpha = 0.25$  (corresponding to Pedersen hash function) and  $\alpha$  = 2.25 (corresponding to SHA-256). We report our performance compared to Hekaton in Figs. [3](#page-12-1) and [5.](#page-12-2) Cirrus outperforms Hekaton when proving Pedersen hashing tasks, yet underperforms Hekaton when proving SHA-256 hash tasks. For reference, we also report our performance compared to Hekaton for  $\alpha = 1$  (i.e., tasks with the same number of PLONK gates and R1CS constraints) in Fig. [4.](#page-12-3) We stress that Hekaton requires a *per-circuit trusted setup* while Cirrus is universal.

## 6. Conclusions and Future Directions

We have introduced Cirrus, the first *accountable* distributed SNARK generation scheme with linear-time worker and coordinator computation time, minimal communication overhead, and supports a universal trusted setup. By accountability, the coordinator can identify any malicious prover, making Cirrus suitable for deployment in decentralized settings, e.g., in prover markets, where workers

<span id="page-12-3"></span>

Figure 4: Cirrus and Hekaton Comparison with  $\alpha = 1$ . C denotes Cirrus, and He denotes Hekaton. The numbers in the parentheses denote the total number of working cores.

<span id="page-12-2"></span>

Figure 5: Cirrus and Hekaton Comparison with  $\alpha = 2.25$ . C denotes Cirrus, and He denotes Hekaton. The numbers in the parentheses denote the total number of working cores.

cannot be fully trusted. We formally define accountability in distributed proof generation schemes and prove that Cirrus satisfies this definition. According to our experiments, Cirrus is horizontally scalable and is concretely faster than the state-of-the-art for representative workloads.

One future direction is to further improve the communication round complexity, currently logarithmic in the size of each sub-circuit due to the distributed SumCheck. It is of both theoretical and practical interest to design an accountable distributed SNARK with a constant number of communication rounds while preserving the efficiency of Cirrus. Additionally, future work could focus on minimizing the coordinator's overhead in holding malicious provers accountable. Currently, identifying malicious provers during the PermTest requires linear time relative to the size of the entire circuit. Enhancing this aspect of the protocol would facilitate its deployment in scenarios with a large number of adversaries, making it more practical for broader applications.

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# Appendix

### <span id="page-14-3"></span>1. Definitions

Succinct non-interactive argument of knowledge. We recall the definitions of SNARKs.

Definition 2 (Interactive Argument of Knowledge). *A tuple of algorithms* (Setup,P, V) *is an interactive argument of knowledge for relation* R *between a prover* P *and a verifier* V *if it has the following completeness and knowledge soundness properties.*

• **Completeness**: For all  $(\mathbf{x}, \mathbf{w}) \in \mathcal{R}$ 

$$
\Pr[\langle \mathcal{P}(w), \mathcal{V} \rangle(\mathbf{x}, \mathsf{pp}) = 1 \mid \mathsf{pp} \leftarrow \mathsf{Setup}(1^{\lambda}, \mathcal{R})] = 1.
$$

• *Knowledge Soundness and Soundness: An interactive protocol is knowledge-sound if for any PPT adversary*  $(A_1, A_2)$ , there exists a PPT extractor algorithm  $\mathcal E$  with *oracle access to*  $A_1, A_2$  *such that this probability is*  $negl(\lambda)$ .

$$
\text{Pr}\left[\langle \mathcal{A}_2(\textbf{w}), \mathcal{V}\rangle(\textbf{x}, \textbf{pp}) = 1 ~\wedge~ (\textbf{x}, \textbf{w}) \notin \mathcal{R} ~\middle|~ \begin{gathered} \textbf{pp} \leftarrow \textbf{Setup}(1^\lambda, \mathcal{R}) \\ \textbf{x} \leftarrow \mathcal{A}_1(\textbf{pp}) \\ \textbf{w} \leftarrow \mathcal{E}(\textbf{pp}, \textbf{x}, \mathcal{R}) \end{gathered} \right] \right.
$$

*If the statement holds when the constraint on the extractor is removed, the interactive protocol is considered sound, and the protocol is considered an interactive argument.*

- *Succinctness: An interactive argument of knowledge is considered succinct if the communication between the prover and the verifier and the running time of the verifier are both*  $O(\lambda, |\mathbf{x}|, \log |\mathbf{w}|)$ *.*
- *Public-Coin: An interactive protocol is considered to be public-coin if all of the* V *messages can be computed as a deterministic function given a random public input.*

A public coin interactive argument of knowledge can be turned into a non-interactive via Fiat-Shamir transformation [\[40\]](#page-14-9). The Fiat-Shamir transformation replaces the challenges of the verifier with PRF outputs of the transcript. A succinct non-interactive argument of knowledge is called a SNARK.

Polynomial interactive oracle proof. Interactive arguments of knowledge can be constructed from argument systems with oracle access to prover messages, e.g., oracle access to polynomials. The argument is then compiled using cryptographic protocols, such as polynomial commitments. Here we define *polynomial interactive oracle proof* (Poly-IOP), a specific category of such argument systems.

Definition 3 (Polynomial Interactive Oracle Proof). *A polynomial interactive oracle proof (Poly-IOP) is a publiccoin interactive argument for an oracle relation* R*. In any instance*  $(x, w) \in \mathcal{R}$  *of the oracle relation, the public input* x *can contain oracles to* µ*-variate polynomials over a field* F*, where* µ *and the degree of the polynomials are specified by the oracles. The oracles can be queried at arbitrary points in* F <sup>µ</sup> *to evaluate the polynomials. The polynomials specifications corresponding to the oracles are*  $\hat{a}$  in pp  $\leftarrow$  Setup $(1^{\lambda}, \mathcal{R})$  and w. In each round of the Poly-*IOP protocol, the prover sends several polynomial oracles to the verifier, and the verifier sends random challenges to the prover.*

The knowledge soundness of a Poly-IOP protocol differs from the knowledge soundness of an ordinary interactive argument protocol in that the extractor  $\mathcal E$  can query the polynomial oracles at arbitrary points. It is shown in [\[8\]](#page-13-7) that a Poly-IOP protocol is knowledge sound if it is sound.

Remark 1. *The Poly-IOP definition here should be more precisely described as polynomial interactive oracle arguments. Yet the usage of the words "proof" and "argument" are often mixed with each other, and it is more conventional to write Poly-IOPs instead of Poly-IOAs. That is why we keep the term Poly-IOP here.*

Polynomial commitment scheme. To compile a Poly-IOPs to a SNARK, a polynomial commitment scheme (PCS) is required. On a high level, a PCS securely implements polynomial oracles and queries to such oracles. Here we define PCS as follows.

Definition 4 (Polynomial Commitment Scheme). *A polynomial commit scheme for a class of functions*  $\mathcal{F} \subseteq \mathbb{F}[X]$  *is a tuple of algorithms* (KeyGen, Commit, Open, Verify)*, where*

- KeyGen $(1^{\lambda}, \mathcal{F}) \rightarrow$  crs *generates the public parameter* crs.
- Commit( $f, \text{crs}) \rightarrow \text{com}_f$  generates a commitment com<sub>f</sub> *of a function* f*.*
- Open $(f, x, \text{crs}) \rightarrow (y, \pi)$  *outputs*  $y := f(x)$  *and an opening proof* π*.*
- Verify(com  $f, x, y, \pi, \text{crs}) \rightarrow b \in \{0, 1\}$  *verifies the polynomial opening given the proof, and returns whether the verification passes.*
- *A PCS protocol should have the following properties:*
- **Completeness**: For all  $f \in \mathcal{F}$  and for all  $x$

$$
\Pr\left[b=1\left|\begin{array}{l} \mathsf{crs} \gets \mathsf{KeyGen}(1^\lambda,\mathcal{F})\\ \mathsf{com}_f \gets \mathsf{Commit}(f,\mathsf{crs})\\ (y,\pi) \gets \mathsf{Open}(f,\boldsymbol{x},\mathsf{crs})\\ b \gets \mathsf{Verify}(\mathsf{com}_f,\boldsymbol{x},y,\pi,\mathsf{crs}) \end{array}\right]=1
$$

• *Binding: For all PPT adversary* A *the following probability is* negl $(\lambda)$ *.* 

$$
\Pr\left[b_1=b_2=1\left|\begin{array}{l} \mathsf{crs} \leftarrow \mathsf{KeyGen}(1^\lambda,\mathcal{F})\\ \mathsf{com}_f \leftarrow \mathsf{Commit}(f,\mathsf{crs})\\ x_1,x_2,y_1,y_2,\pi_1,\pi_2 \leftarrow \mathcal{A}(\mathsf{crs})\\ b_{\{1,2\}} \leftarrow \mathsf{Verify}(\mathsf{com}_f,\pmb{x}_{\{1,2\}},y_{\{1,2\}},\pi_{\{1,2\}},\mathsf{crs})\end{array}\right]\right.\right]
$$

#### <span id="page-15-0"></span>2. HyperPlonk Poly-IOP

HyperPlonk is a Poly-IOP that features linear prover time [\[8\]](#page-13-7). Recall that in Plonk Poly-IOP, we need to interpolate univariate polynomials over a set of size  $O(N)$ . However, interpolating degree- $n$  polynomials would incur at least  $O(n \log n)$  prover time, which means that the prover time is at least  $O(N \log N)$ . To resolve this problem, in HyperPlonk multivariate polynomials are interpolated over the boolean hypercube  $B_{\mu} := \{0,1\}^{\mu}$ . By the merits of linear time algorithms to evaluate and interpolate multilinear polynomials in the previous work [\[6\]](#page-13-5), [\[41\]](#page-14-10), the prover time is finally reduced to  $O(N)$  in HyperPlonk Poly-IOP.

Multivariate polynomials in HyperPlonk. Let  $\nu$  be the length of binary index to represent public inputs, i.e.  $2^{\nu}$  =  $\ell_x$ . Let  $\mu$  be the length of binary index to represent the gates, i.e.  $2^{\mu} = N$ . We can interpret  $\sigma$  to another permutation  $\sigma_{\mu}$  :  $B_{\mu+2} \rightarrow B_{\mu+2}$ . Then the multivariate polynomials that characterizes the Plonk constraint  $(s, \sigma)$  are defined as follows:

• Two multilinear polynomials  $S_{\text{add}}(x)$ ,  $S_{\text{mult}}(x) \in$  $\mathbb{F}_{\mu}^{\leq 1}[X]$  such that for all  $i \in [N]$ 

$$
\begin{cases} S_{\text{add}}(\langle i \rangle_{\mu}) = s_i \\ S_{\text{mult}}(\langle i \rangle_{\mu}) = 1 - s_i \end{cases}
$$

• A multilinear polynomial  $F \in \mathbb{F}_{\mu+2}^{\leq 1}$  such that



• A multilinear polynomial  $I \in \mathbb{F}_\nu^{\leq 1}$  such that for all  $i \in$  $[0, \ell_x - 1]$  we have

$$
I(\langle i \rangle_{\nu}) = x_{i+1}.
$$

Multivariate SumCheck Poly-IOP. Prover wants to show to the verifier that a multivariate polynomial  $f(x) :=$  $h(g_1(\boldsymbol{x}), \cdots, g_c(\boldsymbol{x})) \in \mathbb{F}_{\mu}[\boldsymbol{X}]$  satisfies  $\sum_{\boldsymbol{x} \in B_{\mu}} f(\boldsymbol{x}) = v$ , where each  $g_i$  is multilinear and h can be evaluated using an arithmetic circuit with  $O(d)$  gates. Before describing the final Poly-IOP protocol, recall the original SumCheck protocol in [\[25\]](#page-13-24) that works for  $f \in \mathbb{F}_{\mu}^{\le d}[X]$ :

• For each  $i \in [\mu]$ :  $\mathcal P$  computes  $r_i(x) := \sum_{\boldsymbol{b} \in B_{\mu-i}} f(\alpha_1, \cdots, \alpha_{i-1}, x, \boldsymbol{b})$ and sends the oracle of  $r_i$  to  $\mathcal{V}$ . – V checks if  $v = r_i(0) + r_i(1)$ . Then V samples  $\alpha_i \leftarrow \mathbb{F}$ and sets  $v := r_i(\alpha_i)$ . V sends  $\alpha_i$  to  $\mathcal{P}$ .

• *V* checks if 
$$
f(\alpha) = v
$$
.

Note that in [\[25\]](#page-13-24) the vanilla SumCheck protocol assumes that the prover has oracle access to  $f$ . To efficiently evaluate  $f$  at the given points along the protocol for the prover, Chen et al. build upon the protocols in [\[6\]](#page-13-5), [\[41\]](#page-14-10) and developed a dynamic-programming-based scheme for polynomials  $f \in$  $\mathbb{F}_{\mu}^{\leq d}$ . Specifically, the compiled SumCheck protocol without oracle access to  $f$  is described as follows:

- For all  $j \in [c]$ , P evaluate  $g_j(x)$  for all  $x \in B_\mu$  and build a table  $A_j$  to store the valuations.
- For each  $i \in [\mu]$ : −  $\mathcal{P}$  computes  $r_j^{(b)}(x) := (1 - x)A(0, b) + xA(1, b)$  for all  $\mathbf{b} \in B_{\mu-i}$  and for all  $j \in [c]$ .
	- For all  $\mathbf{b} \in B_{\mu-i}, \mathcal{P}$  computes  $r^{(\mathbf{b})}(x) :=$  $h(r_1^{(b)}(x), \dots, r_c^{(b)}(x))$ , where each addition gate is computed using polynomial addition, and each multiplication is computed using fast polynomial multiplication.

 $\mathcal P$  computes  $r_i(x) := \sum_{b \in B_{\mu-i}} r^{(b)}(x)$ , and sends the oracle of  $r_i$  to  $\mathcal{V}$ .

- V checks if  $v = r_i(0) + r_i(1)$ . Then V samples  $\alpha_i \leftarrow \mathbb{F}$ and sets  $v := r_i(\alpha_i)$ .
- $V$  sends  $\alpha_i$  to  $P$ .
- $\mathcal{P}$  sets  $A_j[\boldsymbol{b}] = r_j^{(\boldsymbol{b})}(\alpha_i)$  for all  $b \in B_{\mu-i}$ .
- V queries  $f(\alpha)$  and checks if it is equal to v.

Here we summarize the complexity of the multivariate SumCheck Poly-IOP:

- Prover Time:  $O(2^{\mu} \cdot d \log^2 d)$
- Verifier Time:  $O(\mu)$
- Communication:  $O(\mu)\mathbb{F}$

• Oracle Access: The prover builds and sends oracle for  $O(\mu)$  degree-d univariate polynomials and  $O(1)$  multivariate polynomials in  $\mathbb{F}_{\mu}^{\leq d}[\bm{X}]$ . The verifier queries  $O(\mu)$ degree-d univariate polynomials and  $O(1)$  multivariate polynomials in  $\mathbb{F}_{\mu}^{\leq d}[\boldsymbol{X}]$ .

Multivariate ZeroTest Poly-IOP. The prover wants to show to the verifier that a multivariate polynomial  $f \in$  $\mathbb{F}_{\mu}^{\leq d}[X]$  satisfies  $f(x) = 0$  for all  $x \in \dot{B}_{\mu}$ . The protocol reduces ZeroTest to SumCheck, and is described as follows:

- V samples and sends to P a random vector  $r \leftarrow \mathbb{F}^{\mu}$ .
- Let  $\tilde{f}(\boldsymbol{x}) := f(\boldsymbol{x}) \cdot \chi_r(\boldsymbol{x})$ . P and V run multilinear SumCheck on  $\tilde{f}$  with target value 0.

 $\sum_{\bm{x} \in B_{\mu}} f(\bm{x}) \cdot \chi_{\bm{y}}(\bm{x})$ . Then it is clear that  $g(\bm{y}) = f(\bm{y})$  for We argue that this protocol is complete. Let  $g(\mathbf{y}) :=$ all  $y \in B_\mu$ . Since g is multilinear, we have  $g(y)$  is zero polynomial if and only if  $f(y)$  is zero over  $B_u$ .

**Multivariate PermTest Poly-IOP.** Let  $f \in \mathbb{F}_{\mu}^{\le d}[X]$  be a multivariate polynomial. Let  $\sigma : B_{\mu} \to B_{\mu}$  be a permutation over  $B_{\mu}$ . The prover wants to show to the verifier that  $f(\boldsymbol{x}) = f(\sigma(\boldsymbol{x}))$  for all  $\boldsymbol{x} \in B_{\mu}$ . The multivariate PermTest Poly-IOP is described as follows:

- Let  $s, s_{\sigma} \in \mathbb{F}_{\mu}^{\leq 1}[X]$  be two mappings, where  $s(x) := [x]$ and  $s_{\sigma}(\boldsymbol{x}) := [\sigma(\boldsymbol{x})]$ .
- *V* samples and sends to the prover  $\gamma, \delta \leftarrow \mathbb{F}$ .
- Let  $f_1 := f + \gamma s + \delta$  and  $f_2 := f + \gamma s_{\sigma} + \delta$ . P builds a multilinear polynomial  $z \in \mathbb{F}_{\mu+1}^{\leq 1}$  such that for all  $x \in B_{\mu}$

$$
\begin{cases} z(0, \boldsymbol{x}) = f_1(\boldsymbol{x})/f_2(\boldsymbol{x}) \\ z(1, \boldsymbol{x}) = z(\boldsymbol{x}, 0) \cdot z(\boldsymbol{x}, 1) \end{cases}
$$

Let  $w_1(x) := z(1, x) - z(x, 0) \cdot z(x, 1)$  and  $w_2(x) :=$  $f_2(x) \cdot z(0, x) - f_1(x)$ .

- $P$  sends the oracle of z to  $V$ .
- $P$  and  $V$  run multilinear ZeroTest on  $w_1$  and  $w_2$ .
- V queries  $z(1, 1, \dots, 1, 0)$  and checks if it is 1.

The HyperPlonk Poly-IOP. The HyperPlonk Poly-IOP is structured as follows:

- Input Constraint:  $F(1, \dots, 1, x) I(x) = 0$  for all  $x \in$  $B_{\nu}$  with multilinear ZeroTest.
- Output Constraint: V queries  $F(1, 0, \langle N-1 \rangle_{\mu})$  and checks if it is zero.
- Gate Constraint: Define a multivariate polynomial

$$
G(\boldsymbol{x}) := S_{\text{add}}(\boldsymbol{x}) (F(0, 0, \boldsymbol{x}) + F(0, 1, \boldsymbol{x})) + S_{\text{mult}}(\boldsymbol{x}) (F(0, 0, \boldsymbol{x}) \cdot F(0, 1, \boldsymbol{x})) - F(1, 0, \boldsymbol{x}).
$$

- $G(\boldsymbol{x}) = 0$  for all  $\boldsymbol{x} \in B_{\mu}$  with multivariate ZeroTest.
- Wiring Constraint: Check if  $F(x) = F(\sigma_{\mu}(x))$  for all  $x \in B_\mu$  with multivariate PermTest.

## 3. ZK for Cirrus

Chen et al. [\[8\]](#page-13-7) described an efficient ZK compiler for SumChecks, built over the previous construction by Xie et al. [\[6\]](#page-13-5).

Zero-knowledge distributed SumCheck. The zeroknowledge distributed SumCheck protocol is structured as follows:

- 1)  $\mathcal{P}_0$  samples two random vectors  $\mathbf{r} \stackrel{\$}{\leftarrow} \mathbb{F}^{\mu}$  and  $\mathbf{r}_0 \stackrel{\$}{\leftarrow} \mathbb{F}^{\xi}$ and defines  $g^{(b)}(\boldsymbol{x}) = \chi_{\boldsymbol{r}}(\boldsymbol{x}) \cdot \chi_{\boldsymbol{r}_0}(\boldsymbol{b}).$
- 2)  $\mathcal{P}_0$  sends r and  $\chi_{r_0}(\boldsymbol{b})$  to  $\mathcal{P}_{\boldsymbol{b}}$ .
- 3) V sends a challenge  $\rho \stackrel{\$}{\leftarrow} \mathbb{F}^*$  to  $\mathcal{P}_0$  that is relayed to all  $\mathcal{P}_{\boldsymbol{b}}$ .
- 4) The provers and the verifier now run SumCheck PIOP [4.3](#page-8-0) over polynomial  $f + \rho q$ .
- 5) V queries g and f at point r' where  $r' \in \mathbb{F}^{\epsilon+\mu}$  is the vector of sumcheck's challenge. V checks that  $f(\mathbf{r}') + \rho g(\mathbf{r}')$ is consistent with the last message of the SumCheck.

Once we have the zero-knowledge Distributed SumCheck, we can build Cirrus the same way it was done in [4.3.](#page-10-1)

#### 4. Application to SNARK Outsourcing

With Cirrus, we propose a novel design for outsourcing ZK-SNARKs. We focus on using trusted execution environments (TEEs) [\[42\]](#page-14-11) to enable efficient ZK-SNARK outsourcing while keeping the secret inputs of the users private. TEEs provide a secure enclave within a processor, where sensitive data and computations can be performed in isolation from the rest of the system. TEEs ensure that even privileged users, such as system administrators, cannot access the data or computation within the enclave. This isolation, combined with the ability to verify the integrity of the execution through remote attestation, makes TEEs a powerful tool for securely outsourcing ZK-SNARK generation. By leveraging TEEs, we can achieve efficient computation while ensuring that the secret inputs remain protected throughout the outsourcing process.

The use of Trusted Execution Environments (TEEs) as a straightforward approach for generating ZK-SNARKs is likely to face considerable performance bottlenecks due to the high memory demands inherent to ZK-SNARK generation. This process involves substantial cryptographic computations—such as group exponentiations and bilinear mappings—that require significant memory resources, posing a challenge to the efficiency of TEE-based solutions. Given that most TEEs, like Intel SGX, provide relatively small secure memory enclaves (e.g., 128 MB or less of usable secure memory), such operations quickly exceed the available memory within the enclave, leading to frequent page thrashing. Page thrashing occurs when memory pages are continuously swapped between the secure enclave and untrusted memory, drastically increasing latency. [\[43\]](#page-14-12) This is particularly problematic for ZK-SNARK generation, where each cryptographic operation must maintain high integrity and constant memory evictions introduce significant overhead.

With Cirrus, we can use the memory-efficient property of the scheme on TEEs to build an efficient TEE-based ZK-SNARK outsourcing protocol. By combining the parallel processing capabilities of Cirrus with the secure computation environment of TEEs, our solution not only addresses the memory bottleneck but also ensures that the secret inputs remain protected during the proof generation process. This hybrid approach allows for both scalable and secure ZK-SNARK outsourcing, overcoming the memory limitations typically associated with TEEs while preserving privacy guarantees.