

Security analysis of the Classic McEliece, HQC and BIKE schemes in low memory

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Abstract

With the advancement of NIST PQC standardization, three of the four candidates in Round 4 are code-based schemes, namely Classic McEliece, HQC and BIKE. Currently, one of the most important tasks is to further analyze their security levels for the suggested parameter sets. At PKC 2022 Esser and Bellini restated the major information set decoding (ISD) algorithms by using nearest neighbor search and then applied these ISD algorithms to estimate the bit security of Classic McEliece, HQC and BIKE under the suggested parameter sets. However, all major ISD algorithms consume a large amount of memory, which in turn affects their time complexities. In this paper, we reestimate the bit-security levels of the parameter sets suggested by these three schemes in low memory by applying K -list sum algorithms to ISD algorithms. Compared with Esser-Bellini's results, our results achieve the best gains for Classic McEliece, HQC, and BIKE, with reductions in bit-security levels of 11.09, 12.64, and 12.19 bits, respectively.

Keywords: Information Set Decoding, Syndrome Decoding, Code-based Cryptography, K -list.

1. Introduction

Coding-based cryptosystems have gained significant attention due to their ability to resist attacks from quantum computers and their success in the NIST PQC standardization process. Among the four candidates advancing

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to the 4th round of the process, three are code-based schemes, namely Classic McEliece, HQC, and BIKE [1]. Currently, a crucial task is to further analyze their bit-security levels of these three schemes under the suggested parameter sets.

It is widely acknowledged that the security of most code-based cryptographic schemes relies primarily on the hardness of the syndrome decoding (SD) problem, which is known to be NP-hard [2, 3]. The best solvers of the SD problem are information set decoding (ISD) algorithms, which can be used to estimate the bit-security levels of the suggested parameter sets for these schemes. In 1962, Prange proposed the first ISD algorithm, which involved finding an error vector with a fixed weight by guessing an information set such that the coordinates of an error vector indexed by the set are error-free [4]. Over the next 50 years, Prange’s algorithm was significantly improved by allowing a few entries of an error vector indexed by the information set to be error-affected and by introducing advanced techniques such as meet-in-the-middle, representation technology, nearest neighbor search, and others [5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19], in which the major ISD algorithms are Stern-1988 [18], MMT-2011 [15], BJMM-2012 [6], MO-2015 [16] and BM-2018 [8]. All these improved ISD algorithms have a common iterative structure, where each iteration attempts to retrieve an error vector with fixed weight distribution corresponding to a given syndrome, and the number of iterations required is determined by the average probability of success for one iteration.

Due to the introduction of additional parameters requiring optimization, ISD algorithms can be quite complex. In 2016, Torres and Sendrier developed a concise and asymptotic mathematical formula to determine the time complexity of the main ISD algorithms [19]. Since then, this formula has been widely used by almost all code-based cryptographic schemes to roughly estimate the security levels of their parameter sets. Accurately estimating the bit security of current code-based schemes is an important task. Recently, Esser and Bellini introduced a new approach to restate the major ISD algorithms by utilizing nearest neighbor search. They then applied these redescribed ISD algorithms to estimate the concrete security levels of parameter sets suggested by Classic McEliece, HQC, and BIKE [20]. Additionally, Esser, May, and Zweyinger achieved two new computing records for McEliece-1223 and McEliece-1284 by implementing MMT’s or BJMM’s ISD algorithms. Based on these records, they estimated the hardness of breaking these three schemes under the round-3 suggested parameter sets [21]. However, advanced ISD al-

gorithms require a large amount of memory, which in turn affects their time complexities. Recently, Esser and Zweydingler extrapolated the bit-security levels of the NIST round-4 parameter sets for Classic McEliece, HQC, and BIKE by proposing a time-memory trade-off for MMT algorithm [22]. Guo et al. also obtained the bit-security levels for these schemes by proposing a new ISD algorithm [23]. Therefore, our goal is to reestimate the bit-security levels of the parameter sets suggested by these three schemes in low memory by applying K -list sum algorithms proposed in [24] to ISD algorithms.

The solving algorithm for a K -list sum problem is essentially a general birthday algorithm first proposed by Wagner in 2002 [25], where K is a power of 2. Howgrave-Graham and Joux then developed a class of exhaustive memory-efficient list sum algorithms for $K = 4$ in order to solve knapsacks [26], while Becker et al. focused on the scenario of $K = 16$ [27]. In 2012, Dinur et al. proposed an improvement to the general birthday algorithm by demonstrating ideal asymmetric partitions for "magic numbers" of K such as 7 and 11, known as the K -dissection algorithm [28]. In 2019, Dinur unified all previous improvements and analyses of the general birthday algorithms into an algebraic framework [24]. The original general birthday algorithms are concerned with finding just one solution, but when multiple (exhaustive) solutions are needed in specific settings such as ISD algorithms, they are referred to as K -list sum algorithms by Dinur. K -list sum algorithms have shown good performance and have been applied to many search problems, including knapsacks [29] and the learning parity with noise (LPN) problem [30]. By using K -list sum algorithms in the ISD framework, Bricout et al. [31] reselected the parameter sets of the Wave scheme, a code-based digital signature scheme on a ternary field [32], to meet given security levels. Karpman-Lefevre [33] derived a time-memory trade-off for the parameter sets in the Wave scheme. Furthermore, Chailloux et al. used the ISD framework and Wagner's general birthday algorithm to solve the SD problem endowed with the Lee metric [34]. Additionally, Wang and Liu improved the ISD algorithm under restricted memory by utilizing the K -dissection algorithms proposed in [28] [35]. Thus, the recent advancements in the K -dissection algorithms have motivated us to conduct this study.

Our Contributions. We firstly extend the 4-list sum algorithms proposed in [24] and then apply them to the ISD framework under three memory models, namely constant memory, logarithmic memory penalty, and cube-root memory penalty, to derive new bit-security levels for the Classic McEliece, HQC,

and BIKE schemes. Finally, we compare our results with the latest findings in [20]. Our best results for the bit-security levels of Classic McEliece, HQC, and BIKE schemes, under a 2^{60} -bit limitation for memory, are 11.09, 12.64, and 12.19 bits lower than those in [20], respectively. Furthermore, our algorithms reveal that, for the case of constant memory, the parameter sets for $n = 4608$, $n = 6688$, and $n = 6960$ of the Classic McEliece scheme do not achieve the claimed security levels, whereas the results in [20] show that only the parameter set for $n = 4608$ fails to achieve the claimed security level.

The paper is structured as follows: Section 2 provides definitions, notations, and lemmas that will be used throughout the paper. In Section 3, we analyze the memory consumption and time complexity of different versions of 4-list sum algorithms applied to the ISD framework. In Section 4, we calculate the new bit-security levels for the Classic McEliece, HQC, and BIKE schemes and compare them to the latest results in [22]. Finally, Section 5 concludes the paper.

2. Preliminaries

2.1. Notations

A finite field with two elements is denoted as \mathbb{F}_2 . If not specified explicitly, the vectors and matrices are represented by the bold lowercase and uppercase letters, respectively. The concatenation of two row vectors \mathbf{v}_1 and \mathbf{v}_2 is denoted as $(\mathbf{v}_1|\mathbf{v}_2)$. For simplicity, let the set $\{1, 2, \dots, n\}$ be $[n]$. \mathbf{I}_k is used to denote the identity matrix with size k . The Hamming weight of a vector \mathbf{v} is written as $wt(\mathbf{v})$. Given a length- n vector \mathbf{v} and a matrix \mathbf{M} with n columns, \mathbf{v}_S denotes the vector composed of the terms of \mathbf{v} indexed by S , and \mathbf{M}_S denotes the matrix composed of the columns of \mathbf{M} indexed by S , where S is a subset of $[n]$. If \mathcal{C} is a set consisting of vectors of length n , then define $\mathcal{C}_S = \{\mathbf{v}_S : \mathbf{v} \in \mathcal{C}\}$. The size of the set $S \subseteq [n]$ is denoted as $|S|$, and so we give a mapping $\delta_{S,[n]}: \mathbb{F}_2^{|S|} \rightarrow \mathbb{F}_2^n$ by $\delta_{S,[n]}(\mathbf{u}) = \mathbf{v}$ such that $\mathbf{v}_S = \mathbf{u}$ and $\mathbf{v}_{[n]\setminus S} = \mathbf{0}$, where $\mathbf{u} \in \mathbb{F}_2^{|S|}$, $\mathbf{v} \in \mathbb{F}_2^n$.

2.2. Syndrome decoding problems

In this subsection, we introduce the syndrome decoding problem, ISD framework and some needed definitions of in coding theory.

Definition 1. *A subspace of \mathbb{F}_2^n with dimensional k is called as a binary linear code \mathcal{C} with length n and dimension k , which is generally denote as an $[n, k]$ code.*

Definition 2. If the rows of a matrix $\mathbf{G} \in \mathbb{F}_2^{k \times n}$ form a basis of an $[n, k]$ code \mathcal{C} , then the matrix \mathbf{G} is called as a generator matrix of code \mathcal{C} . Meanwhile, a matrix $\mathbf{H} \in \mathbb{F}_2^{(n-k) \times n}$ is a parity-check matrix of \mathcal{C} if its kernel corresponds to an $[n, k]$ code \mathcal{C} .

Definition 3. A syndrome \mathbf{s} of a random vector $\mathbf{v} \in \mathbb{F}_2^n$ is obtained as $\mathbf{s} = \mathbf{v}\mathbf{H}^\top$, where \mathbf{H} is a parity-check matrix of an $[n, k]$ code, and one has $\mathbf{v} \in \mathcal{C}$ if and only if $\mathbf{v}\mathbf{H}^\top = 0$.

Problem 1 ((n, k, t) -SD problem). Input a parity-check matrix $\mathbf{H} \in \mathbb{F}_2^{(n-k) \times n}$ of an $[n, k]$ linear code \mathcal{C} , a syndrome $\mathbf{s} \in \mathbb{F}_2^{n-k}$ and a given positive integer t , output an error vector $\mathbf{e} \in \mathbb{F}_2^n$ such that $\mathbf{H}\mathbf{e}^\top = \mathbf{s}^\top$ and $wt_H(\mathbf{e}) = t$.

The SD problem plays an important role in coding theory, and also many variants of this problem were proposed in some cryptography scenarios. In order to describe a general framework of ISD algorithms more clearly, we introduce another problem, i.e. the multiple-solution syndrome decoding (MSSD) problem, which is equivalent to the subset sum problem in [31] and the checkable multiple syndrome decoding problem in [34].

Problem 2 ((n, k, t, m) -MSSD problem). Input a parity-check matrix $\mathbf{H} \in \mathbb{F}_2^{(n-k) \times n}$ of an $[n, k]$ code \mathcal{C} , a syndrome $\mathbf{s} \in \mathbb{F}_2^{n-k}$, and a given positive integer t , output a set $\{\mathbf{e} \in \mathbb{F}_2^n : \mathbf{H}\mathbf{e}^\top = \mathbf{s}^\top, wt(\mathbf{e}) = t\}$ with size 2^m .

2.3. The general framework of ISD algorithms

Before stating the general framework of ISD algorithms, we firstly give the definition of the information set of a binary linear code as follows.

Definition 4. An information set of an $[n, k]$ code \mathcal{C} is a size- k subset $I \subseteq [n]$ is called if $|\mathcal{C}_I| = |\mathcal{C}|$.

All of the advanced ISD algorithms of solving Problem 1, such as MMT's and BJMM's, intrinsically consist of the following framework with four steps:

- 1) **Permutation step.** Choose a random permutation matrix $\mathbf{P} \in \mathbb{F}_2^{n \times n}$ such that $(\mathbf{HP})_{[n] \setminus [k+\ell]}$ is partially reversible.
- 2) **Partial Gaussian elimination step.** Choose an invertible transformation matrix $\mathbf{U} \in \mathbb{F}_2^{(n-k) \times (n-k)}$ satisfying

$$\mathbf{UHP} = \begin{pmatrix} \widehat{\mathbf{H}} & \mathbf{0} \\ \widetilde{\mathbf{H}} & \mathbf{I}_{n-k-\ell} \end{pmatrix}, \quad \mathbf{U}\mathbf{s}^\top = \begin{pmatrix} \widehat{\mathbf{s}} \\ \widetilde{\mathbf{s}} \end{pmatrix},$$

where $\widehat{\mathbf{H}} \in \mathbb{F}_2^{\ell \times (k+\ell)}$, $\widetilde{\mathbf{H}} \in \mathbb{F}_2^{(n-k-\ell) \times (k+\ell)}$, $\widehat{\mathbf{s}} \in \mathbb{F}_2^\ell$ and $\widetilde{\mathbf{s}} \in \mathbb{F}_2^{n-k-\ell}$. Meanwhile, the error vector \mathbf{e} is split into two vectors $\widehat{\mathbf{e}} \in \mathbb{F}_2^{k+\ell}$ and $\widetilde{\mathbf{e}} \in \mathbb{F}_2^{n-k-\ell}$, i.e., $\mathbf{e}\mathbf{P} = (\widehat{\mathbf{e}}^\top | \widetilde{\mathbf{e}}^\top)$. Thus, we have

$$\widehat{\mathbf{H}}\widehat{\mathbf{e}} = \widehat{\mathbf{s}}, \quad (1)$$

$$\widetilde{\mathbf{H}}\widehat{\mathbf{e}} + \widetilde{\mathbf{e}} = \widetilde{\mathbf{s}}. \quad (2)$$

- 3) **MSSD step.** Solve a $(k+\ell, \ell, p, m)$ -MSSD problem, i.e., find 2^m error vectors $\widehat{\mathbf{e}}$ with $wt(\widehat{\mathbf{e}}) = p$ satisfying Equation (1).
- 4) **Test step.** For all of the solutions of MSSD step, compute $\widetilde{\mathbf{e}} = \widetilde{\mathbf{H}}\widehat{\mathbf{e}} + \widetilde{\mathbf{s}}$ according to Equation (2). If there exists $wt(\widetilde{\mathbf{e}}) = t - p$, then output $\mathbf{e} = (\widehat{\mathbf{e}}^\top | \widetilde{\mathbf{e}}^\top)\mathbf{P}^{-1}$. Else, go back to step 1).

The above framework is an iterative structure where each iteration is to size k an error vector with a fixed weight distribution and the number of the iteration depends on the success probability of Test step. According to the result in [34], the probability of success in step 4) is

$$P_{succ}^{-1} = \max \left\{ 1, \frac{\max \{1, \min \{ \binom{n}{t} \cdot 2^{-\ell}, 2^{n-k-\ell} \} \}}{2^m \cdot \binom{n-k-\ell}{t-p}} \right\}.$$

Thus, the running time and memory of the above framework are derived in the following lemma.

Lemma 1 ([34]). *For positive integer parameters n, k, t, ℓ, p, m , if the time complexities of the partial Gaussian elimination and solving a $(k+\ell, \ell, p, m)$ -MSSD problem are T_G and T_{MSSD} , respectively, then an (n, k, t) -SD problem is solved in time complexity*

$$T_{ISD} = O\left(P_{succ}^{-1} \cdot \max\{T_G, T_{MSSD}\}\right),$$

where P_{succ}^{-1} is defined as above.

2.4. K -list sum algorithms

Firstly we introduce the K -list sum problem as follows and then describe the solving algorithms.

Problem 3 (*K*-list sum problem). *Input* K sorted lists L_1, \dots, L_K , where each set L_i contains 2^m uniform random vectors of length n , *output* a set $\{(\mathbf{y}_1, \dots, \mathbf{y}_K) \in L_1 \times \dots \times L_K : \mathbf{y}_1 + \dots + \mathbf{y}_K = \mathbf{0}\}$.

In this paper, we consider a solving algorithm of a 4-list sum problem over \mathbb{F}_2 . In [24], author proposed a 4-list sum algorithm, where the underlying lists has the equal sizes, i.e., $|L_1| = |L_2| = |L_3| = |L_4|$. Here, we general the algorithm by setting

$$|L_1| = |L_3| = 2^{m_1} \geq |L_2| = |L_4| = 2^{m_2}.$$

In the following, we describe the 4-list sum algorithm in Algorithm 1 in pseudo-code form.

Algorithm 1 (4-list sum algorithm)

Input: three integers m_1, m_2 and $0 \leq v \leq m_2$ and 4 sorted lists L_1, L_2, L_3, L_4 with $|L_1| = |L_3| = 2^{m_1} \geq |L_2| = |L_4| = 2^{m_2}$, where each set L_i is composed of uniformly random vectors of length $n = m_1 + m_2 + v$.

Output: a size- 2^{m_1} set $L_{1234} = \{(\mathbf{x}_1, \dots, \mathbf{x}_4) \in L_1 \times \dots \times L_4 : \mathbf{x}_1 + \dots + \mathbf{x}_4 = \mathbf{0}\}$.

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1: Initialize  $L_{12}, L_{34}, L_{1234} \leftarrow \emptyset$ .
2: for all  $(\mathbf{0}|\mathbf{x}) \in 0^{m_2-v} \times \mathbb{F}_2^v$  do
3:   for all  $\mathbf{x}_1 \in L_1$  do
4:      $L_{12} \leftarrow L_{12} \cup \{\mathbf{x}_1 + \mathbf{x}_2 : (\mathbf{x}_1 + \mathbf{x}_2)_{[m_2]} = (\mathbf{0}|\mathbf{x}), \mathbf{x}_2 \in L_2\}$ 
5:   end for
6:   for all  $\mathbf{x}_3 \in L_3$  do
7:      $L_{34} \leftarrow L_{34} \cup \{\mathbf{x}_3 + \mathbf{x}_4 : (\mathbf{x}_3 + \mathbf{x}_4)_{[m_2]} = (\mathbf{0}|\mathbf{x}), \mathbf{x}_4 \in L_4\}$ 
8:   end for
9:   for all  $\mathbf{x}_{12} \in L_{12}$  do
10:     $L_{1234} \leftarrow L_{1234} \cup \{\mathbf{x}_{12} + \mathbf{x}_{34} : (\mathbf{x}_{12} + \mathbf{x}_{34})_{[n] \setminus [m_2]} = \mathbf{0}, \mathbf{x}_{34} \in L_{34}\}$ 
11:   end for
12: end for

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For a 4-list sum algorithm, the four given lists are randomly combined in pairs to generate two intermediate sets such that $m_2 - v$ entries are zero and the other v entries are randomly selected from \mathbb{F}_2 for each element of the intermediate sets, and then the final solution set is generated by finding collisions between the elements of the two intermediate sets on the remaining entries and traversing all choices on the v entries. Note that the intermediate

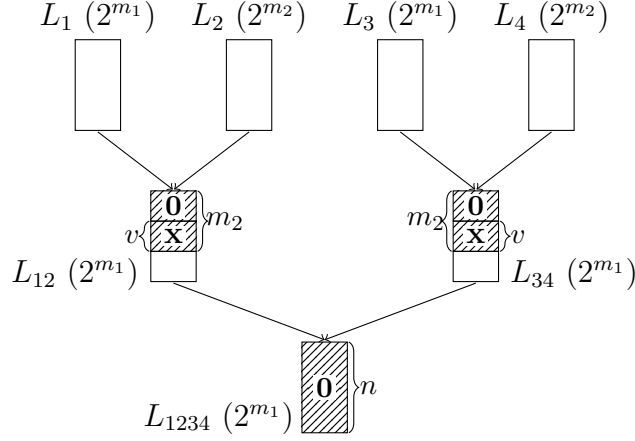


Figure 1: 4-list sum algorithm with memory 2^{m_1} and length n with $m_1 \geq m_2$.

and final sets constructed by the meet-in-the-middle technology have the equal size 2^{m_1} . Next, we illustrate this process in the following Figure.

Then, the running time and memory cost of Algorithm 1 are given as follows.

Lemma 2. *The running time and memory of Algorithm 1 are T_4 and M_4 , respectively, i.e.,*

$$T_4 = O(2^{m_1+v}), \quad M_4 = O(2^{m_1}).$$

Proof. Since the elements in the sets L_1 and L_2 are randomly uniform and $|L_1| = 2^{m_1} \geq |L_2| = 2^{m_2}$, then we have

$$|L_{12}| = \frac{|L_1| \cdot |L_2|}{2^{m_2}} = \frac{2^{m_1} \cdot 2^{m_2}}{2^{m_2}} = 2^{m_1}.$$

Thus, Lines 3-5 in Algorithm 1 use the meet-in-the-middle to construct the set L_{12} from L_1 and L_2 with the time complexity

$$T_{12} = \max\{|L_1|, |L_2|, |L_{12}|\} = 2^{m_1}.$$

Similarly, the time complexities of constructing the sets L_{34} and L_{1234} are

$$T_{34} = T_{1234} = 2^{m_1}$$

where the sets L_{34} and L_{1234} have the equal size 2^{m_1} .

The above process was repeated 2^v times and so the total time complexity is

$$T_4 = 2^v \cdot \max\{T_{12}, T_{34}, T_{1234}\} = 2^{m_1+v},$$

and the total memory consumption is

$$M_4 = \max\{|L_1|, |L_2|, |L_3|, |L_4|, |L_{12}|, |L_{1234}|\} = 2^{m_1},$$

□

3. Our ISD algorithms

In this section, we firstly sketch a general ISD framework by 4-list sum algorithm, and then analyze the time complexities of the ISD algorithms by applying four different versions of the 4-list sum algorithm, respectively.

According to Section 2.3, almost all ISD algorithms can be represented as a framework with 4 steps, i.e., 1) Permutation step, 2) Partial Gaussian elimination step, 3) MSSD step and 4) Test step. Here, we adopt the 4-list sum algorithm in the step 3).

3.1. Applying 4-list sum algorithm to the ISD framework

For an (n, k, t) -SD problem, after the steps 1) and 2) of the general ISD algorithm, two equations are yielded as follows:

$$\widehat{\mathbf{H}}\hat{\mathbf{e}} = \hat{\mathbf{s}}, \quad (3)$$

$$\widetilde{\mathbf{H}}\hat{\mathbf{e}} + \tilde{\mathbf{e}} = \tilde{\mathbf{s}}. \quad (4)$$

where $\ell < n - k$ is a positive integer, $\widehat{\mathbf{H}} \in \mathbb{F}_2^{\ell \times (k+\ell)}$, $\widetilde{\mathbf{H}} \in \mathbb{F}_2^{(n-k-\ell) \times (k+\ell)}$, $\hat{\mathbf{s}} \in \mathbb{F}_2^\ell$, $\tilde{\mathbf{s}} \in \mathbb{F}_2^{n-k-\ell}$, $\hat{\mathbf{e}} \in \mathbb{F}_2^{k+\ell}$, $\tilde{\mathbf{e}} \in \mathbb{F}_2^{n-k-\ell}$ and $wt(\hat{\mathbf{e}}) = p$. Next, we use the 4-list sum algorithm to find $\hat{\mathbf{e}}$ satisfying Equation (3).

Firstly, we need to divide $\widehat{\mathbf{H}}$ and $\hat{\mathbf{e}}$ into four disjoint parts, respectively, i.e.,

$$\begin{aligned} \widehat{\mathbf{H}}\hat{\mathbf{e}} &= \begin{pmatrix} \mathbf{H}_1 & \mathbf{H}_2 & \mathbf{H}_3 & \mathbf{H}_4 \end{pmatrix} \begin{pmatrix} \mathbf{e}_1 \\ \mathbf{e}_2 \\ \mathbf{e}_3 \\ \mathbf{e}_4 \end{pmatrix} \\ &= \mathbf{H}_1\mathbf{e}_1 + \mathbf{H}_2\mathbf{e}_2 + \mathbf{H}_3\mathbf{e}_3 + \mathbf{H}_4\mathbf{e}_4 = \hat{\mathbf{s}}, \end{aligned}$$

and then construct the four sets as follows:

$$L_i = \left\{ (\mathbf{e}_i, \mathbf{H}_i \mathbf{e}_i) : \mathbf{H}_i \in \mathbb{F}_2^{\ell \times n_i}, \mathbf{e}_i \in \mathbb{F}_2^{n_i}, wt(\mathbf{e}_i) = p_i \right\}$$

for $i = 1, 2, 3$ and

$$L_4 = \left\{ (\mathbf{e}_4, \mathbf{H}_4 \mathbf{e}_4 + \hat{\mathbf{s}}) : \mathbf{H}_4 \in \mathbb{F}_2^{\ell \times n_4}, \mathbf{e}_4 \in \mathbb{F}_2^{n_4}, wt(\mathbf{e}_4) = p_4 \right\},$$

where $n_1 + n_2 + n_3 + n_4 = k + \ell$, $p_1 + p_2 + p_3 + p_4 = p$ and the sizes of the sets are

$$|L_i| = \binom{n_i}{p_i} = 2^{m_i}, \quad i = 1, 2, 3, 4.$$

For the second components of the sets L_1, L_2, L_3, L_4 , whenever a 4-tuple $(\mathbf{H}_1 \mathbf{e}_1, \mathbf{H}_2 \mathbf{e}_2, \mathbf{H}_3 \mathbf{e}_3, \mathbf{H}_4 \mathbf{e}_4)$ satisfying the condition $\mathbf{H}_1 \mathbf{e}_1 + \mathbf{H}_2 \mathbf{e}_2 + \mathbf{H}_3 \mathbf{e}_3 + \mathbf{H}_4 \mathbf{e}_4 + \hat{\mathbf{s}} = \mathbf{0}$ is found, record and store the corresponding 4-tuple $(\mathbf{e}_1, \mathbf{e}_2, \mathbf{e}_3, \mathbf{e}_4)$. If we set

$$|L_1| = |L_3| = 2^{m_1} \geq |L_2| = |L_4| = 2^{m_2},$$

by using the 4-list sum algorithm of Algorithm 1, we obtain 2^{m_1} solutions $\hat{\mathbf{e}} = (\mathbf{e}_1^\top | \mathbf{e}_2^\top | \mathbf{e}_3^\top | \mathbf{e}_4^\top)$ satisfying Equation (3).

Next, run the step 4) of the general ISD framework to obtain the final solution to the (n, k, t) -SD problem. The above process is described in Algorithm 2 in pseudo-code form.

Algorithm 2 (ISD by 4-list sum algorithm)

Input: a parity-check matrix $\mathbf{H} \in \mathbb{F}_2^{(n-k) \times n}$, a syndrome $\mathbf{s} \in \mathbb{F}_2^{n-k}$ and the parameters $t, m_1, \ell, v, p_i, n_i$ for $i = 1, 2, 3, 4$, which satisfy $m_1 = \binom{n_1}{p_1} = \binom{n_3}{p_3} \geq \binom{n_2}{p_2} = \binom{n_4}{p_4} := m_2$ and $\ell = m_1 + m_2 + v$.

Output: an error vector $\mathbf{e} \in \mathbb{F}_2^{n-k}$ satisfying $\mathbf{H}\mathbf{e}^\top = \mathbf{s}^\top$ and $wt(\mathbf{e}) = t$.

- 1: Choose a set $I \in [n]$ of size $k + \ell$ containing an information set.
- 2: Partition I into four disjoint sets I_1, I_2, I_3, I_4 with $|I_i| = n_i$ for $i = 1, 2, 3, 4$.
- 3: Choose an binary invertible matrix $\mathbf{U} \in \mathbb{F}_2^{(n-k) \times (n-k)}$, which satisfies

$$\mathbf{U}\mathbf{H}_I = \begin{pmatrix} \widehat{\mathbf{H}} \\ \widetilde{\mathbf{H}} \end{pmatrix}, \quad \mathbf{U}\mathbf{H}_{[n] \setminus I} = \begin{pmatrix} \mathbf{0} \\ \mathbf{I}_{n-k-\ell} \end{pmatrix}, \quad \mathbf{U}\mathbf{s}^\top = \begin{pmatrix} \widehat{\mathbf{s}} \\ \widetilde{\mathbf{s}} \end{pmatrix},$$

where $\widehat{\mathbf{H}} \in \mathbb{F}_2^{\ell \times (k+\ell)}$, $\widetilde{\mathbf{H}} \in \mathbb{F}_2^{(n-k-\ell) \times (k+\ell)}$, $\widehat{\mathbf{s}} \in \mathbb{F}_2^\ell$, $\widetilde{\mathbf{s}} \in \mathbb{F}_2^{n-k-\ell}$.

- 4: Let $\mathbf{H}_i = (\widehat{\mathbf{H}})_{I_i}$ for $i = 1, 2, 3, 4$. Build four sets

$$L_i = \left\{ (\mathbf{e}_i, \mathbf{H}_i \mathbf{e}_i) : \mathbf{H}_i \in \mathbb{F}_2^{\ell \times n_i}, \mathbf{e}_i \in \mathbb{F}_2^{n_i}, wt(\mathbf{e}_i) = p_i \right\}, \quad i = 1, 2, 3,$$

$$L_4 = \left\{ (\mathbf{e}_4, \mathbf{H}_4 \mathbf{e}_4 + \widehat{\mathbf{s}}) : \mathbf{H}_4 \in \mathbb{F}_2^{\ell \times n_4}, \mathbf{e}_4 \in \mathbb{F}_2^{n_4}, wt(\mathbf{e}_4) = p_4 \right\}.$$

- 5: Initialize $L_{12}, L_{34}, L_{1234} \leftarrow \emptyset$.
- 6: **for all** $(\mathbf{0}|\mathbf{y}) \in 0^{m_2-v} \times \mathbb{F}_2^v$ **do**
- 7: **for all** $(\mathbf{e}_1, \mathbf{y}_1) \in L_1$ **do**
- 8: $L_{12} \leftarrow L_{12} \cup \{(\mathbf{e}_{12}, \mathbf{H}_{12} \mathbf{e}_{12}) : I_{12} = I_1 \cup I_2, \mathbf{e}_{12} = \delta_{I_1, I_{12}}(\mathbf{e}_1) + \delta_{I_2, I_{12}}(\mathbf{e}_2), \mathbf{H}_{12} = \widehat{\mathbf{H}}_{I_{12}}, (\mathbf{y}_1 + \mathbf{y}_2)_{[m_2]} = (\mathbf{0}|\mathbf{y}), (\mathbf{e}_2, \mathbf{y}_2) \in L_2\}$.
- 9: **end for**
- 10: **for all** $(\mathbf{e}_3, \mathbf{y}_3) \in L_3$ **do**
- 11: $L_{34} \leftarrow L_{34} \cup \{(\mathbf{e}_{34}, \mathbf{H}_{34} \mathbf{e}_{34} + \widehat{\mathbf{s}}) : I_{34} = I_3 \cup I_4, \mathbf{H}_{34} = \widehat{\mathbf{H}}_{I_{34}}, \mathbf{e}_{34} = \delta_{I_3, I_{34}}(\mathbf{e}_3) + \delta_{I_4, I_{34}}(\mathbf{e}_4), (\mathbf{y}_3 + \mathbf{y}_4)_{[\ell_1]} = (\mathbf{0}|\mathbf{y}), (\mathbf{e}_4, \mathbf{y}_4) \in L_4\}$.
- 12: **end for**
- 13: **for all** $(\mathbf{e}_{12}, \mathbf{y}_{12}) \in L_{12}$ **do**
- 14: $L_{1234} \leftarrow L_{1234} \cup \{\widehat{\mathbf{e}} = \delta_{I_{12}, I}(\mathbf{e}_{12}) + \delta_{I_{34}, I}(\mathbf{e}_{34}) : (\mathbf{y}_{12} + \mathbf{y}_{34})_{[\ell] \setminus [m_2]} = \mathbf{0}, (\mathbf{e}_{34}, \mathbf{y}_{34}) \in L_{34}\}$.
- 15: **end for**
- 16: **end for**
- 17: **for all** $\widehat{\mathbf{e}} \in L_{1234}$ **do**
- 18: **if** $wt(\widetilde{\mathbf{s}} - \widetilde{\mathbf{H}}\widehat{\mathbf{e}}) = t - (p_1 + p_2 + p_3 + p_4)$ **then**
- 19: Output: $\mathbf{e} = \delta_{I, [n]}(\widehat{\mathbf{e}}) + \delta_{[n] \setminus I, [n]}(\widetilde{\mathbf{s}} - \widetilde{\mathbf{H}}\widehat{\mathbf{e}})$.
- 20: **else**
- 21: Return to Step 1.
- 22: **end if**
- 23: **end for**

3.2. Analyzing the complexities

In this section, we firstly derive the details of the time and memory complexities for Algorithm 2, and then give four instantiations of the parameters.

Theorem 1. *Let $|L_1| = |L_3| = 2^{m_1} \geq |L_2| = |L_4| = 2^{m_2}$ in Algorithm 2. The ISD by a 4-list sum algorithm solves an (n, k, t) -SD problem with the time complexity T_{ISD} and memory consumption M_{ISD} , where*

$$T_{ISD} = O\left(\max\{n(n-k)^2, 2^{m_1+v}\} \cdot \max\left\{1, \frac{\max\{1, \min\{\binom{n}{t} \cdot 2^{-\ell}, 2^{n-k-\ell}\}\}}{\binom{n-k-\ell}{t-p} \cdot 2^{m_1}}\right\}\right),$$

$$M_{ISD} = O(\max\{n(n-k), 2^{m_1}\}),$$

with $0 \leq p \leq \min\{(k+\ell), t\}, 0 \leq v \leq m_2, \ell = m_1 + m_2 + v$.

Proof. Lines 1-3 in Algorithm 2 represent the partial Gaussian elimination step in the general ISD algorithm, which has the time complexity

$$T_G = (n-k)^2(n+1).$$

Note that the sets L_1, L_2, L_3, L_4 have the sizes

$$|L_1| = |L_3| = 2^{m_1} \geq |L_2| = |L_4| = 2^{m_2},$$

and then lines 4-16 solve a $(k+\ell, \ell, p, m_1)$ -MSSD problem by using the 4-list sum algorithm described in Algorithm 1. According to Lemma 2, the time complexity and memory are

$$T_{MSSD} = 2^{m_1+v}, M_{MSSD} = 2^{m_1}.$$

In addition, the memory of the parity-check matrix is $n(n-k)$ and hence the result is obtained according to Lemma 1. \square

Next, we give four instantiations of Theorem 1 by choosing different parameters m_1 and m_2 as follows:

- Version 1 (V1): $\binom{\lfloor (k+\ell)/4 \rfloor}{p/4} = 2^{m_1} = 2^{m_2}$,
- Version 2 (V2): $\binom{\lfloor (k+\ell)/3 \rfloor}{p/3} = 2^{m_1}$, $\binom{\lfloor (k+\ell)/6 \rfloor}{p/6} = 2^{m_2}$,

- Version 3 (V3): $\binom{\lfloor (k+\ell)/5 \rfloor}{p/5} = 2^{m_1}$, $\binom{\lfloor (k+\ell)/10 \rfloor}{p/10} = 2^{m_2}$,
- Version 4 (V4): $\binom{\lfloor (k+\ell)/7 \rfloor}{p/7} = 2^{m_1}$, $\binom{\lfloor (k+\ell)/14 \rfloor}{p/14} = 2^{m_2}$,

where ℓ needs to satisfy $\ell = m_1 + m_2 + v$ according to Theorem 1.

4. Numerical Results

4.1. Bit-security levels

In this section, we used four instantiations of Algorithm 2 to reestimate the bit securities of the parameters suggested by the Classic McEliece, HQC and BIKE schemes.

Since all advanced ISD algorithms consume an exponential amount of memory while improving the time complexity, and the large memories actually affect the time complexity during the implementation of ISD algorithms. Therefore, both McEliece team [36] and [21] suggested that one should penalize the time complexity with memory appropriately when considering the specific security level of the scheme, i.e., multiplying the running time by some type of penalty factor. In order to compare with the experimental results in [20], we use three memory models, i.e., constant memory, logarithmic memory penalty and cube-root memory penalty, and so the corresponding results are given as T_{ISD} , $T_{ISD} \cdot \log M_{ISD}$ and $T_{ISD} \cdot \sqrt[3]{M_{ISD}}$ in the following tables.

In addition, for the HQC and BIKE schemes with the cyclic structures, [20] and [22] used the Decoding-One-Out-of-Many (DOOM) technique to obtain a speedup by a factor of roughly \sqrt{k} in the running time of the ISD algorithm. In this paper, we still adopt the DOOM technique to obtain bit-security levels for HQC and BIKE.

In the above tables, “ V_1 ”, “ V_2 ”, “ V_3 ” and “ V_4 ” denote four instantiations of 4-list sum algorithms in Section 3.2, respectively, and “ $M < 60$ ” indicates that the final time complexities of the ISD algorithms are computed with the constant memory no more than 60 bits. Meanwhile, “logarithmic” and “cube-root” represent the time complexities of ISD algorithms under logarithmic memory penalty and cube-root memory penalty, respectively.

In order to compare with the results in [20], the values in parentheses refer to the gain of the bit-security levels obtained by our algorithms relative to that in [20], i.e., $T_{[20]} - T_{\text{ours}}$.

Table 1: Bit-security levels of Classic McEliece.

	Category 1		Category 3		Category 5a		Category 5b		Category 5c	
	M	T	M	T	M	T	M	T	M	T
<u>constant:</u>										
$M \leq 60$	[20] 60	145	60	187	58	262	60	263	59	298
	[22] –	145.47	–	188.16	–	263.16	–	263.64	–	298.65
	[23] 46	143.4	53	184.4	57	257.7	58	258.1	54	293.8
	V1 26	140.04 (4.96)	27	181.22 (5.78)	29	255.02 (6.98)	38	254.21 (8.79)	39	289.07 (8.93)
	V2 35	139.49 (5.51)	37	179.70 (7.30)	39	254.37 (7.63)	39	255.48 (7.52)	58	289.36 (8.64)
	V3 36	138.44 (6.56)	38	179.44 (7.56)	40	255.06 (6.94)	40	255.27 (7.73)	41	291.06 (6.94)
	V4 52	139.71 (5.29)	54	180.88 (6.12)	58	253.71 (8.29)	58	254.83 (8.17)	60	289.71 (8.29)
<u>logarithmic:</u>										
$M \leq 60$	[20] 89	147	113	187	165	253	160	253	194	283
	[22] –	151.06	–	193.59	–	268.66	–	269.17	–	304.19
	V1 26	144.74 (2.26)	27	185.97 (1.03)	29	259.88 (6.88)	38	259.46 (6.46)	39	294.36 (11.36)
	V2 35	144.62 (2.38)	37	184.90 (2.10)	39	259.66 (6.66)	39	260.77 (7.77)	58	295.21 (12.21)
	V3 36	143.61 (3.39)	38	184.69 (2.31)	40	260.38 (7.38)	40	260.59 (7.59)	41	296.42 (13.43)
	V4 52	145.41 (1.59)	54	186.64 (0.36)	58	259.57 (6.57)	58	160.69 (7.69)	60	295.62 (12.62)
<u>cube-root:</u>										
$M \leq 60$	[20] 25	156	26	199	36	275	36	276	47	312
	[22] –	157.25	–	199.69	–	276.13	–	276.97	–	313.12
	V1 18	148.05 (7.95)	23	189.32 (9.68)	29	264.69 (10.31)	29	265.69 (10.31)	30	300.91 (11.09)
	V2 35	151.16 (4.84)	37	192.03 (6.97)	39	267.37 (7.63)	39	268.48 (7.52)	40	303.71 (8.29)
	V3 36	150.44 (5.56)	38	192.11 (6.89)	40	268.39 (6.61)	40	268.60 (7.50)	41	304.73 (7.27)
	V4 52	157.05 (1.05)	54	198.88 (0.12)	58	273.05 (1.95)	58	274.17 (1.83)	60	309.71 (2.29)

Table 2: Bit-security levels of HQC and BIKE.

		Category 1		Category 3		Category 5	
		M	T	M	T	M	T
<u>constant: $M \leq 61$</u>							
BIKE-message	[20]	40	146	43	211	61	276
BIKE-message	[22]	–	142.35	–	206.41	–	272.26
BIKE-message	[23]	31	139.9	34	204.1	36	268.6
BIKE-message	V1	29	135.07 (10.93)	31	198.81 (12.19)	32	263.92 (12.08)

BIKE-key	[20]	40	147	57	210	61	278
BIKE-key	[22]	–	143.73	–	205.93	–	274.13
BIKE-key	[23]	31	140.7	34	203.6	36	270.6
BIKE-key	V1	29	142.50 (4.50)	31	205.47 (4.53)	32	273.55 (4.45)

HQC	[20]	39	145	44	213	39	276
HQC	[22]	–	141.16	–	208.19	–	271.14
HQC	[23]	32	139.1	36	206.2	38	267.6
HQC	V1	30	134.74 (10.26)	32	201.43 (11.57)	27	263.36 (12.64)

<u>logarithmic:</u>							
BIKE-message	[20]	31	150	33	215	34	281
BIKE-message	[22]	–	142.64	–	206.79	–	272.84
BIKE-message	V1	29	139.93 (10.07)	31	203.76 (11.24)	32	268.92 (12.08)

BIKE-key	[20]	31	151	33	215	34	283
BIKE-key	[22]	–	144.24	–	206.90	–	275.50
BIKE-key	V1	29	147.36 (3.64)	31	210.42 (4.58)	32	278.55 (4.45)

HQC	[20]	32	150	34	218	36	280
HQC	[22]	–	141.49	–	208.61	–	271.62
HQC	V1	30	139.65 (10.35)	32	206.43 (11.57)	27	268.40 (11.60)

<u>cube-root:</u>							
BIKE-message	[20]	30	152	32	217	33	283
BIKE-message	[22]	–	143.37	–	207.77	–	273.99
BIKE-message	V1	29	144.74 (7.26)	31	209.14 (7.86)	32	274.59 (8.41)

BIKE-key	[20]	30	153	32	217	33	285
BIKE-key	[22]	–	144.89	–	207.79	–	276.60
BIKE-key	V1	29	152.17 (0.83)	31	215.80 (1.2)	32	284.22 (0.78)

HQC	[20]	31	151	33	220	35	282
HQC	[22]	–	142.33	–	209.71	–	272.90
HQC	V1	30	144.74 (6.26)	32	212.10 (7.90)	27	274.36 (7.64)

Table 3: The parameters in Table 1.

	Category 1 (ℓ, p, v)	Category 3 (ℓ, p, v)	Category 5a (ℓ, p, v)	Category 5b (ℓ, p, v)	Category 5c (ℓ, p, v)
<u>constant:</u>					
V1	(57, 12, 5)	(60, 12, 6)	(63, 12, 5)	(76, 16, 0)	(78, 16, 0)
V2	(52, 12, 0)	(55, 12, 0)	(58, 12, 0)	(58, 12, 0)	(86, 18, 0)
V3	(45, 10, 0)	(47, 10, 0)	(49, 10, 0)	(50, 10, 0)	(51, 10, 0)
V4	(60, 14, 0)	(62, 14, 0)	(67, 14, 0)	(67, 14, 0)	(69, 14, 0)
<u>logarithmic:</u>					
V1	(57, 12, 5)	(60, 12, 6)	(63, 12, 5)	(76, 16, 0)	(78, 16, 0)
V2	(52, 12, 0)	(55, 12, 0)	(58, 12, 0)	(58, 12, 0)	(86, 18, 0)
V3	(45, 10, 0)	(47, 10, 0)	(49, 10, 0)	(50, 10, 0)	(51, 10, 0)
V4	(60, 14, 0)	(62, 14, 0)	(67, 14, 0)	(67, 14, 0)	(69, 14, 0)
<u>cube-root:</u>					
V1	(49, 8, 13)	(52, 8, 14)	(63, 12, 5)	(63, 12, 5)	(65, 12, 5)
V2	(52, 12, 0)	(55, 12, 0)	(58, 12, 0)	(58, 12, 0)	(60, 12, 0)
V3	(45, 10, 0)	(47, 10, 0)	(49, 10, 0)	(50, 10, 0)	(51, 10, 0)
V4	(60, 14, 0)	(62, 14, 0)	(67, 14, 0)	(67, 14, 0)	(69, 14, 0)

Table 4: The parameters in Table 2.

		Category 1 (ℓ, p, v)	Category 3 (ℓ, p, v)	Category 5 (ℓ, p, v)
<u>constant:</u>				
	BIKE-message	V1 (68, 8, 19)	(70, 8, 20)	(73, 8, 21)
	BIKE-key	V1 (65, 8, 19)	(70, 8, 20)	(73, 8, 21)
	HQC	V1 (68, 8, 20)	(73, 8, 21)	(76, 8, 22)
<u>logarithmic:</u>				
	BIKE-message	V1 (68, 8, 19)	(70, 8, 20)	(73, 8, 21)
	BIKE-key	V1 (65, 8, 19)	(70, 8, 20)	(73, 8, 21)
	HQC	V1 (68, 8, 20)	(73, 8, 21)	(76, 8, 22)
<u>cube-root:</u>				
	BIKE-message	V1 (68, 8, 19)	(70, 8, 20)	(73, 8, 21)
	BIKE-key	V1 (65, 8, 19)	(70, 8, 20)	(73, 8, 21)
	HQC	V1 (68, 8, 20)	(73, 8, 21)	(76, 8, 22)

Table 5: Security parameters of Classic McEliece, HQC and BIKE.

Category		1	3	5(a)	5b	5c
Classic McEliece	n	3488	4608	6688	6960	8192
	k	2720	3360	5024	5413	6528
	t	64	96	128	119	128
BIKE	n	24646	49318	81946		
	k	12323	24659	40973		
	-message	t	134	199	264	
	-key	t	142	206	274	
HQC	n	35338	71702	115274		
	k	17669	35851	57637		
	t	132	200	262		

Remark 1. Table 1 compares the bit-security levels between our algorithms and [20] for Classic McEliece in 2^{60} -bit limitation for memory and we provide the following observations.

- a) In constant memory and cube-root memory penalty, for all parameters, our algorithms always have lower bit-security levels than all results in [20, 22, 23], and in particular, the best result is 11.09 bits lower than that of [20] for $n = 8192$ in cube-root memory penalty model.
- b) For the scenario of logarithmic memory penalty, our algorithms only have the improvements of the bit-security levels for the parameter sets of the categories 1 and 3 in [20].
- c) In constant memory, for parameters $n = 4608$, $n = 6688$ and $n = 6960$, the bit complexity of [20] are 187, 262, and 263, respectively, but our results are 179.44, 253.71, and 254.21, respectively. Obviously, our bit-complexity for these three parameters do not achieve the claimed security levels, while [20] show that only $n = 4608$ does not achieve the claimed security levels.

Remark 2. Table 2 compares the bit-security levels between our algorithms and [20] for HQC and BIKE in 2^{60} -bit limitation for memory and we give some considerations as follows.

- a) *For the parameters of HQC and BIKE, V1 of the four algorithm instantiations in this paper always performs the best, and so we only list the bit-security levels for the instantiation V1.*
- b) *In three memory models, our bit-security levels are always lower than those of [20] for all parameters of both HQC and BIKE.*

5. Conclusion

In this paper, we apply four types of 4-list sum algorithms to the ISD framework to derive lower time complexity in low memory. Furthermore, the new bit-security levels of the Classic McEliece, HQC and BIKE schemes are determined by using our improved ISD algorithms under three memory models, i.e., constant memory, logarithmic memory penalty and cube-root memory penalty, respectively, and we compare these bit-security levels with the latest results in [20]. The numerical results show that for the parameter sets of the Classic McEliece, HQC and BIKE schemes, our algorithms always have lower bit-security levels than [20] in 2^{60} -bit limitation for memory. Therefore, our results provide more possibilities for security estimation of the code-based cryptographic schemes and are indicative in the design and implementation of the schemes in the future.

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