

Bitslicing Arithmetic/Boolean Masking Conversions for Fun and Profit

with Application to Lattice-Based KEMs

Olivier Bronchain and Gaëtan Cassiers

Crypto Group, ICTEAM Institute, UCLouvain, Louvain-la-Neuve, Belgium.
 {olivier.bronchain,gaetan.cassiers}@uclouvain.be

Abstract. The performance of higher-order masked implementations of lattice-based based key encapsulation mechanisms (KEM) is currently limited by the costly conversions between arithmetic and boolean masking. While bitslicing has been shown strongly speed up to masked implementations of symmetric primitives, it has never been used in higher-order arithmetic-to-boolean and boolean-to-arithmetic masking conversion gadgets. In this paper, we first show that bitslicing can indeed accelerate existing conversion gadgets. We then optimize these gadgets, exploiting the degrees of freedom offered by bitsliced implementations. As a result, we introduce new arbitrary-order boolean masked addition, arithmetic-to-boolean and boolean-to-arithmetic masking conversion gadgets, each in two variants: modulo 2^k and modulo p (for any integers k and p). Practically, our new gadgets achieve a speedup of up to 25x over the state of the art. Turning to the KEM application, we develop the first open-source embedded (Cortex-M4) implementations of **Kyber768** and **Saber** masked at arbitrary order. The implementations based on the new bitsliced gadgets achieve a speedup of 1.8x for **Kyber** and 3x for **Saber**, compared to the implementation based on state of the art gadgets. The bottleneck of the bitslice implementations is the masked Keccak-f [1600] permutation.

Keywords: Masking · Lattice-based KEM · Kyber · Saber · Bitslice · PINI

1 Introduction

Quantum attacks against traditional asymmetric cryptography schemes (based on RSA, discrete logarithm or elliptic curves) have been a growing concern. This led to the introduction of post-quantum (PQ) schemes for signatures and key encapsulation mechanisms (KEM), many of which are based on lattices. Their implementation raises new challenges, in particular for embedded systems that require protection against side-channel attacks (SCA) such as power or electro-magnetic analysis [KJJ99, QS01]. Such attacks are particularly powerful against many state-of-the-art PQ KEMs due to their usage of the Fujisaki-Okamoto (FO) transform [FO99]: an adversary can carefully forge ciphertexts to trigger the re-encryption of a single bit whose value depends on a secret (sub-)key. The leakage from this re-encryption depends only on this single secret bit, which is thus easily recovered and from which information on the secret key can be retrieved [RRCB20, UXT⁺22]. Strong protection against side-channel attacks is therefore a must for lattice-based cryptography in embedded systems deployed on-the-field [ABH⁺22].

The most studied countermeasure against SCA is masking, whose core idea is to randomize the intermediate computations while maintaining their correctness [CJRR99, ISW03]. When using arithmetic masking, each intermediate variable x of the original computation is replaced by a sharing (x_0, \dots, x_{d-1}) such that $x = x_0 + \dots + x_{d-1} \pmod p$

for some integer p , where the addition degenerates to the boolean XOR in the particular case $p = 2$, which is therefore named boolean masking. Masked implementations are usually analyzed in the t -probing model [ISW03], which formalizes the notion of t -order security by requiring all tuples of t intermediate values in the computation to be independent of any secret value. However, security in the t -probing model is not composable: the sequential use of two t -probing secure gadgets (gadgets are algorithms computing on masked values) is not necessarily probing secure [CPRR13]. To circumvent the t -probing security analysis of a full masked cryptographic algorithm (which is impractical), composable security properties have been introduced, such as (strong-)non-interference (NI/SNI) [BBD⁺16], or probe-isolating non-interference (PINI) [CS20]. These properties are stronger than probing security and gadgets that satisfy them can be securely composed.

The protection of masking does not come for free and sometimes leads to orders of magnitudes larger costs than non-masked implementation [BGR⁺21]. A key question in the design of masked implementation is therefore the minimization of computational cost, which is particularly critical when considering embedded software PQ KEMs implementations. Indeed, unprotected implementations of PQ KEMs are already computationally expensive [KRSS], and on top of this a high masking order is needed, due to the low intrinsic noise level on commercial micro-controllers [BS20, BS21]. Masking overheads (in randomness usage and runtime) generally grow quadratically with the number of shares, except for masked linear operations modulo p , which incur only linear computational overhead (and no randomness usage).

Lattice-based KEMs use many arithmetic operations in the field of integers modulo p (e.g., $p = 3329$, 2^{10} or 2^{13}). These operations are often linear with respect to the secret values [ABD⁺19, BBMD⁺19], which leads to a very efficient implementation when using arithmetic masking modulo p [RRVV15, OSPG18]. These KEMs also use symmetric cryptography primitives to generate pseudo-randomness, which are often best implemented using boolean masking since they contain many bit-level operations [BDPA13, GR16, BDM⁺20]. As a result, conversions between arithmetic and boolean masking are key components of masked implementations of lattice-based KEMs.

These conversions are a bottleneck of the current state of the art implementations [BGR⁺21, FBR⁺22] and they are an active field of research. Arbitrary-order arithmetic-to-boolean masking conversions (A2B) were first introduced in [CGV14] for fields of characteristic two and a masking order equal to half of the number of shares. In a series of works [CGTV15, BBE⁺18, SPOG19], the construction was generalized to arbitrary p and optimal masking order $(d - 1)$, along with optimizations to reach $\mathcal{O}(d^2 \log(\log p))$ CPU instructions. Alternative table-based constructions have also been introduced, achieving similar properties [CGMZ21b]. Boolean-to-arithmetic conversion (B2A) has also been studied thoroughly. The original arbitrary-order B2A [CGV14] is based on A2B and benefited from its improvements, as well as being proven secure at optimal security order in [BBE⁺18]. Recently, efficient B2A algorithms for conversion of a single bit have been introduced [SPOG19, CGMZ21b], from which a B2A algorithm for arbitrary number of bits can be derived. Finally, the *compression* modulo p is an operation which consists in a linear scaling then a rounding, and is commonly found in Lattice-based KEMs. Its masking can be performed thanks to A2B conversions and has been recently optimized in [BPO⁺20, BDH⁺21, CGMZ21a].

In parallel over the last years, the *bitslicing* technique has brought significant speed improvements to software implementations of symmetric cryptography, be it masked [GR16, BDM⁺20] or not [Bih97, AP21]. In short, bitslicing leverages the intrinsic parallelism of bitwise operations within processors. E.g., a processor that manipulates 32-bit integers can perform 32 bitwise operations with a single instruction. Therefore, bitslicing only applies to algorithms whose operations are bitwise, such as [GLSV14], but sometimes an algorithm can be re-written to use bit-level operations (while preserving efficiency) [BMP13]. In particular, boolean masking is very well suited to bitslicing since most boolean masking

gadgets only use bit-level operations, whereas arithmetic masking gadgets use additions and multiplications (whose equivalent bitwise circuits are large) and therefore do not benefit from bitslicing. To the best of our knowledge, despite the large number of works on A2B and B2A, no efficient bitslice implementation of such conversion algorithm has ever been introduced.

Contributions We introduce the usage of bitslicing for the masked implementation of lattice-based cryptography, and for this purpose, we design new masked gadgets for all masking orders. Our new gadgets are A2B and B2A conversions. Additionally, we also design a new addition gadget for boolean masking which is used in the conversion gadgets. These gadgets come in two variants: one for arithmetic modulo any integer p , and one for the particular case of arithmetic modulo 2^k , which is more efficient. All our gadgets are PINI, and are therefore easily composed.

As a testbed for our new gadgets, we develop arbitrary-order masked Kyber and Saber implementations on the Cortex-M4 platform. First, for each of them, we build a non-bitsliced masked implementation (hereafter named respectively K1 and S1) based on state-of-the-art components: the gadgets and implementations of Coron et al. [CGMZ21b], some gadgets from [SPOG19] and some (non-masked) functions from the NIST PQ benchmarking project (PQM4) [KRSS]. To the best of our knowledge, implementations K1 and S1 are the first open-source¹ embedded masked at arbitrary order Kyber and Saber software implementations. Next, we build new bitslice implementations (named K2 and S2) that use our new gadgets and satisfy the PINI secure composition strategy. Implementation K2 achieves a speedup of up to 1.84 over K1, and up to 8.7 over the best reported performance in the state-of-the-art on an embedded platform [BGR⁺21]. Similarly S2 achieves a speedup to 3x over S1. In both K2 and S2, the execution time is dominated by hashing respectively by 50% for Kyber and 72% for Saber.

Finally, exploiting the PQM4 [KRSS] framework, we release an easily reusable and extensible implementation of **Kyber768**.² These implementations are based on the same gadgets as K2 and S2.

Organization In Section 2, we introduce some preliminaries on masking and describe the state-of-the-art gadgets for boolean masked addition, A2B masking conversion, as well as B2A. Next, we present our new gadgets and prove that they are PINI in Section 3, before comparing their performance to the state-of-the-art in Section 4. Finally, we describe our Kyber and Saber implementations and measure their performance in Section 5.

2 Background

In this Section, we first introduce our notations and the masking schemes we use, then we describe state-of-the-art gadgets that operate on masked values to perform simple operations, namely addition and conversion between masking schemes.

Notations We denote by $\llbracket x, y \rrbracket$ the set $[x, y] \cap \mathbb{N}$ and by $\llbracket x, y \llbracket$ the set $[x, y) \cap \mathbb{N}$. For non-negative integers x and y , $x \oplus y$ is the (unsigned) integer whose binary representation is the bitwise XOR of the binary representations of x and y .

¹Available at https://github.com/uclcrypto/pqm4_masked/files/8049274/implements.zip.

²Available at https://github.com/uclcrypto/pqm4_masked.

2.1 Masking and elementary gadgets

In this paper, we consider two masking schemes: arithmetic and boolean masking. A secret variable $x \in \llbracket 0, p \rrbracket$ for some integer p is represented by the d -shares arithmetic sharing

$$\mathbf{x}^{A,p} = \left(\mathbf{x}_i^{A,p} \right)_{i=0,\dots,d-1} \in \llbracket 0, p \rrbracket^d \text{ such that } x = \mathbf{x}_0^{A,p} + \mathbf{x}_1^{A,p} + \dots + \mathbf{x}_{d-1}^{A,p} \pmod p.$$

In order to achieve $d - 1$ -order security for x , any set of $d - 1$ shares must be uniformly distributed. Similarly, the k -bit boolean sharing of a secret $x \in \llbracket 0, 2^k \rrbracket$ is

$$\mathbf{x}^{B,k} = \left(\mathbf{x}_i^{B,k} \right)_{i=0,\dots,d-1} \in \llbracket 0, 2^k \rrbracket^d \text{ such that } x = \mathbf{x}_0^{B,k} \oplus \mathbf{x}_1^{B,k} \oplus \dots \oplus \mathbf{x}_{d-1}^{B,k}.$$

Computation on sharings is performed by algorithms named gadgets. The inputs and outputs of a d -share gadget are d -shares sharings, which allows such gadgets to be composed: the composition of multiple gadgets (which must all have the same number of shares) results in a composite gadget. The input sharings of the composing gadgets (named hereafter sub-gadgets) may be the input sharing of the composite gadget, or an output sharing of another sub-gadget.

For both arithmetic and boolean masking, the operations that are linear with respect to the sharing operation are implemented by simple gadgets: the operation can be applied share-wise, hence the computational cost is $\mathcal{O}(d)$. In particular, for arithmetic (respectively boolean) masking, one such operation is the addition modulo p (resp. bitwise XOR) of two shared variables. We denote these algorithms as $+^A$ (resp. \oplus^B).

The ISW multiplication gadget [ISW03], which we denote **SecAnd** allows to compute bitwise AND of boolean-shared values at a randomness and computational cost $\mathcal{O}(d^2)$. This gadget may also be used to compute the product modulo p of two arithmetically shared secrets.

A last commonly used gadget is the refresh gadget, which implements the identity function, but re-randomizes the sharing. This gadget is sometimes used to ensure the security of a computation that composes multiple simpler gadgets.

2.2 Composable probing security

In this paper, we target $(d - 1)$ -probing security for our d -shares implementations. That is, the statistical distribution of any $d - 1$ intermediate values (named probes) in our computation should be independent of any secret. We build our masked gadgets by composing multiple smaller gadgets. However, probing security is not composable [CPRR13]: composing $(d - 1)$ -probing secure gadgets is not enough to ensure $(d - 1)$ -probing security.

As a result, we consider stronger security definitions which are composable. The two following definitions were introduced in [BBD⁺16].

Definition 1 (t -NI). A gadget is t -Non-Interfering (t -NI) if every set of t probes can be simulated by using at most t shares of each input sharing.

Definition 2 (t -SNI). A gadget with one output sharing is t -Strong-Non-Interfering (t -SNI) if every set of t_1 probes on the internal values and t_2 probes on the output shares, with $t_1 + t_2 \leq t$, can be simulated by using at most t_1 shares of each input sharing.

The $+^A$ and \oplus^B gadgets are $(d - 1)$ -NI while the ISW multiplication is $(d - 1)$ -SNI. Furthermore, the refresh gadget obtained by setting one input sharing of the ISW multiplication to $(1, 0, \dots, 0)$ is also SNI, and this set of gadgets enables to securely mask any computation [BBD⁺16].

Composition based on the NI and SNI definitions requires usage of refresh gadgets, which may significantly increase the computational and randomness cost. More recently, Cassiers and Standaert [CS20] introduced a new definition which allows to remove those refresh gadgets.

Definition 3 (t -PINI). A gadget is t -Probe-Isolating-Non-Interfering (t -PINI) if, for every set P of t_1 probes on the internal values and set $A \subset \llbracket 0, d \rrbracket$ with $t_1 + |A| \leq t$, there exists a set $B \subset \llbracket 0, d \rrbracket$ with $|B| \leq t_1$ such that the probes in P and the output shares whose index (i.e., position of the share in the sharing) belongs to A can be simulated by using the input shares whose share index belongs to $A \cup B$.

Following [CGZ20], we say in the following that a gadget with d shares is PINI if it is $(d-1)$ -PINI, since this implies that it is t -PINI for any t . The $+^A$ and \oplus^B are *share-isolating*: all the computation on the input and output shares with a given share index is isolated from computations for any other share index. All share-isolating gadgets are PINI [CS20], but the ISW multiplication is not PINI. There however exists a PINI multiplication gadget ([CS20], Algorithm 2) with a similar cost as the ISW multiplication: same amount of randomness and roughly double the number of arithmetic operations. Finally, PINI gadgets are trivially composable: the composition of t -PINI gadgets is t -PINI [CS20], which enables composition without the use of refresh gadgets.

2.3 Modular addition in boolean masking

We first consider the addition modulo 2^k of two k -bit boolean sharings, and denote this gadget as **SecAdd**. It can be implemented by taking the boolean circuit of a k -bit binary adder, rewriting it to only use AND and XOR gates, and finally implementing this circuit with 1-bit **SecAnd** and \oplus^B gadgets. The 1-bit inputs of this circuit are obtained by selecting single bit sharings in the k -bit input sharings. Using a chain of full-adders, this technique yields a complexity of $\mathcal{O}(kd^2)$ operations (each on single-bit words).

This technique has been refined in [CGTV15] by using the Kogge-Stone (KS) adder. This circuit allows to perform some boolean operations in parallel, that is, with multiple-bit **SecAnd** and \oplus^B gadgets. This gives a complexity of $\mathcal{O}(\log(k)d^2)$ operations (on up-to k -bit words). Using adequate refreshing [BBE⁺18], the **SecAdd** gadget is made $(d-1)$ -NI.

Algorithm 1 **SecAddModp** $_k^d$ from [BBE⁺18] (NI)

Input: Boolean sharings $\mathbf{x}^{B,k}$ and $\mathbf{y}^{B,k}$, integer p such that $p < 2^k$ and $x, y \in \llbracket 0, p \rrbracket$.

Output: Boolean sharing $\mathbf{z}^{B,k}$ such that $z = x + y \pmod p$.

- 1: $\mathbf{p}^{B,k+1} \leftarrow (2^k - p, 0, \dots, 0)$
 - 2: $\mathbf{s}^{B,k+1} \leftarrow \text{SecAdd}_{k+1}^d(\mathbf{x}^{B,k}, \mathbf{y}^{B,k})$ ▷ Left 0-extend the input sharings by 1 bit.
 - 3: $\mathbf{s}'^{B,k+1} \leftarrow \text{SecAdd}_{k+1}^d(\mathbf{s}^{B,k+1}, \mathbf{p}^{B,k+1})$
 - 4: $\mathbf{b}^{B,1} \leftarrow \mathbf{s}'^{B,k+1}[k]$
 - 5: $\mathbf{c}^{B,1} \leftarrow \text{RefreshSNI}_1^d(\mathbf{b}^{B,1})$
 - 6: $\mathbf{c}'^{B,1} \leftarrow \neg \text{RefreshSNI}_1^d(\mathbf{b}^{B,1})$
 - 7: $\mathbf{c}^{B,k} \leftarrow \text{BitCopyMask}_k^d(\mathbf{c}^{B,1}, 2^k - 1)$ ▷ Copy input sharing where bitmask $(2^k - 1)$ is set.
 - 8: $\mathbf{c}'^{B,k} \leftarrow \text{BitCopyMask}_k^d(\mathbf{c}'^{B,1}, 2^k - 1)$
 - 9: $\mathbf{z}^{B,k} \leftarrow \text{SecAnd}_k^d(\mathbf{s}^{B,k+1} \llbracket 0, k \rrbracket, \mathbf{c}^{B,k}) \oplus^B \text{SecAnd}_k^d(\mathbf{s}'^{B,k+1} \llbracket 0, k \rrbracket, \mathbf{c}'^{B,1})$ ▷ MUX
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Next, we consider the **SecAddModp** gadget which performs the addition modulo p . The construction of **Algorithm 1** (from [BBE⁺18]) is based on the **SecAdd** gadget. Namely, it first computes the sum s of the inputs x and y on $k+1$ (to avoid overflow and thus modulo 2^k reduction), then adds $2^k - p$ to obtain s' . The most significant bit of s' indicates whether $x + y \geq p$. Based on this bit, either s or s' is selected as the output, using a MUX implemented with **SecAnd** and \oplus^B gadgets. Finally the most significant bit is dropped to

get the result on k bits. The complexity is still $\mathcal{O}(\log(k)d^2)$ operations on up-to k -bit words.

2.4 Arithmetic-to-boolean masking conversion

Coron et al. [CGV14] introduced a simple way to convert from arithmetic to boolean masking (**SecA2BModp**): mask with boolean masking each arithmetic share, then perform the addition modulo p of the resulting sharings. Remarking that the addition of d' arithmetic shares can be securely masked using d' -shares boolean masking instead of d , and organizing the $d - 1$ additions to perform in a binary tree (halving the number of shares at each layer) leads to [Algorithm 2](#) (from [SPOG19]), which has a complexity of $\mathcal{O}(\log(k)d^2)$ on up-to k -bit words. As an alternative, a table-based **SecA2BModp** implementation with the same complexity was recently introduced in [CGMZ21b].

Algorithm 2 **SecA2BModp** $_k^d$ from [SPOG19] (SNI)

Input: d shares arithmetic sharing \mathbf{x}^{A_p} , integer p such that $p < 2^k$ and $x \in \llbracket 0, p \rrbracket$.

Output: d shares boolean sharing $\mathbf{z}^{B,k}$ such that $z = x$.

```

1: if  $d = 1$  then
2:    $\mathbf{z}^{B,k} \leftarrow \mathbf{x}^{A_p}$ 
3: else
4:    $\mathbf{y}^{B,k} \leftarrow \text{SecA2BModp}_k^{\lfloor d/2 \rfloor} \left( \mathbf{x}_{\llbracket 0, \lfloor d/2 \rfloor \rrbracket}^{A_k} \right)$ 
5:    $\mathbf{y}'^{B,k} \leftarrow \text{SecA2BModp}_k^{d - \lfloor d/2 \rfloor} \left( \mathbf{x}_{\llbracket \lfloor d/2 \rfloor, d \rrbracket}^{A_k} \right)$ 
6:    $\mathbf{y}^{B,k} \leftarrow \text{RefreshSNI}_k^d \left( \left( \mathbf{y}_0^{B,k}, \mathbf{y}_1^{B,k}, \dots, \mathbf{y}_{\lfloor d/2 \rfloor - 1}^{B,k}, 0, \dots, 0 \right) \right)$   $\triangleright$  Expand to  $d$  shares.
7:    $\mathbf{y}'^{B,k} \leftarrow \text{RefreshSNI}_k^d \left( \left( 0, \dots, 0, \mathbf{y}_{\lfloor d/2 \rfloor}^{B,k}, \dots, \mathbf{y}_{d-1}^{B,k} \right) \right)$   $\triangleright$  Expand to  $d$  shares.
8:    $\mathbf{z}^{B,k} \leftarrow \text{SecAddModp}_k^d \left( \mathbf{y}^{B,k}, \mathbf{y}'^{B,k} \right)$ 

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2.5 Boolean-to-arithmetic masking conversion

Similarly to arithmetic to boolean, there are multiple efficient techniques for boolean-to-arithmetic conversion. First, one may generate $d - 1$ random arithmetic shares, generate a d -share boolean masking of the opposite of their sum (using **SecA2BModp**), add this to the input sharing (with **SecAddModp**), and finally unmask (that is, XOR the shares together) the result to get the last arithmetic share. This idea, originally introduced in [CGTV15], has been adapted to the modulo p setting in [BBE⁺18] (see [Algorithm 3](#)). This gadget is $(d - 1)$ -SNI.³

Second, Schneider et al. [SPOG19] introduced a conversion based on the observation that if $x, y \in \llbracket 0, 1 \rrbracket$, $x \oplus y = x + y - 2xy$. The gist of the conversion algorithm is to start from a 1-bit boolean sharing $\mathbf{x}^{B,1}$, then arithmetically mask each share, and finally use the previous equation to compute the XOR of these arithmetic sharings. This single-bit conversion algorithm may then be applied to each of a multi-bit input, and the results can be recombined sharewise (see [Algorithm 4](#)). Thanks to various optimizations of the algorithm [SPOG19], the complexity of this technique is $\mathcal{O}(kd^2)$ operations on k -bit words.

Finally, Coron et al. [CGMZ21b] introduced recently another conversion algorithm. This algorithm also performs k single-bit conversions, but the single-bit conversion is a table-based gadget.

³The proof that **SecB2AModp** is SNI is not given explicitly, in [BBE⁺18], but it can be deduced from the proof of Lemma 5, if **SecA2BModp** is SNI.

Algorithm 3 SecB2AModp_k^d from [BBE⁺18] (SNI)

Input: d shares boolean sharing $\mathbf{x}^{B,k}$, integer p such that $p < 2^k$ and $x \in \llbracket 0, p \llbracket$.

Output: d shares arithmetic sharing z^{A_p} such that $z = x$.

```

1: for  $i = 0$  to  $d - 2$  do
2:    $z_i^{A_k} \xleftarrow{\$} \mathbb{Z}_p$ 
3:    $z_i'^{A_k} \leftarrow p - z_i^{A_k}$ 
4:  $z_{d-1}'^{A_k} \leftarrow 0$ 
5:  $\mathbf{a}^{B,k} \leftarrow \text{SecA2BModp}_k^d(z_i'^{A_p})$ 
6:  $\mathbf{b}^{B,k} \leftarrow \text{SecAddModp}_k^d(\mathbf{a}^{B,k}, \mathbf{x}^{B,k})$ 
7:  $z_{d-1}^{A_k} \leftarrow \text{UnMask}_k^{d-1}(\text{FullRefresh}_k^{d-1}(\mathbf{b}^{B,k}))$ 

```

Algorithm 4 SecB2AModp_k^d based single bit conversion (from [SPOG19])

Input: d shares boolean sharing $\mathbf{x}^{B,k}$, integer p such that $p < 2^k$ and $x \in \llbracket 0, p \llbracket$.

Output: d shares arithmetic sharing z^{A_p} such that $z = x$.

```

1:  $z^{A_p} \leftarrow \text{SecB2AModpBit}_k^d(\mathbf{x}^{B,k}[k-1])$ 
2: for  $i = k - 2$  down to  $0$  do
3:    $\mathbf{b}^{A_p} \leftarrow \text{SecB2AModpBit}_k^d(\mathbf{x}^{B,k}[i])$ 
4:    $z^{A_p} \leftarrow 2 \cdot z^{A_p} + \mathbf{b}^{A_p} \pmod p$ 

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3 New gadgets

As we already mentioned in introduction, our starting point is the observation that high-level cryptographic algorithms such as Kyber have large data parallelism, hence they may benefit from bitsliced implementations for the boolean sharings (while staying non-bitsliced for the arithmetic sharings). We therefore introduce algorithms that work on 1-bit words, and which are therefore well-suited to bitslicing. As main elementary gadgets, we use \oplus^B and PINI SecAnd , where the SecAnd is more expensive than \oplus^B ($\mathcal{O}(d^2)$ vs. $\mathcal{O}(d)$).

The complexity of our algorithms is measured in single-bit operations, which may be amortized over the bit width w of the processor thanks to bitslicing. In order to compare to the algorithms of Section 2 which required k -bit words, the complexity should be divided by k to consider k -bit bitslicing (in practice, if $w > k$, the gain is even larger).

3.1 SecAdd: Bitslice boolean masked addition modulo 2^k

Our first algorithm is a new SecAdd implementation (Algorithm 6). Thanks to bitslicing, we do not have any structure constraint and simply aim to minimize the number of SecAnd . Therefore, we use a simple chain of full-adders, where the addition of x , y and c computes $a := x \oplus y$, then outputs $(a \oplus c, x \oplus a \cdot (x \oplus c))$. This requires only one SecAnd per full-adder, hence $k - 1$ in total (since the carry-out does not have to be computed for the most significant bits addition), which is the minimum achievable (we prove this in Appendix A). The total complexity of Algorithm 6 is $\mathcal{O}(kd^2)$ bit operations. We finally prove the security of this gadget.

Proposition 1. *Algorithm 6 and Algorithm 5 are PINI.*

Proof. These two gadgets are the composition of PINI gadgets, therefore they are PINI. \square

Algorithm 5 SecFullAdder^d New (PINI)

Input: Boolean sharings $\mathbf{x}^{B,1}$, $\mathbf{y}^{B,1}$ and $\mathbf{z}^{B,1}$.

Output: Boolean sharing $\mathbf{w}^{B,2}$ such that $w = x + y + z$.

- 1: $\mathbf{a}^{B,1} \leftarrow \mathbf{x}^{B,1} \oplus^{\mathbb{B}} \mathbf{y}^{B,1}$
 - 2: $\mathbf{w}^{B,2}[0] \leftarrow \mathbf{z}^{B,1} \oplus^{\mathbb{B}} \mathbf{a}^{B,1}$
 - 3: $\mathbf{w}^{B,2}[1] \leftarrow \mathbf{x}^{B,1} \oplus^{\mathbb{B}} \text{SecAnd}_1^d(\mathbf{a}^{B,1}, \mathbf{x}^{B,1} \oplus^{\mathbb{B}} \mathbf{z}^{B,1})$ ▷ PINI SecAnd
-

Algorithm 6 SecAdd_k^d New (PINI)

Input: Boolean sharings $\mathbf{x}^{B,k}$ and $\mathbf{y}^{B,k}$, such that $x, y \in \llbracket 0, 2^k \llbracket$.

Output: Boolean sharing $\mathbf{z}^{B,k}$ such that $z = x + y \pmod{2^k}$.

- 1: $\mathbf{c}^{B,1} \leftarrow (0, 0, \dots, 0)$
 - 2: **for** $i = 0$ to $k - 2$ **do**
 - 3: $\mathbf{t}^{B,2} \leftarrow \text{SecFullAdder}^d(\mathbf{x}^{B,k}[i], \mathbf{y}^{B,k}[i], \mathbf{c}^{B,1})$ ▷ Algorithm 5
 - 4: $(\mathbf{z}^{B,k}[i], \mathbf{c}^{B,1}) \leftarrow (\mathbf{t}^{B,2}[0], \mathbf{t}^{B,2}[1])$
 - 5: $\mathbf{z}^{B,k}[k - 1] \leftarrow \mathbf{x}^{B,k}[k - 1] \oplus^{\mathbb{B}} \mathbf{y}^{B,k}[k - 1] \oplus^{\mathbb{B}} \mathbf{c}^{B,1}$
-

3.2 SecAddMod_p : Bitslice boolean masked addition modulo p

Next, we consider addition modulo p . A simple approach is to adapt Algorithm 1 to use Algorithm 6 as SecAdd . On top of this adaptation, we remark that the MUX in Algorithm 1 costs $2k$ 1-bit SecAnd gadgets, and that we can replace it with the computation of $s' + p \cdot b \pmod{2^k}$, which costs one SecAdd_k^d (i.e., $k - 1$ single-bit SecAnd). This replacement is correct: if $b = 0$, the result is s' , and if $b = 1$ the result is $s' + p \pmod{2^k} = s$. Overall, our new addition modulo p requires two $k + 1$ -bit adders and one k -bit adder, totaling to $3k - 1$ 1-bit PINI SecAnd , hence $\mathcal{O}(kd^2)$ bit operations and randomness.

Proposition 2. *Algorithm 7 is PINI.*

Proof. All the sub-gadgets are PINI (BitCopyMask only replicates a sharing, hence it is share-isolating, which implies that it is PINI). \square

Algorithm 7 SecAddMod_p^d New (PINI)

Input: Boolean sharings $\mathbf{x}^{B,k}$ and $\mathbf{y}^{B,k}$, integer p such that $p < 2^k$ and $x, y \in \llbracket 0, p \llbracket$.

Output: Boolean sharing $\mathbf{z}^{B,k}$ such that $z = x + y \pmod{p}$.

- 1: $\mathbf{p}^{B,k+1} \leftarrow (2^{k+1} - p, 0, \dots, 0)$
 - 2: $\mathbf{s}^{B,k+1} \leftarrow \text{SecAdd}_{k+1}^d(\mathbf{x}^{B,k}, \mathbf{y}^{B,k})$ ▷ Use Algorithm 6.
 - 3: $\mathbf{s}'^{B,k+1} \leftarrow \text{SecAdd}_{k+1}^d(\mathbf{s}^{B,k+1}, \mathbf{p}^{B,k+1})$ ▷ Use Algorithm 6.
 - 4: $\mathbf{b}^{B,1} \leftarrow \mathbf{s}'^{B,k+1}[k]$
 - 5: $\mathbf{a}^{B,k} \leftarrow \text{BitCopyMask}_k^d(\mathbf{b}^{B,1}, p)$ ▷ Copy sharing b where bitmask p is set (computes $a = p \cdot b$).
 - 6: $\mathbf{z}^{B,k} \leftarrow \text{SecAdd}_k^d(\mathbf{a}^{B,k}, \mathbf{s}'^{B,k+1})$ ▷ Use Algorithm 6.
-

3.3 SecA2B : Bitslice arithmetic-to-boolean conversion modulo 2^k

For arithmetic modulo 2^k to boolean conversion (SecA2B), we take inspiration from the conversion algorithm of [SPOG19] (Algorithm 2). Namely, we also use a recursive structure

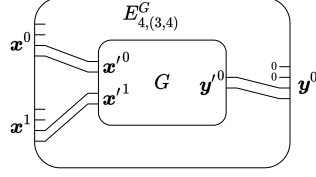


Figure 1: Example of 2-share to 4-share gadget embedding.

where two halves of the arithmetic sharing are first converted to boolean, then the two resulting sharing are added together. We use our new **SecAdd** (Algorithm 6) for this purpose, which, thanks to PINI composition, allows us to remove the refresh gadget, giving Algorithm 8 whose complexity is $\mathcal{O}(kd^2)$ random bits and single-bit operations.

Algorithm 8 SecA2B_k^d New (PINI)

Input: d shares arithmetic sharing $\mathbf{x}^{A_{2^k}}$, such that $x \in \llbracket 0, 2^k \rrbracket$.

Output: d shares boolean sharing $\mathbf{z}^{B,k}$ such that $z = x$.

- 1: **if** $d = 1$ **then**
 - 2: $\mathbf{z}^{B,k} \leftarrow \mathbf{x}^{A_{2^k}}$
 - 3: **else**
 - 4: $\mathbf{y}^{B,k} \leftarrow \text{SecA2B}_k^{\lfloor d/2 \rfloor}(\mathbf{x}^{A_{2^k}} \llbracket \llbracket 0, \lfloor d/2 \rfloor \rrbracket \rrbracket)$ ▷ $\lfloor d/2 \rfloor$ sharing.
 - 5: $\mathbf{y}'^{B,k} \leftarrow \text{SecA2B}_k^{d - \lfloor d/2 \rfloor}(\mathbf{x}^{A_{2^k}} \llbracket \llbracket \lfloor d/2 \rfloor, d \rrbracket \rrbracket)$ ▷ $d - \lfloor d/2 \rfloor$ sharing.
 - 6: $\mathbf{s}^{B,k} \leftarrow (\mathbf{y}_0^{B,k}, \mathbf{y}_1^{B,k}, \dots, \mathbf{y}_{\lfloor d/2 \rfloor - 1}^{B,k}, 0, \dots, 0)$ ▷ Expand to d shares.
 - 7: $\mathbf{s}'^{B,k} \leftarrow (0, \dots, 0, \mathbf{y}'_{\lfloor d/2 \rfloor}^{B,k}, \dots, \mathbf{y}'_{d-1}^{B,k})$ ▷ Expand to d shares.
 - 8: $\mathbf{z}^{B,k} \leftarrow \text{SecAdd}_k^d(\mathbf{s}^{B,k}, \mathbf{s}'^{B,k})$ ▷ Use Algorithm 6.
-

To prove that Algorithm 8 is PINI, we will use the PINI composition theorem from [CS20], and introduce a new technique to deal with the composition of PINI gadget with various number of shares. The core idea is to embed gadgets that use a lower number of shares into “virtual gadgets” that use more shares, with a mapping from the share indexes of the embedded gadgets to the indexes of the embedding gadgets. The embedding gadget discards the input shares that are not used, and sets to 0 the output shares that are not generated by the embedded gadgets, as illustrated in Figure 1.

Definition 4 (Gadget embedding). Let G be a d' -share gadget and let $m \in \llbracket 0, d \rrbracket^{d'}$ (with $d \geq d'$) have unique components ($m_i \neq m_j$ for all i, j). The d -share embedding of G with mapping m is the d -share gadget denoted $E_{d,m}^G$ and built as follows. $E_{d,m}^G$ has the same number of input and output sharings as G (with a one-to-one correspondence), and instantiates G internally. Each input sharing \mathbf{x}^j of G is generated as $(\mathbf{x}_{m_i}^j)_{i=0, \dots, d'-1}$, where \mathbf{x}^j is the input sharing of $E_{d,m}^G$ that corresponds to \mathbf{x}^j . For each output sharing \mathbf{y}^j of $E_{d,m}^G$, we set $\mathbf{y}_{m_i}^j = \mathbf{y}'_i^j$ for all $i \in \llbracket 0, d' \rrbracket$ where \mathbf{y}'^j is the output sharing of G that corresponds to \mathbf{y}^j . For all $i \in \llbracket 0, d \rrbracket$ that do not appear in m , we set $\mathbf{y}_i^j = 0$.

Lemma 1 (PINI embedding). *If G is a PINI gadget, its embedding $E_{d,m}^G$ is PINI for any d and m .*

Proof. We describe the $(d-1)$ -PINI simulator for $E_{d,m}^G$ that has to simulate a set of internal probes P and the output shares with index in B . First of all, P can be partitioned in a set P_G of probes in G and a set P_i of probes on the input shares. Next, B is partitioned as B_0 (the elements of B that appear in m), and B_1 (the remaining elements).

Let $B'_0 = \{i \in \llbracket 0, d' \rrbracket \text{ s.t. } m_i \in B_0\}$, we have $|B'_0| = |B_0|$. We use the PINI simulator of G to simulate the probes P_G and its output shares with index in B'_0 (which are the outputs of $E_{d,m}^G$ with index in B_0). This simulator requires knowledge of its input shares with index in $A' \cup B'$, for some A'_0 such that $|A'_0| \leq |P_G|$. Let us define $A_0 = \{m_i \text{ for all } i \in A'_0\}$, such that knowing the input shares of $E_{d,m}^G$ with index in $A_0 \cup B_0$ allows to send the inputs required to the simulator of G , hence to simulate the probes P_G and the output shares with index in B_0 .

Finally, the probes in P_i can be simulated with the input shares with index in A_1 , for some A_1 such that $|A_1| \leq |P_i|$, and all the output shares with index in B_1 can be trivially simulated (their value is always 0). As a result, all the required values can be simulated with the input shares of $E_{d,m}^G$ with index in $(A_0 \cup A_1) \cup B$, and $|A_0 \cup A_1| \leq |P|$. \square

Proposition 3. *Algorithm 8 is PINI.*

Proof. In the case $d = 1$, this is trivial. In the other cases, we decompose the gadget in three sub-gadgets: $E_{d,(0,\dots,[d/2]-1)}^{\text{SecA2B}_k^{[d/2]}}$ (which computes $\mathbf{s}^{B,k}$ from $\mathbf{x}^{A_{2^k}}$), $E_{d,([d/2],\dots,d-1)}^{\text{SecA2B}_k^{d-[d/2]}}$ (which computes $\mathbf{s}'^{B,k}$ from $\mathbf{x}^{A_{2^k}}$) and SecAdd_k^d (which computes $\mathbf{z}^{B,k}$ from $\mathbf{s}^{B,k}$ and $\mathbf{s}'^{B,k}$). Since $\text{SecA2B}_k^{[d/2]}$ and $\text{SecA2B}_k^{d-[d/2]}$ are PINI (by induction on d), their embeddings are PINI (by Lemma 1). Furthermore, SecAdd_k^d is PINI (Proposition 1). Therefore, Algorithm 8 is a composition of PINI gadgets. \square

3.4 SecA2BModp: Bitslice arithmetic-to-boolean conversion modulo p

A simple way to implement arithmetic modulo p to boolean masking conversion is to adapt Algorithm 8 (SecA2B) to use addition modulo p (SecAddModp, Algorithm 7) instead of addition modulo 2^k (SecAdd, Algorithm 6).⁴ On top of this adaptation, we can perform a small optimization inspired by the first-order A2B conversion from [FBR⁺22]: the first operation of our addition modulo p (Algorithm 7) is to subtract p from one of the two operands which can be done before double the number of shares in the A2B algorithm. This has no impact on the final result, but the cost of this subtraction is divided by about 4 (since this operation is in $\mathcal{O}(kd^2)$).

These changes do not impact the asymptotic complexity of the algorithm, which is still $\mathcal{O}(kd^2)$ random bits and single-bit operations.

Proposition 4. *Algorithm 9 is PINI.*

Proof. The proof is almost identical to the proof of Algorithm 9. The case $d = 1$ is trivial, and in the other cases, we exhibit a decomposition into PINI sub-gadgets. We first consider the d -share embedding of the $[d/2]$ -share composite gadget whose input is $\mathbf{x}^{A_p}[\llbracket 0, [d/2] \rrbracket]$ and whose output is $\mathbf{s}^{B,k+1}$. This gadget is the composition of two PINI gadgets ($\text{SecA2BModp}_k^{[d/2]}$ and $\text{SecAdd}_{k+1}^{[d/2]}$), hence it is PINI, and the embedding is PINI. Next, the d -share embedding of $\text{SecA2BModp}_k^{d-[d/2]}$ is PINI, as well as the other d -share sub-gadgets (SecAdd, BitCopyMask). \square

3.5 SecB2AModp: Bitslice boolean-to-arithmetic conversion modulo p

We now adapt in Algorithm 10 the SecB2AModp from [BBE⁺18] (Algorithm 3) to use our new SecA2BModp and SecAddModp algorithms.⁵ Furthermore, we replace the refresh gadget

⁴Another solution would be to use the compression algorithm (H0Compress) from [CGMZ21a] which it has a worse asymptotic complexity of $\mathcal{O}(kd^2 \log(d))$, but which might be an interesting alternative if we care only about small enough d .

⁵The conversion modulo 2^k SecB2A $_k^d$ can be implemented following Algorithm 10, using the new SecA2B and SecAdd instead of SecA2BModp and SecAddModp. The security proof is not changed.

Algorithm 9 SecA2BModp_k^d New (PINI)

Input: d shares arithmetic sharing \mathbf{x}^{A_p} , integer p such that $p < 2^k$ and $x \in \llbracket 0, p \rrbracket$.

Output: d shares boolean sharing $\mathbf{z}^{B,k}$ such that $z = x$.

```

1: if  $d = 1$  then
2:    $\mathbf{z}^{B,k} \leftarrow \mathbf{x}^{A_p}$ 
3: else
4:    $\mathbf{y}^{B,k} \leftarrow \text{SecA2BModp}_k^{\lfloor d/2 \rfloor}(\mathbf{x}^{A_p}[\llbracket 0, \lfloor d/2 \rfloor \rrbracket])$  ▷  $\lfloor d/2 \rfloor$  sharing.
5:    $\mathbf{y}'^{B,k} \leftarrow \text{SecA2BModp}_k^{d - \lfloor d/2 \rfloor}(\mathbf{x}^{A_p}[\llbracket \lfloor d/2 \rfloor, d \rrbracket])$  ▷  $d - \lfloor d/2 \rfloor$  sharing.
6:    $\mathbf{p}^{B,k+1} \leftarrow (2^k - p, 0, \dots, 0)$  ▷  $\lfloor d/2 \rfloor$  sharing.
7:    $\mathbf{s}^{B,k+1} \leftarrow \text{SecAdd}_{k+1}^{\lfloor d/2 \rfloor}(\mathbf{p}^{B,k+1}, \mathbf{y}^{B,k})$  ▷ Use Algorithm 6.
8:    $\mathbf{s}^{B,k+1} \leftarrow (\mathbf{y}_0^{B,k+1}, \mathbf{y}_1^{B,k+1}, \dots, \mathbf{y}_{\lfloor d/2 \rfloor - 1}^{B,k+1}, 0, \dots, 0)$  ▷ Expand to  $d$  shares.
9:    $\mathbf{s}'^{B,k} \leftarrow (0, \dots, 0, \mathbf{y}'_{\lfloor d/2 \rfloor}^{B,k}, \dots, \mathbf{y}'_{d-1}^{B,k})$  ▷ Expand to  $d$  shares.
10:   $\mathbf{u}^{B,k+1} \leftarrow \text{SecAdd}_{k+1}^d(\mathbf{s}^{B,k+1}, \mathbf{s}'^{B,k})$  ▷ Use Algorithm 6.
11:   $\mathbf{b}^{B,1} \leftarrow \mathbf{u}^{B,k+1}[k]$ 
12:   $\mathbf{a}^{B,k} \leftarrow \text{BitCopyMask}_k^d(\mathbf{b}^{B,1}, p)$  ▷ Copy sharing  $b$  where bitmask  $p$  is set ( $a := p \cdot b$ ).
13:   $\mathbf{z}^{B,k} \leftarrow \text{SecAdd}_k^d(\mathbf{a}^{B,k}, \mathbf{u}^{B,k+1})$  ▷ Use Algorithm 6.

```

to reduce its cost (from $\mathcal{O}(d^2)$ to $\mathcal{O}(d \log d)$). The new refresh gadget is input-output separative (IOS) and is described in Algorithm 17 (Appendix B). It is a generalization (to handle any value of d , and not only power of 2) of the one introduced in [GPRV21].

Definition 5 (IOS ([GPRV21], adapted)). A refresh gadget G is t -IOS if it is uniform⁶ and if for every admissible pair (\mathbf{x}, \mathbf{y}) ⁷ and every set of probes P with $|P| \leq t$, there exists a simulator which can perfectly simulate the probes by knowing only $|P|$ input shares and $|P|$ output shares. A refresh gadget with d shares is said to be IOS if it is $(d - 1)$ -IOS.

Algorithm 10 SecB2AModp_k^d New (PINI)

Input: d shares boolean sharing $\mathbf{x}^{B,k}$, integer p such that $p < 2^k$ and $x \in \llbracket 0, p \rrbracket$.

Output: d shares arithmetic sharing \mathbf{z}^{A_p} such that $z = x$.

```

1: for  $i = 0$  to  $d - 2$  do
2:    $\mathbf{z}_i^{A_p} \xleftarrow{\$} \mathbb{Z}_p$ 
3:    $\mathbf{z}'_i^{A_p} \leftarrow p - \mathbf{z}_i^{A_p}$ 
4:  $\mathbf{z}'_{d-1}^{A_p} \leftarrow 0$ 
5:  $\mathbf{a}^{B,k} \leftarrow \text{SecA2BModp}_k^d(\mathbf{z}'^{A_p})$  ▷ Use Algorithm 9.
6:  $\mathbf{b}^{B,k} \leftarrow \text{SecAddModp}_k^d(\mathbf{a}^{B,k}, \mathbf{x}^{B,k})$  ▷ Use Algorithm 7.
7:  $\mathbf{c}^{B,k} \leftarrow \text{RefreshIOS}_k^d(\mathbf{b}^{B,k})$  ▷ Use Algorithm 17.
8:  $\mathbf{z}_{d-1}^{A_p} \leftarrow \text{UnMask}_k^d(\mathbf{c}^{B,k})$  ▷ XOR all shares together.

```

Proposition 5. *Algorithm 10 is PINI.*

⁶Its output is an uniformly distributed sharing of x for any fixed input sharing \mathbf{x} . This implies that the distribution of the output sharing \mathbf{y} is independent of \mathbf{x} , conditioned on x .

⁷The pair (\mathbf{x}, \mathbf{y}) is admissible if there exists an assignment for the randomness used in G such that, on input \mathbf{x} , the output of G is \mathbf{y} .

Proof. We build a PINI simulator: given a set of probes P and share indexes B . We distinguish two cases: either (i) $d - 1 \in B$ or there is a probe of P in the `UnMask` gadget, or (ii) there is no such probe.

In case (ii), we remark that the gadgets `SecA2BModp` and `SecAddModp` are PINI, as well as `RefreshIOS` (Proposition 7, Appendix B). The probes in these gadgets can thus be simulated by knowing at most $|P|$ shares of $\mathbf{x}^{B,k}$ and some $z_i^{A_p}$ for $i \in \llbracket 0, d - 2 \rrbracket$. Such $z_i^{A_p}$, which also are the possible output shares to simulate, can be perfectly simulated since they are randomly generated by the gadget.

In case (i), we consider the $(d - 1)$ -PINI simulator that has to simulate the output shares with index in B and the internal probes P . Let (P_0, P_r, P_u) be a partition of P such that the probes of P_0 are in `SecA2BModp` and `SecAddModp`, the ones of P_r are in `RefreshIOS`, and the ones of P_u are in `UnMask`. We first describe the simulator, then prove that it is correct.

The PINI simulator for `SecB2AModp` first selects randomly $z_{d-1}^{A_p}$, then it generates a uniformly random sharing $\mathbf{c}^{B,k}$ of $z_{d-1}^{A_p}$, from which it can simulate any probe in P_u . Next, using the IOS simulator, it determines the set of share indexes B_r of $\mathbf{b}^{B,k}$ required to simulate P_r , with $|B_r| \leq |P_r|$ (some shares from $\mathbf{c}^{B,k}$ are also needed for this simulation, but they are already simulated). We then consider the PINI simulation of the composition of `SecA2BModp` and `SecAddModp` (since these two gadgets are PINI): the shares of $\mathbf{b}^{B,k}$ with index in B_r and the probes P_0 can be simulated with the shares of $\mathbf{x}^{B,k}$ and $z_i^{A_p}$ whose index belongs to $B_r \cup B_0$, for some B_0 such that $|B_0| \leq |P_0|$. Finally, the simulator completes the simulation by requesting the shares of $\mathbf{x}^{B,k}$ with index in $B_r \cup B_0$ and draws randomly all shares $z_i^{A_p}$ with $i \in (B_r \cup B_0 \cup B) \setminus \{d - 1\}$, which enables the simulation of the required $z_i^{A_p}$.

Let us first observe that the number of inputs required for the simulation is admissible: $|B_r \cup B_0| \leq |P|$. Further, let us denote by $B^* \subset \llbracket 0, d - 2 \rrbracket$ the set of i such that $z_i^{A_p}$ is used in the simulation (we exclude $z_{d-1}^{A_p}$ for now). We remark $B^* = B_r \cup B_0 \cup (B \setminus \{d - 1\})$, an therefore that $|B^*| \leq |P_r \cup P_0| + |B \setminus \{d - 1\}| \leq d - 2$ where the latter inequality comes from the hypothesis that either $|P_u| \geq 1$ (hence $|P_0 \cup P_r| + |B| \leq d - 2$), or $d - 1 \in B$ (hence $|P| + |B \setminus \{d - 1\}| \leq d - 2$). As a result $\llbracket 0, d - 2 \rrbracket \setminus B^* \geq 1$, and, taking $i^* \in \llbracket 0, d - 2 \rrbracket \setminus B^*$, we observe that $z_{i^*}^{A_p}$ is never used in the simulation.

We now show that the simulation is correct: for each value that is simulated, we show that its distribution matches the true distribution, and furthermore we prove that the simulation is consistent with (i.e., the simulated joint distribution is equal to the true distribution) the simulation of the values for which we already proved the correctness. First, the simulated shares $z_i^{A_p}$ (except $z_{d-1}^{A_p}$) and $z_i^{\prime A_p}$ follow the same distribution as in Algorithm 10. Next, since $z_{d-1}^{A_p} = z - \sum_{i=0}^{d-2} z_i^{A_p} \pmod p$ and since one of the terms of the sum ($z_{i^*}^{A_p}$) is not used in the simulation and is uniformly distributed, $z_{d-1}^{A_p}$ appears to the adversary as a fresh uniform value, and its simulation is correct. We continue with the correct simulation of the probes in P_0 and the shares $\mathbf{b}_i^{B,k}$: it follows from the PINI simulators of `SecA2BModp` and `SecAddModp`. Since `RefreshIOS` is uniform, its output sharing $\mathbf{c}^{B,k}$ is a uniform sharing of $z_{d-1}^{A_p}$ which is independent of $\mathbf{b}^{B,k}$. The simulation of the probes in P_r by the `RefreshIOS` simulator ensures that the simulation of these probes and of $\mathbf{c}^{B,k}$ are correct. Finally, the simulation of the probes P_u is trivially correct. \square

4 Gadgets performance

In this Section, we will compare the performance of each of our new gadgets to the state of the art gadgets implementing the same feature (ignoring the difference in security property).

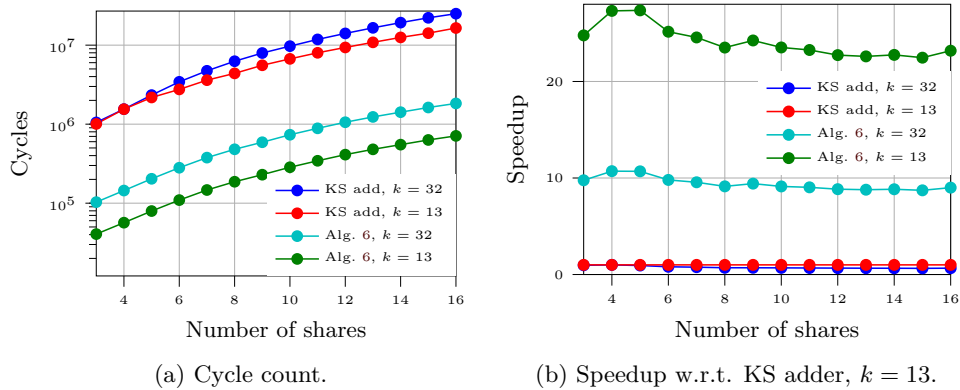


Figure 2: Performance comparison of SecAdd implementations.

We first describe the benchmark setup and the general implementation strategy, then we report the performance of state of the art gadgets compared to the new gadgets.

4.1 Benchmarking setup

We implement all the gadgets in C and measure the performance on a ARM Cortex-M4, a 32-bit micro-controller. Concretely, we target the NUCLEO-L4R5ZI development board which is used by the PQM4 benchmarking library [KRSS].⁸ The firmware and clock tree are the same as the one used in PQM4, as well as the performance measurement method (i.e., we use the cycle accurate counter DWT_CYCNT). In order to generate randomness, we use the dedicated TRNG. According to the datasheet, a fresh random 32-bit word is available in a dedicated TRNG register every 40 cycles. In practice, access to this TRNG is wrapped with checks to ensure that randomness is not used twice, which increases the cost of loading 32 bits of randomness to 53 cycles. In order to generate uniform randomness in \mathbb{Z}_p , we generate two random elements based on 32 random bits and use rejection sampling. Overall, one random over \mathbb{Z}_p is available every 26.5 cycles. When uniform randomness in \mathbb{F}_2^k with $k < 32$ is needed (e.g., in the Kyber implementation, $k = 13$ for the KS adder), we generate $\lceil 32/k \rceil$ k -bit words from 32 bits of randomness, dropping the remaining bits. Finally, we used naive implementation of the recursive algorithms, only forcing inlining at a few places where the control flow overhead was identified as a bottleneck.

In the rest of this Section, we report the performance of concrete implementations, for which we have to fix the value of p . We take the prime of Kyber: $p = 3329$, which implies that most of the gadgets will be benchmarked for $k = \lceil \log_2(p) \rceil = 12$. All the cycle counts reported in this Section are for 256 independent calls to a given gadgets since it is the polynomial size of Kyber. Since 256 is a multiple of the register width (32 bits), we fully exploit the bitslicing potential of the processor.

4.2 Performance of SecAdd_k^d

We first analyze masked adders on k bits. We compare in Figure 2 the Kogge-Stone adder from [BBE⁺18], which has a complexity of $\mathcal{O}(\log(k)d^2)$ word operations, and the Algorithm 6 which has a complexity of $\mathcal{O}(kd^2)$ bit operations. First, we observe that Algorithm 6 requires less cycles than the KS adder. For $k = 13$, Algorithm 6 is about 23 times faster and for $k = 32$, the speedup is about 9x. Interestingly, Algorithm 6 is faster than the KS adder despite its higher asymptotic complexity, thanks to the bitslicing gain.

⁸Our benchmarks are compiled with options `-O2 -f1to`, and we note that speedup figures for the `-O3` and `-Os` optimization levels are very similar.

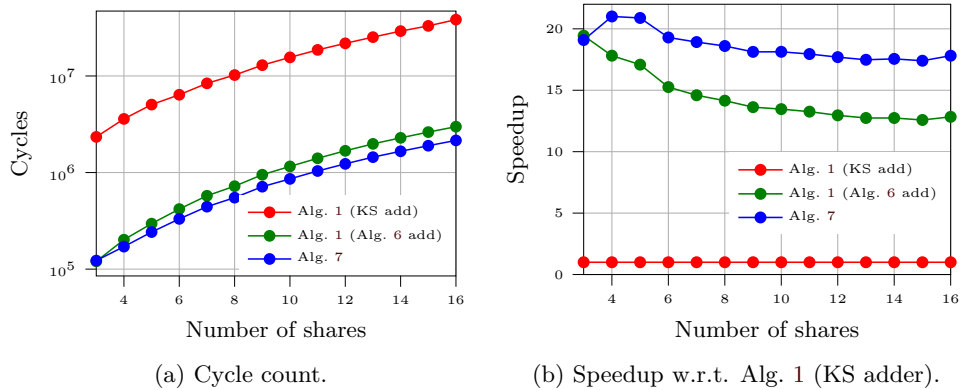


Figure 3: Performance comparison of SecAddModp_k^d implementations.

As expected from the complexities, the gain of [Algorithm 6](#) decreases as k increases. Yet for relevant parameters for lattice-based cryptography, it provides a significant improvement.

4.3 Performance of SecAddModp_k^d

Next, we compare in [Figure 3](#) the execution time for various SecAddModp_k^d gadgets. Concretely, we compare (i) [Algorithm 1](#) when using the KS adder (not bitsliced), (ii) [Algorithm 1](#) with the [Algorithm 6](#) as underlying SecAdd (hence leveraging bitslicing), and (iii) [Algorithm 7](#) (also using [Algorithm 6](#)). We observe that (ii) has a speedup of about 12x over (i), which is smaller than the improvement of 21x on the adder (SecAdd) itself. Indeed, the execution time of (ii) is dominated by the SecAdd calls and the MUX (Line 9) since both require in total $2(13 - 1)$ SecAnd executions, and while the speedup for the SecAdd part is 21x, the one for the MUX part is only the bitslicing gain of $32/12 = 2.7$ x. Finally, in case (iii), the dedicated gadget allows to roughly halve the cost of the MUX by replacing it with a SecAdd , which gives a speedup of about 1.3x over (ii).

4.4 Performance of SecA2BModp_k^d

Similarly, we compare the performance of SecA2BModp_k^d implementations in [Figure 4](#). The reference implementation (i) is [Algorithm 2](#) (with KS adder). We compare it to (ii) a modified [Algorithm 2](#) using the bitsliced adder ([Algorithm 7](#)), and to (iii) the new [Algorithm 9](#). We note that the speedup of (ii) over (i) is similar to the one we got for the corresponding SecAddModp gadgets (albeit a bit lower due to the presence of RefreshSNI whose bitslicing speedup is only $32/12$). The new gadget (iii) has a speedup of 2x over (ii), thanks to the removal of refresh gadgets and the execution of one SecAdd with the number of shares halved.

4.5 Performance of SecB2AModp_k^d

Finally, we compare in [Figure 5](#) the performance of various implementations of SecB2AModp . We consider as state of the art the algorithms from [\[SPOG19\]](#) and [\[CGMZ21b\]](#) which both implement SecB2AModp_k^d from single-bit conversions using [Algorithm 4](#). As a result, their computational cost is proportional to k , and we observe that they have comparable cost, with a small advantage for [\[SPOG19\]](#) (which agree with the results on Intel x86 processors of [\[CGMZ21b\]](#), Table 4).

Our bitsliced conversion gadget ([Algorithm 10](#)) always operates on $\lceil \log_2(p) \rceil$ bits (here, 12). Concretely, for 16 shares, the bitsliced conversion of any $x \in \mathbb{Z}_p$ is only twice as slow

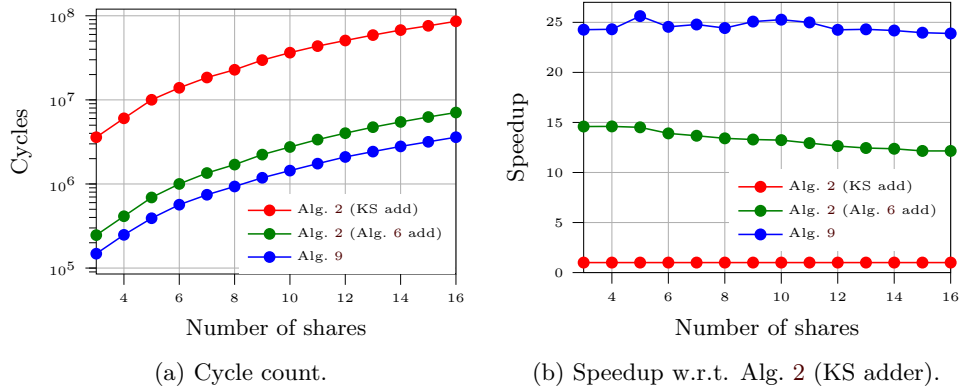


Figure 4: Performance comparison of SecA2BModp_{12}^d implementations.

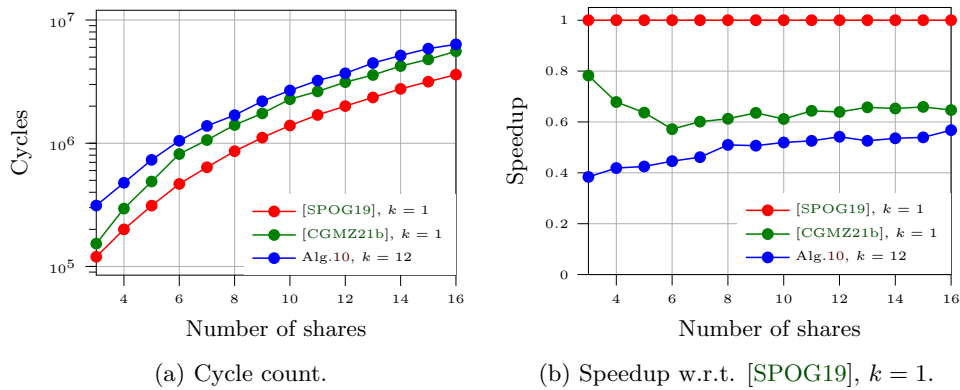


Figure 5: Performance comparison of SecB2AModp implementations.

as the state of the art single-bit conversions, and is therefore on par with state of the art 2-bit conversions. For larger k -bit conversions, the advantage of Algorithm 10 grows linearly with k .

5 Application to lattice-based KEMs

In this Section, we put our new gadgets together into an implementation of Kyber. We focus on Kyber768 to maximize comparability with the recent works of Coron et al. [CGMZ21b, CGMZ21a]. Eventually, we applied the same methodology to Saber and report the results.

5.1 Overview of masked Kyber

Kyber leverages the Fujisaki-Okamoto (FO) transform to transform a chosen-plaintext attack (CPA) secure public encryption scheme (PKE) into a chosen-ciphertext attack (CCA) secure KEM [FO99, ABD⁺19]. Kyber decapsulation is described Algorithm 11 where the ciphertext c is decrypted with $\text{CPAPKE.Dec}(\cdot)$ to obtain the message m' . This message is then re-encrypted with $\text{CPAPKE.Enc}(\cdot)$ to derive the ciphertext c' using some pseudo-randomness σ' derived from m' and the public key. The encapsulated secret K is then returned only if c and c' are identical, which ensures that the c has been derived from the public key. We focus on the masked implementation of Kyber.CCAKEM.Dec since it is the most sensitive to SCA [RRCB20, UXT⁺22]. In the following algorithms, green means

Algorithm 11 `Kyber.CCAKEM.Dec`(c, sk)

Input: Ciphertext $c = (c_u, c_v)$, secret key $sk := (\hat{s}, pk, \mathbb{H}(pk), z)$.

Output: Decapsulated secret K .

```
1:  $m' := \text{Kyber.CPAPKE.Dec}(\hat{s}, c)$ 
2:  $(\bar{K}', \sigma') := \mathbb{G}^d(m' || \mathbb{H}(pk))$ 
3:  $(c'_u, c'_v) := \text{Kyber.CPAPKE.Enc}(pk, m', \sigma')$ 
4: if  $(c_u = c'_u) \ \&\ \ (c_v = c'_v)$  then
5:    $K := \text{KDF}(\bar{K}' || \mathbb{H}(c))$ 
6: else
7:    $K := \text{KDF}(z || \mathbb{H}(c))$ 
```

Algorithm 12 `Kyber.CPAPKE.Dec`(\hat{s}, c)

Input: Secret key $\hat{s} \in \mathbb{R}_p^l$, ciphertext $c = (c_u, c_v)$.

Output: Plaintext m .

```
1:  $\mathbf{u} := \text{Decompress}_{p, d_u}^d(c_u) \triangleright \mathbf{u} \in \mathbb{R}_p^l, d_u = 10$ 
2:  $v := \text{Decompress}_{p, d_v}^d(c_v) \triangleright v \in \mathbb{R}_p, d_v = 4$ 
3:  $\hat{z} = \hat{s}^T \circ \text{NTT}(\mathbf{u}) \triangleright \hat{z} \in \mathbb{R}_p$ 
4:  $w := v - \text{NTT}^{-1}(\hat{z}) \triangleright w \in \mathbb{R}_p$ 
5:  $m := \text{Compress}_{p, 1}^d(w) \triangleright m$  is a 256-bit string
```

that no masking is required⁹, blue that masking is required and has linear complexity with d (when implemented with arithmetic masking), and red that masking with quadratic complexity is required, which means that bitsliced boolean masking may be beneficial.

`Kyber.CPAPKE` manipulates polynomial ring $\mathbb{Z}_p[X]/(X^n + 1)$ that we denote as \mathbb{R}_p . Vectors of size l of polynomials are next denoted with bold such that $\mathbf{x} \in \mathbb{R}_p^l$. `Kyber` makes also use of NTT representation that we denote $\hat{x} := \text{NTT}(x)$. The first step (Line 1-2) in decryption is to map the ciphertext c into the corresponding (vector of) polynomial(s). Then, the secret key \hat{s} is multiplied with \mathbf{u} and subtracted to v (Line 3-4). Concretely, these operations (addition, multiplications and NTT) are performed with arithmetic masking and can be applied share-by-share, hence with a linear complexity. Finally, each coefficient (in \mathbb{Z}_p) of the resulting polynomial is compressed to a single bit, which represents the rounding to $\lceil p/2 \rceil$ or 0. We detail the masked implementation of $\text{Compress}_{p,c}^d$ in [Algorithm 14](#).

Finally, `Kyber.CPAPKE.Enc` is described in [Algorithm 13](#). This algorithm starts by generating $2l + 1$ noise polynomials (Line 2-4) whose coefficients follow a central binomial distribution (CBD, see [Algorithm 16](#)) with parameter η , such that they belong to $\llbracket -\eta, \eta \rrbracket$. The CBD takes as input a pseudo-random string of bits which is computed as the hash PRF of the random seed σ and a nonce. Next, the noise (e_1 and e_2) is added to the product of the public key and the vector of noise polynomials \mathbf{r} (Line 5-7). The message m is decompressed to a polynomial with $\text{Decompress}_{q,1}^d$ (see [Algorithm 15](#)) and added to the sum. The last step is to compress (i.e., rounding then divide) both \mathbf{u} to d_u bits and v to d_v bits, which gives the ciphertext (Line 8-9).

Algorithm 13 `Kyber.CPAPKE.Enc`(pk, m, σ)

Input: $pk = (\hat{t}, \hat{A})$ with $\hat{t} \in \mathbb{R}_p^l$, $\hat{A} \in \mathbb{R}_p^{l \times l}$; message $m \in \{0, 1\}^n$, randomness $\sigma \in \{0, 1\}^{256}$.

Output: Ciphertext $c = (c_u, c_v)$.

```
1: for  $i = 0$  to  $l - 1$  do  $\triangleright$  Noise sampling
2:    $\mathbf{r}[i] := \text{CBD}_{\eta_1}^d(\text{PRF}^d(\sigma, i)) \triangleright \mathbf{r} \in \mathbb{R}_p^l, \eta_1 = 2$ 
3:    $\mathbf{e}_1[i] := \text{CBD}_{\eta_2}^d(\text{PRF}^d(\sigma, i + l)) \triangleright \mathbf{e}_1 \in \mathbb{R}_p^l, \eta_2 = 2$ 
4:  $\mathbf{e}_2 := \text{CBD}_{\eta_2}^d(\text{PRF}^d(\sigma, 2 \cdot l)) \triangleright \mathbf{e}_2 \in \mathbb{R}_p, \eta_2 = 2$ 
5:  $\hat{\mathbf{r}} := \text{NTT}(\mathbf{r})$ 
6:  $\mathbf{u} := \text{NTT}^{-1}(\hat{A}^T \circ \hat{\mathbf{r}}) + \mathbf{e}_1 \triangleright \mathbf{u} \in \mathbb{R}_p^l$ 
7:  $v := \text{NTT}^{-1}(\hat{t}^T \circ \hat{\mathbf{r}}) + \mathbf{e}_2 + \text{Decompress}_{p,1}^d(m) \triangleright v \in \mathbb{R}_p$ 
8:  $c_u := \text{Compress}_{p, d_u}^d(\mathbf{u}) \triangleright d_u = 10$ 
9:  $c_v := \text{Compress}_{p, d_v}^d(v) \triangleright d_v = 4$ 
```

⁹We focus on long-term security of the `Kyber` private key, and assume that the exchanged key K can be leaked to a side-channel adversary. Otherwise, the derivation of K should also be protected.

Algorithm 14 $\text{Compress}_{q,c}^d$, from [CGMZ21a]

Input: d shares arithmetic sharing \mathbf{x}^{A^p} such that $p < 2^k$ and $x \in \llbracket 0, p \rrbracket$. Compression factor $c \in \llbracket 1, k \rrbracket$.
Output: d shares Boolean sharing $\mathbf{z}^{B,c}$ such that $z = \lfloor (2^c/p) \cdot x \rfloor \bmod 2^c$.

- 1: $\alpha \leftarrow \lceil \log_2(p \cdot d) \rceil$
 - 2: $\mathbf{y}_{d-1}^{A_{2^{c+\alpha}}} \leftarrow \lfloor (\mathbf{x}_{d-1}^{A^p} \cdot 2^{c+\alpha+1} + p) / (2p) \rfloor + 2^{\alpha-1} \bmod 2^{c+\alpha}$
 - 3: **for** $i = 0$ to $d - 2$ **do**
 - 4: $\mathbf{y}_i^{A_{2^{c+\alpha}}} \leftarrow \lfloor (\mathbf{x}_i^{A^p} \cdot 2^{c+\alpha+1} + p) / (2p) \rfloor \bmod 2^{c+\alpha}$
 - 5: $\mathbf{z}^{B,c+\alpha} \leftarrow \text{SecA2B}_{c+\alpha}^d(\mathbf{y}^{A_{2^{c+\alpha}}}) \quad \triangleright$ Algorithm 8
 - 6: $\mathbf{z}^{B,c} \leftarrow \mathbf{z}^{B,c+\alpha} \lll \alpha, \alpha + c \rrr$
-

Algorithm 15 $\text{Decompress}_{q,1}^d$

Input: d shares Boolean sharing $\mathbf{x}^{B,1}$, integer p such that $p < 2^k$ and $x \in \llbracket 0, p \rrbracket$.
Output: d shares arithmetic sharing \mathbf{z}^{A^p} such that $z = x \cdot (p/2) \bmod p$.

- 1: $\mathbf{y}^{A^p} \leftarrow \text{SecB2AModpBit}_k^d(\mathbf{x}^{B,1})$
 - 2: $\mathbf{z}^{A^p} \leftarrow (p/2) \cdot \mathbf{y}^{A^p} \bmod p$
-

5.2 Implementations: K1 and K2

Next, we detail our implementation of Kyber768, whose parameters are $d_u = 10$, $d_v = 4$, $\eta_1 = \eta_2 = 2$, $l = 3$ and $p = 3329$.¹⁰ For each of the algorithms $\text{Compress}_{p,c}^d$, $\text{Decompress}_{p,c}^d$ and CBD_{η}^d , we will describe our new construction together with the previous state-of-the-art solution.

The implementations K1 and K2 based on the one developed by Coron et al. [CGMZ21b, CGMZ21a]. We changed the NTT and arithmetic operations with the optimized version for Cortex-M4 from [KRSS]. We also keep a single noise polynomial in memory at any time in Algorithm 13 to reduce the stack usage. Implementation K1 relies on the original gadget provided by Coron et al., but we replaced some of them with our implementations of the gadgets of [SPOG19] which perform better. Implementation K2 leverages our new bitsliced gadgets.

Compress $_{p,c}^d$. The **Compress** allows to map an element in \mathbb{Z}_p to $z = \lfloor (2^c/p) \cdot x \rfloor \bmod 2^c$.

We leverage the masked compression algorithm from [CGMZ21a] (Algorithm 14) for the implementation of Compress_k^d in K2. Our Compress_k^d algorithm takes as input an arithmetic sharing \mathbf{x}^{A^p} and transforms it into an arithmetic sharing $\bmod 2^{c+\alpha}$ (where $\alpha = \lceil \log_2(p \cdot d) \rceil$) using sharewise operations. The result is then converted into a $(c + \alpha)$ -bit boolean sharing with the bitsliced **SecA2B** (Algorithm 8). Finally, the c most significant bits of the boolean sharing are taken as output. After compression, we test the joint equality to the ciphertext of all the compressed polynomial coefficients (c'_u and c'_v) using bitsliced boolean $\oplus^{\mathbb{B}}$ (for individual bit equality testing) then **SecAnd** (to summarize all equality test results in a single bit).

For K1, each of the polynomial comparison are detailed in [CGMZ21a]. More precisely, we consider as reference for their hybrid-method. For the test of c_u , Coron et al. compare (in arithmetic masking) \mathbf{u}' with all the possible candidates \mathbf{u} that could lead to the compression c_u . For the test of c_v , Coron et al. uses Algorithm 14 without bitslicing. Eventually, the $\text{Compress}_{p,1}^d$ in K1 is performed with the table-based conversion from [CGMZ21b].

Decompress $_{p,1}^d$. **Decompress** is mapping a single bit to $\lceil p/2 \rceil$ or 0, and we implement it with Algorithm 15, in which single-bit boolean sharing $\mathbf{x}^{B,1}$ is converted to arithmetic sharing \mathbf{y}^{A^p} with the single-bit dedicated conversion from [SPOG19]. We do not use our

¹⁰Note that the proposed construction also applies to both **Kyber512** (with $l = 2$, $\eta_1 = 3$) and **Kyber1024** (with $l = 4$, $d_u = 11$, $d_v = 5$).

generic SecB2AModp_k^d for this purpose since, as shown in Figure 5, it is slower by a factor 2 for single-bit conversions.

CBD₂^d. The CBD takes as input two random strings a and b of η bits and outputs $\text{HW}(a) - \text{HW}(b) \bmod p$. For K1, we use the implementation from [SPOG19] which computes $\text{HW}(a) - \text{HW}(b) + \eta$ in boolean masking (using their SecAdd_k^d), then converts it to arithmetic masking using Algorithm 4 and their SecB2AModpBit_k^d , and finally subtracts η . For K2, we use Algorithm 16, which is close to the gadget of [SPOG19], but uses an optimal full adder composition for the addition of the bits of a and $-b$, and furthermore uses our new SecFullAdder and SecB2AModp bitslice gadgets. The new CBD_η^d uses $\lfloor 2\eta/2 \rfloor + \lfloor 2\eta/4 \rfloor + \lfloor 2\eta/8 \rfloor + \dots$ full-adders to compute $\text{HW}(a) - \text{HW}(b) + \eta$, which amounts to 3 SecAnd when $\eta = 2$, instead of 8 SecAnd for the implementation of [SPOG19].

Algorithm 16 CBD_η^d New (PINI, by composition)

Input: d shares Boolean sharing $\mathbf{a}^{B,\eta}$ and $\mathbf{b}^{B,\eta}$, integer p such that $p < 2^k$ and $x \in \llbracket 0, p \rrbracket$.

Output: d shares arithmetic sharing $\mathbf{z}^{A,p}$ such that $z = \text{HW}(a) - \text{HW}(b) \bmod p$.

```

1:  $(\mathbf{s}^{B,2\eta}[\llbracket 0, \eta \rrbracket], \mathbf{s}^{B,2\eta}[\llbracket \eta, 2\eta \rrbracket]) \leftarrow (\mathbf{a}^{B,\eta}, -\mathbf{b}^{B,\eta})$  ▷  $\text{HW}(s) = \text{HW}(a) - \text{HW}(b) + \eta$ 
2:  $\ell \leftarrow 2\eta$ 
3:  $k \leftarrow \lceil \log_2(\ell + 1) \rceil$ 
4: for  $i = 0$  to  $k - 1$  do ▷ Iterate from output LSB to MSB.
5:    $\mathbf{x}^{B,1} \leftarrow$  if  $\ell \bmod 2 = 1$  then  $\mathbf{s}^{B,2\eta}[\ell - 1]$  else  $(0, 0, \dots, 0)$ 
6:    $\ell \leftarrow \lfloor \ell/2 \rfloor$ 
7:   for  $j = 0$  to  $\ell - 1$  do ▷ Accumulate all bits of weight  $i$ .
8:      $\mathbf{t}^{B,2} \leftarrow \text{SecFullAdder}^d(\mathbf{s}^{B,2\eta}[2j], \mathbf{s}^{B,2\eta}[2j + 1], \mathbf{x}^{B,1})$  ▷ Algorithm 5
9:      $(\mathbf{x}^{B,1}, \mathbf{s}^{B,2\eta}[j]) \leftarrow (\mathbf{t}^{B,2}[0], \mathbf{t}^{B,2}[1])$  ▷ Sum bit goes to  $\mathbf{x}^{B,1}$  and carry to  $\mathbf{s}^{B,2\eta}[j]$ .
10:   $\mathbf{y}^{B,k}[i] \leftarrow \mathbf{x}^{B,1}$ 
11:  $\mathbf{z}^{A,p} \leftarrow \text{SecB2AModp}_d^p(\mathbf{y}^{B,k})$  ▷ Algorithm 10,  $y = \text{HW}(a) - \text{HW}(b) + \eta$ 
12:  $\mathbf{z}_0^{A,p} \leftarrow \mathbf{z}_0^{A,p} - \eta \bmod p$ 

```

G, H and PRF. All the hash functions used are based on SHA-3 and therefore all use the Keccak-f [1600] permutation. Concretely, we use the masked Keccak-f [1600] provided by Coron et al. and a SNI SecAnd for K1, while we use a PINI SecAnd for K2.

Probing security The Kyber implementation K2 is a composition of PINI gadgets, hence it is PINI itself, and therefore probing secure.

5.3 Kyber performance

We show in Figure 6 the performance of the top-level masked components of the Kyber implementations K1 (based on state of the art gadgets) and K2 (new).

First, we remark that $\text{Compress}_{p,1}^d$ in K2 achieves a speedup of more than 10x over K1, showing that Algorithm 14 (bitsliced) is faster than the table-based approach by Coron et al. For $\text{Compress}_{p,4}^d$, the speedup (about 20x) is exactly the one of our new SecA2B_k^d since both implementations implement the same algorithm and SecA2B is the bottleneck. Next, the speedup for the compressed comparison of c_u and c'_u is smaller. Indeed, Coron et al. have already vastly improved this polynomial comparison in [CGMZ21a], which limits the speedup of K2 to 1.8x. Finally, regarding the CBD (which includes the boolean to arithmetic masking conversion of the noise), the gain in performances is directly dependent on the gain for SecB2AModp_k^d that we discussed in Figure 5, since this gadget is the bottleneck.

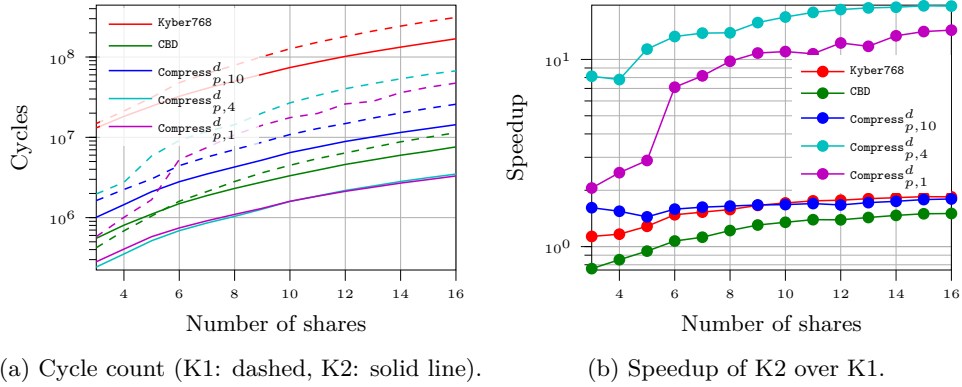


Figure 6: Comparison of the performance of various components of Kyber768: implementations K1 (state of the art gadgets) and K2 (new).

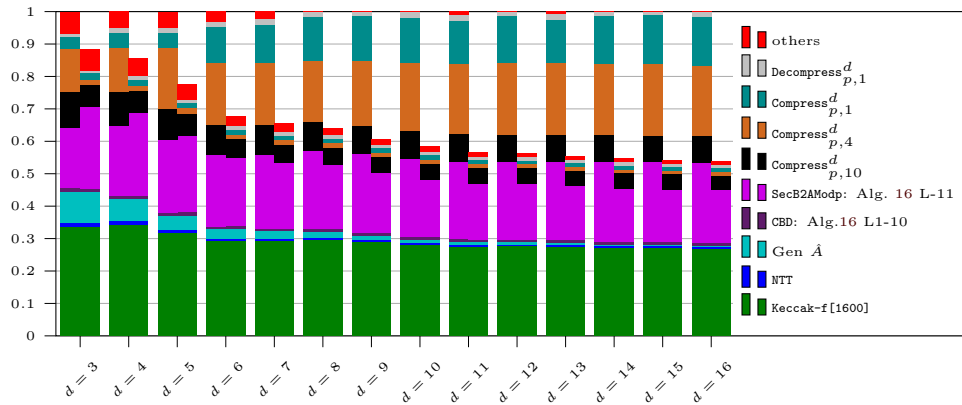


Figure 7: Performance comparison of Kyber768 implementations: K1 (state of the art gadgets, left) and K2 (new, right). Performance is normalized w.r.t. K1. For better performances and small d , users should swap SecB2AModp_k^d conversions.

For number of shares up to 6, the CBD based only on gadgets from [SPOG19] is faster, while for a larger number of shares, the gain is around 1.5.

Overall, our new gadgets lead to a speedup of about 1.8x for the entire Kyber768. As shown in the decomposition of Figure 7, the speedup mostly comes from the improvement on polynomial compressions and comparisons (reduced from 45% to about 10% of the total execution time). This leaves the implementation K2, dominated by the masked Keccak-f[1600] (for 50% of the cycles) whose implementation is already efficiently bitsliced in the state of the art, and by the SecB2AModp_k^d conversion of the noise polynomials (in Algorithm 16) for about 30% of the cycles.

5.4 Saber performance

We implement and benchmark Saber [BBMD⁺19] with the methodology we used for Kyber. Indeed, the structure of Saber is very similar to the one of Kyber, the main difference being the use of a field of characteristic two instead of a prime order field. We developed the implementation S1 starting from the one of Coron et al. [CGMZ21a], adapting and optimizing it for the Cortex-M4, and finally integrating the best state of

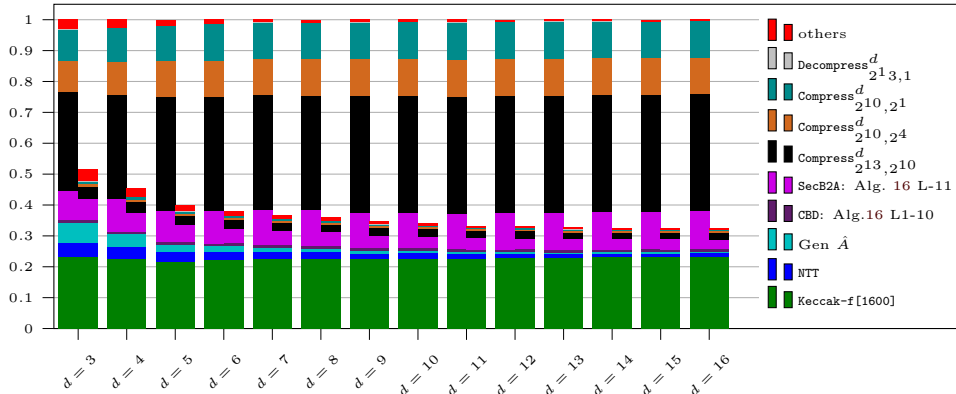


Figure 8: Performance comparison of **Saber** implementations: S1 (state of the art gadgets, left) and S2 (new, right). Performance is normalized w.r.t. S1.

the art gadgets.¹¹ We then developed implementation S2, replacing the **SecA2B**, **SecB2A** and **CBD** implementations by our new bitslice algorithms. Implementation S2 is trivially probing secure thanks to PINI composition.

Overall, implementation S2 achieves a speedup of about 3x over S1 for the entire **Saber** as reported in Figure 8. Concretely, our new gadgets reduces the execution time of the conversions by a large factor such that the fraction of runtime dedicated to them is reduced from 78% down to 20%. In implementation S2 (for $d = 16$), 72% of the execution is spent in masked **Keccak-f[1600]**, 12% in **SecB2A**_k^d and around 10% in **SecA2B**_k^d to perform polynomial compression.

6 Conclusion

We begin our conclusion with the performance improvements. Thanks to very large performance improvement (about 20x) on arithmetic-to-boolean masking conversion gadgets and to various smaller improvements (notably on boolean-to-arithmetic conversions), our **Kyber768** implementation K2 based on new gadgets achieves a speedup of 1.8x over the implementation K1 based on state of the art gadgets (see Figure 7). Similarly, we improve the performance of **Saber** by a factor 3x. The bottleneck of both new implementations of **Kyber** and **Saber** is the computation of masked **Keccak**, meaning that without improvement on the masked hash function, further speedup opportunities are limited.

Next, we remark that in addition to improving performance in software by 1.3x to 25x, our bitsliced gadgets are very amenable to simple and efficient hardware implementations thanks to their bit-level structure, compared to tabled-based gadget or to other non-bitsliced gadgets. Additionally, we expect that the use of PINI as security property will help for security against glitches and transitions [CGLS21, CS21].

Finally, we note that most of the security proofs of this paper are simple: their sole argument is that a gadget is a composition of PINI sub-gadgets. We next discuss the takeaways of the more interesting security proofs. The proofs of Propositions 3 and 4 (arithmetic-to-boolean conversion) rely on the new definition of gadget embedding (Definition 4 and Lemma 1), which can be viewed as an extension of trivial PINI composition to the composition of sub-gadgets with mixed number of shares. Further, the proof of Proposition 5 (**SecB2A**Mod_p) shows that one may securely “unmask” a sharing using only a **RefreshIOS**, instead of the **FullRefresh** which was used in previous works.

¹¹We only replaced the **CBD** with the from [SPOG19].

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A Minimum number of AND gates for a k -bit adder

In the following, we name k -bit adder the boolean function with $2k$ inputs and k coordinates that implements addition modulo 2^k when its inputs and outputs are viewed as k -bit binary representations of integers.

Proposition 6. *A boolean circuit implementing a k -bit adder, When implemented with only 2-input AND, XOR and NOT gates, uses at least $k - 1$ AND gates.*

Proof. We next prove the lower bound of $k - 1$ AND gates for the addition of two k -bit integers. Let B_0 be the set of all linear and affine boolean functions whose inputs are the $2k$ adder input bits, then by induction, c_i be the product of two elements a_i and b_i of B_i , and B_{i+1} be the span (in the vector space of boolean functions) of $B_i \cup \{c_i\}$. We remark that for any (vectorial) boolean function f that can be implemented with i 2-input AND gates and any number of XOR and NOT gates, there exists $(a_j)_{j=0,\dots,i-1}$ and $(b_j)_{j=0,\dots,i-1}$ such that f has all its coordinates in B_i .

Let D_i be the set of all the degrees of the functions in B_i . We have $D_0 = \{0, 1\}$, and for any i , $|D_{i+1}| \leq |D_i| + 1$, thus $|D_i| \leq i + 2$. The induction inequality can be proven as follows: by construction, any function in B_{i+1} can be written as $\alpha_0 c_i \oplus \bigoplus_{j=1}^k \alpha_j f_j$ where all coefficients α belong to \mathbb{F}_2 and all f_j belong to B_i . Since B_i is a vector subspace, there exists $f \in B_i$ such that $f = \bigoplus_{j=1}^k \alpha_j f_j$. Therefore, all elements of $B_{i+1} \setminus B_i$ can be written as $c_i \oplus f$ for some $f \in B_i$. If the degree of c_i (denoted $\deg(c_i)$) does not belong to D_i , then $\deg(c_i \oplus f)$ is either $\deg(c_i)$ or $\deg(f)$, thus $D_{i+1} \subset D_i \cup \{\deg(c_i)\}$ and the inequality follows. Let us now assume that $\deg(c_i) \in D_i$. Let $f, f' \in B_i$ such that $\deg(c_i \oplus f) \neq \deg(c_i \oplus f')$, let $d = \max(\deg(c_i \oplus f), \deg(c_i \oplus f'))$ and assume by contradiction that both degrees do not belong to D_i . Therefore, $\deg(c_i \oplus f) \leq \deg(c_i)$ and the sets of terms in the algebraic normal forms (ANF) of c_i and f whose degree belong to $[\deg(c_i \oplus f), \deg(c_i)]$ are equal. The same goes for f' , and furthermore the sets of terms of degree d of f and f' are distinct. As a result, $\deg(f \oplus f') = d \in D_i$, which contradicts the hypothesis.

Numbering from 0 to $k - 1$ (from least to most significant) the output bits of the adder, the bit i is a function of degree $i + 1$ of the input bits. Therefore, the k -bit adder vectorial boolean function has coordinates of all degrees in $[[1, k]]$. Hence, the adder does not belong to any B_{k-2} : since $0 \in D_{k-2}$, $|D_{k-2} \setminus \{0\}| \leq k - 1 < |[1, k]|$, and therefore $D_{k-2} \not\subset [1, k]$. We conclude that the k -bit adder cannot be implemented with $k - 2$ AND gates (or less). \square

B Generalized IOS refresh gadget

In this Section, we generalize the IOS refresh algorithm of [GPRV21] to deal with any number of shares (instead of only power-of-2). In a nutshell, we take the SNI refresh of [BCPZ16] and apply the same changes as [GPRV21] applied to the power-of-2 special

case, resulting in Algorithm 17. The main difference with [GPRV21] is that the recursive call do not necessarily have the same number of shares, and that the last share is not re-randomized in the final layer when d is odd. For the sake of simplicity and consistency of notations, we specialize the gadget to boolean masking, but the generalization of the gadget and the proofs to linear masking are trivial.

Algorithm 17 RefreshIOS $_k^d$

Input: Boolean sharing $\mathbf{x}^{B,k}$.

Output: Boolean sharing $\mathbf{y}^{B,k}$ such that $x = y$.

```

1: if  $d = 1$  then
2:    $\mathbf{y}^{B,k} \leftarrow \mathbf{x}^{B,k}$ 
3: else if  $d = 2$  then
4:    $r \xleftarrow{\$} \mathbb{F}_2^k$ 
5:    $\mathbf{y}_0^{B,k} \leftarrow \mathbf{x}_0^{B,k} \oplus r$ 
6:    $\mathbf{y}_1^{B,k} \leftarrow \mathbf{x}_1^{B,k} \oplus r$ 
7: else
8:    $\mathbf{z}_{[0, \lfloor d/2 \rfloor]}^{B,k} \leftarrow \text{RefreshIOS}_k^{\lfloor d/2 \rfloor} \left( \mathbf{x}_{[0, \lfloor d/2 \rfloor]}^{B,k} \right)$ 
9:    $\mathbf{z}_{[\lfloor d/2 \rfloor, d]}^{B,k} \leftarrow \text{RefreshIOS}_k^{d - \lfloor d/2 \rfloor} \left( \mathbf{x}_{[\lfloor d/2 \rfloor, d]}^{B,k} \right)$ 
10:  for  $i \in [0, \lfloor d/2 \rfloor]$  do
11:     $r_i \xleftarrow{\$} \mathbb{F}_2^k$ 
12:     $\mathbf{y}_i^{B,k} \leftarrow \mathbf{z}_i^{B,k} \oplus r_i$ 
13:     $\mathbf{y}_{\lfloor d/2 \rfloor + i}^{B,k} \leftarrow \mathbf{z}_{\lfloor d/2 \rfloor + i}^{B,k} \oplus r_i$ 
14:  if  $d \bmod 2 = 1$  then
15:     $\mathbf{y}_{d-1}^{B,k} \leftarrow \mathbf{z}_{d-1}^{B,k}$ 

```

Security proof We now prove that Algorithm 17 is input-output separative for $d \geq 2$. Since the proof is very similar to the original proof of [GPRV21], we only mention the few significant differences. Throughout the proof we denote $L = [0, \lfloor d/2 \rfloor]$ and $H = [\lfloor d/2 \rfloor, d]$. Furthermore, we replace $d/2$ by $\lfloor d/2 \rfloor$ everywhere and adapt the indices (from 0 to $d - 1$ instead of 1 to n).

Uniformity The proof is still by induction, and the base cases are $d = 1$ and $d = 2$. The proof for $d = 2$ is unchanged, while the case $d = 1$ is trivial since there is only one admissible output sharing for a fixed input. Next, for $d \geq 3$, the original induction proof still holds.

IOS The case $d = 1$ is trivial: the full input and output sharings are known if there is at least one probe. The case $d = 2$ is not changed. The induction case only requires changes when d is odd, in order to handle the share $\mathbf{z}_{d-1}^{B,k}$ (wlog we assume that $\mathbf{y}_{d-1}^{B,k}$ is not probed): we define \mathcal{V}_{d-1} as $\{\mathbf{z}_{d-1}^{B,k}\}$ and add $d - 1$ for J if \mathcal{V}_{d-1} is not empty, and in that case the simulator sets $\mathbf{z}_{d-1}^{B,k} = \mathbf{y}_{d-1}^{B,k}$. Simulation then proceeds as in the original proof.

Re-ordering operations The execution of Algorithm 17 can be re-written in the following manner. Let first L_d be well a well-chosen list of pairs (x_i, y_i) (formally, $L_d \in ([0, d]^2)^*$). Then, for each (x_i, y_i) in L_d , generate $r_i \in \mathbb{F}_2^k$ and update the shares with index x_i and y_i by XORing r_i to them. We remark that L_d may be shuffled without impacting the set of

internal variables if we preserve the relative order of any pairs (x_i, y_i) and (x_j, y_j) such that $\{x_i, y_i\} \cap \{x_j, y_j\} \neq \emptyset$. This gives freedom in the implementation to choose the order that minimizes control flow and spilling (i.e., copies from registers to the RAM) overheads.

PINI Finally, we prove that *Algorithm 17* is PINI.

Proposition 7. *Algorithm 17 is PINI.*

Proof. *Algorithm 17* it can be partitioned in one randomness generation circuit G , and d sub-circuits that each take as input randomness (produced by G) and one input share of **RefreshIOS**, and output the corresponding output share. The simulator can simply simulate the randomness generation circuit, as well as any sub-circuit in which there is a probe (or for which the output must be simulated). \square