An efficient verifiable state for zk-EVM and beyond from the Anemoi hash function

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Abstract

In our survey of the various zk-EVM constructions, it becomes apparent that verifiable storage of the EVM state starts to be one of the dominating costs. This is not surprising because a big differentiator of EVM from UTXO is exactly the ability to carry states and, most importantly, their transitions; i.e., EVM is a state machine.

In other words, to build an efficient zk-EVM, one must first build an efficient verifiable state. The common approach, which has been used in production, is a Merkle forest to authenticate the memory that would be randomly accessed within zk-SNARK, and optimize the verification of such memory accesses.

In this note, we describe a way to instantiate a Merkle tree with very few gates in TurboPlonk. We use customized gates in TurboPlonk to implement a SNARK-friendly hash function called Anemoi and its Jive mode of operation [Bou+22], by Clémence Bouvier, Pierre Briaud, Pyrros Chaidos, Léo Perrin, Robin Salen, Vesselin Velichkov, and Danny Willems.

We demonstrate that with 14 gates (≈ 1 gate per round in a 12-round Amenoi hash), one can verify a 3-to-1 compression in a 3-ary Merkle tree. Before this, prior implementations would often require hundreds of gates. We anticipate this technique to benefit a large number of applications built off zk-SNARK.

Our implementation can be found in noah, a library for modern privacy tokens.

https://github.com/FindoraNetwork/noah
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1 Introduction

Verifiable accesses to persistent storage (referred to as “state” in the rest of the note) have been a recurring topic in the research of zero-knowledge proofs for many years. The history of finding an efficient instantiation can be summarized as follows.

- **SNARK with a quasilinear prover** is a critical enabler of the original Zerocash paper [Ben+14], which uses SHA256 to build a Merkle tree for a verifiable state. In the paper, for each layer of the binary Merkle tree, it takes $2^{8161}$ R1CS constraints.
- **Ajtai hash**, or the subset sum hash, is used in the proof-carrying data via cycles of curves paper [BCTV14]. This hash takes bits as inputs. As a result, in many situations, a bit-decomposition of the field elements needs to be performed. The parameter choice of Ajtai hash is a complicated discussion. Daira Hopwood has estimated this cost [Dai], and suggests a heuristic that Ajtai hash often takes 7% of the time for SHA256, which means about $1970$ R1CS constraints. However, this means that the Ajtai hash is not more efficient than Pedersen hash described below. Given that the security of the Ajtai hash is less well-understood, Zcash did not use the Ajtai hash.
- **Pedersen hash** over a suitable embedded curve is used in production first by Zcash Foundation. A twisted Edwards curve, called Jubjub [Jub], was identified to be an embedded curve of BLS12-381, which the Zcash Foundation builds the Sapling proof system over. The advantage of using a twisted Edwards curve is that it has a simple formula to validate point additions. In the end, Pedersen hash shows that for each layer, one can do with about $1000$ R1CS constraints.
- **Starting from MiMC** [AGRRT16], we have witnessed a number of algebraic hash functions that are designed to be SNARK-friendly (or originally MPC-friendly). Two such hash functions used in production are Rescue [AABSDS20] and Poseidon [GKRRS21]. It is worth noting that there is complexity-theory evidence that the need for SNARK-friendly cryptographic primitives could be inherent [CL20]. Many prior instantiations have about 300 R1CS constraints and about 150 customized gates for verification of each layer of the Merkle tree.

**Trend: algebraic-hash-proof-system co-design.** There is a trend of research for algebraic hashes to be designed, not just as standalone cryptographic instantiations, but also related to a particular proof system. For example, Reinforced Concrete [GKLRSW21] leverages the lookup arguments that become popular recently in UltraPlonk implementations. Sinsemilla [Sin] in Zcash is another example, which uses lookup arguments to implement a hash function with the same security guarantees as the Pedersen hash.

We also see work in the other direction: building a zero-knowledge proof system that is friendly to a particular algebraic hash. This is the case for most TurboPlonk implementations used in production, with customized gates for Rescue and Poseidon. We consider this note to be another example, as we are making modifications to TurboPlonk to suit Anemoi, without modifying Anemoi.

The future will be a closer collaboration between practitioners and cryptanalysts, i.e., a co-design of algebraic hashes and proof systems, which is similar to the trend of hardware-software co-design in hardware acceleration for ZK and hardware-friendly proof systems.

We now come back to describe our approach.
1.1 Our approach: use the standard TurboPlonk recipe on Anemoi

Our approach is to leverage a recent algebraic hash function—Anemoi [Bou+22]—by Clémence Bouvier, Pierre Briaud, Pyrrros Chaidos, Léo Perrin, Robin Salen, Vesselin Velichkov, and Danny Willem and tweak an existing TurboPlonk implementation\(^1\) to inline Anemoi as part of the proof system. We do not claim novelty for our work of tweaking, as it follows the standard recipe of TurboPlonk, and we expect many practitioners in zk-SNARK can independently come up with the exact same implementation without reading the rest of this note.

We now turn our attention to the main enabler—Anemoi hash, which explores the CCZ equivalence [CCZ98] between a high-degree polynomial, which is used for computation of the hash, and a low-degree polynomial, which is used for verification of the hash. Readers are encouraged to read the original Anemoi paper [Bou+22], as we will only be providing the necessary background.

**Our approach: inline Anemoi in TurboPlonk.** The Anemoi hash follows the classical substitution-permutation network (SPN). It consists of three steps: constant additions, MDS diffusion (here, MDS means “maximum distance separable”), and S-box. We can also understand such a construction as a combination of a linear layer, consisting of constant additions and MDS diffusion, and a nonlinear layer with an S-box. We already know how to inline the linear layer: one only needs to represent the linear combination explicitly in the quotient polynomial. Many TurboPlonk implementations used in the industry have already created customized gates for some of the heavy operations in a SNARK-friendly hash function, such as a power-5 gate that computes \(x \mapsto x^5\).

A round with input \((a, b, c, d)\) is being processed correctly resulting in output \((a'', b'', c'', d'')\) if and only if the following equations are satisfied.

\[
\begin{align*}
(c' - c'')^5 + g \cdot (c')^2 &= a' \\
(d' - d'')^5 + g \cdot (d')^2 &= b' \\
(c' - c'')^5 + g \cdot (c'')^2 + g^{-1} &= a'' \\
(d' - d'')^5 + g \cdot (d'')^2 + g^{-1} &= b''
\end{align*}
\]

where \(g\) is a generator of the field \(\mathbb{F}\), \((a', b', c', d')\) is the result of applying the linear layer to \((a, b, c, d)\), defined as follows:

\[
\begin{align*}
a' &= a + g \cdot b + prk_1[i] \\
b' &= g \cdot a + (g^2 + 1) \cdot b + prk_2[i] \\
c' &= d + g \cdot c + prk_3[i] \\
d' &= g \cdot d + (g^2 + 1) \cdot c + prk_4[i]
\end{align*}
\]

Here, \(prk_1\) to \(prk_4\) are *processed* round key vectors, described later in the notes. For an SPN with \(r\) rounds, there will be four vectors each containing \(r\) processed round key constants, and \(prk_1[i]\) means the first processed round key constant for the \(i\)-th round.

We observe, however, if we use a TurboPlonk with four input wires (i.e., \(w_1, w_2, w_3, w_4\), we can replace \((a, b, c, d)\) and \((a'', b'', c'', d'')\) as the input for the \(j\)-th gate and the \((j + 1)\)-th gate. That is,

\[
\begin{align*}
gate j: & \quad \begin{align*}
w_1[j] &= a \\
w_2[j] &= b \\
w_3[j] &= c \\
w_4[j] &= d
\end{align*} \\
gate j + 1: & \quad \begin{align*}
w_1[j + 1] &= a'' \\
w_2[j + 1] &= b'' \\
w_3[j + 1] &= c'' \\
w_4[j + 1] &= d''
\end{align*}
\]

\(^1\)In this note, we refer to TurboPlonk as a large family of protocols improved upon the textbook Plonk with customized gates but without lookups, rather than a specific proof system.
In TurboPlonk, the input wire vectors are arithmetized as witness polynomials. Let \( \omega \) be the generator of a suitable domain. We have:

\[
\begin{align*}
\text{gate } j: & \quad w_1(\omega_{j-1}) = a \quad w_2(\omega_{j-1}) = b \quad w_3(\omega_{j-1}) = c \quad w_4(\omega_{j-1}) = d \\
\text{gate } j + 1: & \quad w_1(\omega_j) = a' \quad w_2(\omega_j) = b' \quad w_3(\omega_j) = c' \quad w_4(\omega_j) = d'
\end{align*}
\]

which allows us to express the equations as a constraint system between the input wires in the two gates. For example, \((c' - c'')^5 + g \cdot (c')^2 - a' = 0\) can be represented as:

\[
(w_4(\omega_{j-1}) + g \cdot w_3(\omega_{j-1}) + q_{prk3}(\omega_{j-1}) - w_3(\omega_j))^5 \\
+ g \cdot (w_4(\omega_{j-1}) + g \cdot w_3(\omega_{j-1}) + q_{prk3}(\omega_{j-1}))^2 \\
= w_1(\omega_{j-1}) + g \cdot w_2(\omega_{j-1}) + q_{sel}(\omega_{j-1})
\]

This equation, and other equations of this kind, can be easily represented in TurboPlonk. Note that not all the gates in the constraint system need to satisfy this equation: for example, those gates that are not relevant to the Anemoi hash. Therefore, to express such a constraint more precisely, we use an additional selector polynomial \(q_{sel}(X)\), defined as follows. For the \( j \)-th gate, if \( q_{sel}(\omega_{j-1}) = 0 \), then this gate is not related to \((c' - c'')^5 + g \cdot (c')^2 - a' = 0\), and if \( q_{sel}(\omega_{j-1}) \neq 0 \), then this gate must satisfy \((c' - c'')^5 + g \cdot (c')^2 - a' = 0\). Without loss of generality, we can let \( q_{sel}(\omega_{j-1}) = 1 \). This gives the following equation that all the gates can satisfy.

\[
q_{sel}(\omega_{j-1}) \cdot \left( \frac{(w_4(\omega_{j-1}) + g \cdot w_3(\omega_{j-1}) + q_{prk3}(\omega_{j-1}) - w_3(\omega_j))^5}{w_1(\omega_{j-1}) - g \cdot w_2(\omega_{j-1}) - q_{prk3}(\omega_{j-1})} \right) = 0
\]

We can represent this in TurboPlonk by adding a term in the quotient polynomial \( t(X) \). We use the following polynomial in our initial construction discussed in Section 4.

\[
-\alpha^6 Z_H(X) \cdot q_{sel}(X) \cdot \left( \frac{(\tilde{w}_4(X) + g \cdot \tilde{w}_3(X) + q_{prk3}(X) - \tilde{w}_3(X \omega))^5}{-\tilde{w}_1(X) - g \cdot \tilde{w}_2(X) - q_{prk3}(X)} \right)
\]

The prover convinces the verifier by committing the witness polynomials \( \tilde{w}_1(X) \), \( \tilde{w}_2(X) \), \( \tilde{w}_3(X) \), and \( \tilde{w}_4(X) \) and opening the polynomials \( \tilde{w}_1(X) \), \( \tilde{w}_2(X) \), \( \tilde{w}_3(X) \), \( \tilde{w}_4(X) \), \( q_{sel}(X) \), \( q_{prk3}(X) \), and \( q_{prk3}(X) \) at a random point \( \zeta \) as well as opening \( \tilde{w}_3(X) \) at \( \zeta \omega \). The verifier can use such information to check if \((c' - c'')^5 + g \cdot (c')^2 - a' = 0\) is satisfied.

We apply the same approach to the other three equations. In the end, we have four more such terms to the quotient polynomial \( t(X) \) in TurboPlonk. This construction is not without cost: we need to add 12 field elements in \( \mathbb{F} \) in the TurboPlonk proof. With subsequent optimization (described in Section 5.1, Section 5.2, Section 5.3), we can reduce the number of additional field elements to 5.

**Experiment results.** To demonstrate the efficiency gains from using Anemoi hash function, we measure the number of gates, prover time, proof size, and verifier time of (1) a membership proof in a 20-layer 3-ary Merkle tree and (2) an implementation of privacy token transfer following the Zerocash construction, with different number of inputs and outputs (burn-and-mint). We compare

5
the new TurboPlonk design (tailored for the Anemoi hash function) with a prior TurboPlonk design (tailored for the Rescue hash function). We summarize the result as follows, and the detail of our experiments is given in Section 8.

- The prior design uses 3248 gates for a membership proof in a 20-layer 3-ary Merkle tree, while the new design uses 353 gates.
- Using the prior design, the TurboPlonk proof for a 6-in 6-out privacy token transfer takes 11.0 s, while the TurboPlonk proof in the new construction takes 2.99 s. Originally, the TurboPlonk requires an SRS of size 32768 for the KZG polynomial commitment [KZG10], but the new design is able to reduce it to 8192.

This shows that the new design reduces the number of gates needed for a Merkle tree membership proof by about 8.5×. As a result, it reduces the indexer time, re-indexer time,\(^2\) prover time, and in some situations the required SRS length in TurboPlonk.

The experiment result is encouraging as it shows that using the Anemoi hash function is able to reduce the cost of verifying a membership proof of a Merkle tree in zk-SNARK, and can be used in many applications, which we will discuss in Section 1.2 and Section 1.3.

**Trend: proof systems with a complex lexicon and concise witnesses.** Our work in this note is consistent with a trend in modern proof systems to explore the sparsity in a zero-knowledge proof system, in which we prefer to put more logic in the proof system (i.e., having a complex lexicon) in order to reduce the size of the witness representation (i.e., having concise witnesses), which in practice often reduces the prover cost.

For example, in TurboPlonk, one can enable customized gates and represent the same program in zk-SNARK with fewer gates. When the number of gates is lower, the TurboPlonk proof system can work on shorter polynomials, and sometimes polynomials with a lower density (or weight).

We expect a more systematic study on the sparsity theory to appear soon as the different varieties of open-sourced industry implementations of TurboPlonk are sufficient for us to do experiments and collect empirical evidence. Through such a study, we can then have a better understanding and eventually define the lexicon and conciseness here regarding a proof system.

**Mic drop.** We have the feeling that the endgame of the algebraic hash may be coming soon, as it does not seem to be easy to further reduce the number of gates per invocation. This has something to do with the security analysis of the several design strategies for algebraic hash—the number of rounds in a substitution-permutation network is unlikely to be small. An informal example to help readers understand the issue is that, if the algebraic hash can be expressed with only a few polynomial identity relations in the TurboPlonk, it seems to suggest the possibility of being vulnerable to Gröbner basis attacks. Nevertheless, the authors are not experts in this area, and we would like to see this discussion above to be false.

Another reason for the endgame is that the algebraic hash that can be instantiated with very few gates in TurboPlonk is already good enough for many ZK applications. To explain why, we evaluate a privacy token transfer protocol based on Zerocash. It shows that the portion of the prover

\(^2\)We define re-indexing as the process of running the indexer without recomputing the polynomial commitments of the selectors. Those polynomial commitments may have been computed and stored somewhere.
overhead due to the Merkle tree has dropped significantly. This suggests that the use of a Merkle tree will unlikely continue being the performance bottleneck in many applications as time goes on. Therefore, the priorities today are to optimize other building blocks in various ZK applications, including zk-EVM, zk-DID, and zk-BatchVerify.

**The semifinal with table lookup.** Recently we have seen a lot of work in table lookup, including Caulk [ZBKMNS22], Caulk+ [PK22], and flookup [GK22]. Compared with the Merkle tree, table lookup requires a very long SRS and has a concretely large table construction time, but it is known to be concretely more efficient than a construction that uses a Merkle tree in zk-SNARK to perform a lookup. The original Caulk [ZBKMNS22] paper estimates that Caulk is $100 \times$ faster than proving the Merkle tree in Groth16 [Gro16] using the Poseidon hash function [GKRRS21]—this is impressive because Groth16 requires circuit-specific setup, while Caulk only requires universal (and updatable) setup.

We feel that it is time to revisit the Merkle tree, as it has various benefits that are important for real-world deployment. Table lookup’s requirement of a very long SRS is hard to meet through a setup ceremony, and the table construction also takes a significant amount of time, while the Merkle tree has sublinear overhead and is easily updatable. Our preliminary estimations show that with tree-top caching, the gap between Merkle tree (with TurboPlonk in this note and Anemoi) and Caulk in terms of proving time is narrowed down to $1.5 \times$ to $2 \times$, instead of $100 \times$. We expect future improvements in these two directions, SNARK-friendly hash functions in proof systems and table lookup, will be an exciting competition.

### 1.2 Applications on scalability: zk-Rollup, zk-EVM, and zk-BatchVerify

An efficient SNARK-friendly hash implementation benefits applications that seek for improving blockchain efficiency: zk-Rollup, zk-EVM, and zk-BatchVerify.

**zk-Rollup: a succinct proof for a batch of transactions.** We start with the simple example of rolling up a number of coin transfer operations, where a user in layer 2 can transfer coins to another user. This is often done by maintaining and updating a Merkle tree of all the accounts and their associated balances, and a (zero-knowledge) proof shows that all operations on the Merkle tree are legitimate, in that coins are correctly transmitted between the users. This proof is then provided to a smart contract in layer 1 which keeps track of the root hash of the Merkle tree and only approves updates to the root hash when a valid proof is present. This smart contract on layer 1, additionally, facilitates the transfer of tokens between layer 1 and layer 2.

By making the proving cost for such a membership proof in the Merkle tree cheaper, we are able to reduce the overhead to maintain and update the account balances. This improvement makes it easier for layer 2 to support more users and more types of tokens.

**zk-EVM: a succinct proof for a virtual random access machine.** As an extension to traditional zk-Rollup, in zk-EVM layer 2 rolls up not only simple transactions, but also smart contract executions. Many important EVM applications are memory-heavy, such as Uniswap, ERC-20, and ERC-721, as their logic is generally simple, but they need to work with an ever-growing state. Moreover, the access to this state is often “random access”, in which the EVM may access a few
Layer 2

Verifying key hash | Input hash | Proof | Result
--- | --- | --- | ---
0xdb8f07...77b4 | 0x4f47...30 | 0x3d...b9 | ✓
0x393ff...9c | 0x6ddd...db | 0x30...ec | ✓
... | ... | ... | ...
0xc96e21...f29b | 0xc751...da | 0xdb8...3f | ✓

Layer 1

Merkle tree root hash

data shared with layer 1

data staying on
layer 2

an aggregated proof

contract call (verifying key hash, input hash)

other L1 contracts

external contract call

verifier oracle contract

Merkle tree of valid verifying key-input pairs

Figure 1: System architecture of zk-BatchVerify.

locations in a large memory, and the locations, which depend on the current state and the contract call, cannot be easily predicted without knowing the information in the contract call. That is, a more pressing and inherent challenge for zk-EVM may be about the “random access” part, which has not received enough attention.

For a given smart contract, one can build an application-specific and SNARK-friendly state representation through a Merkle forest, which consists of many Merkle trees. The Merkle trees can be instantiated with a SNARK-friendly collision-resistant hash (CRH) function. This note shows that with the Anemoi hash function as well as a TurboPlonk variant that is tailored for the Anemoi hash function, one can instantiate such a Merkle forest efficiently.

zk-BatchVerify: a substitute for layer-1 on-chain proof verification. We anticipate more layer-1 applications to use zero-knowledge proofs. In more detail, the smart contracts of these layer-1 applications may receive a proof (if privacy is needed, this proof can be zero-knowledge) from the user through the contract call and then run the verifier on this proof. One concern with this design is that proof verification on layer 1 is expensive. According to Matter Labs [Zks], the verification cost of a Groth16 proof can be 300 k, the verification cost of a TurboPlonk proof can be 600 k, and the verification of a STARK proof can be 5000 k. Though the transaction fee in USD is changing, at the time of writing it is at least $50, which is already expensive for many dApps. One solution to solve this problem, we call it zk-BatchVerify, is to verify the proofs on layer 2 and return the verification results to layer 1, as shown in Figure 1. Since this is a new application, we provide a brief description as follows.

- **Layer 2:** The sequencer on layer 2 maintains a table as shown in Figure 1. Each row consists
of the verifying key hash, the input hash, the proof, and the verification result. If the verification result is true, it means that given a verification key and an input with the corresponding hash values as well as a proof as input, a verifier will accept this proof. Otherwise, the result will be false. Note that layer 2 does not need to be restricted to a particular proof system—it can support different variants of TurboPlonk, Groth16, and STARK, and it can be curve-agnostic—it can support BLS12-381, BN254, and other application-specific curves. A SNARK-friendly hash function can be used to hash the verifying key and the input, while the L1 dApp contracts store the verifying key hash as a hard-coded parameter.

- **Merkle tree**: Layer 2 then arranges the list of verifying key hashes and the input hashes of those rows that pass the verification on layer 2 to a Merkle tree. There may be more than one hash functions: one SNARK-friendly, one EVM-friendly, and to reduce the overhead of opening this Merkle tree in EVM, one may use the cap hash optimization [Wu21]. The root hash (or the cap hash) is then passed to the verifier oracle contract on layer 1.

- **Aggregated proof**: A zk-Rollup, or an optimistic zk-Rollup, will be used to convince the layer-1 verifier oracle contract that the Merkle tree consists of only valid verifying key-input hash pairs. The use of SNARK-friendly hash function in the Merkle tree may help with proof generation.

- **Verifier oracle contract**: The verifier oracle contract is a smart contract on layer 1, and it supports two operations: (1) it can accept the Merkle root hash (or cap hash) and the corresponding aggregated proof from the sequencer and update its content once the proof is validated and (2) it can respond to contract calls from other L1 contracts asking if a particular pair of verifying key and input (given as hashes) is included in the Merkle tree, given a Merkle tree membership proof. It is useful that the verifier oracle contract can make the Merkle tree root hash (or cap hash) private, so it can enforce that other L1 contracts must make an explicit contract call, and the verifier oracle contract can charge a fee in this process. This provides a revenue model for zk-BatchVerify in addition to charging for each proof verification on layer 2.

- **Other L1 contracts**: Other contracts on layer 1 are clients of zk-BatchVerify. They have the hash of the verifying key hardcoded (or, if needed, computed on the fly). To see if a particular input should be accepted, it computes the hash of the input (probably through a different hash function), and makes a contract call to the verifier oracle contract.

One can see that the SNARK-friendly hash function is being used in various places in the system of zk-BatchVerify. Improvement on the hash function can reduce the overhead of the construction of the Merkle tree as well as the hashing of verifying keys and inputs. For proof systems that use a SNARK-friendly hash function as the Fiat-Shamir sponge, an efficient SNARK-friendly hash function also helps with the cost.

### 1.3 Applications on security: Zerocash, zk-DID, and zk-Bridge

An efficient SNARK-friendly hash implementation also benefits various applications that offer security: Zerocash, zk-DID, and zk-Bridge. It is important to realize that efficiency is a crucial limiting factor for the adoption of these applications.

**Zerocash.** In order to create a privacy token transfer transaction in Zerocash, a user may need to download a structured reference string (SRS, often at the scale of 1 GB for Groth16) and run a
prover on the user’s device. In many dApps, the prover will be running in WebAssembly (which is the case of a browser prover), the use of WebAssembly leads to a performance penalty of $10 \times$, according to an experiment by the Manta Network [Man].

**zk-DID.** Privacy ensures that a user only discloses minimal information about the DID necessary for the dApp. For example, a user may need to convince a smart contract that the user meets the age requirement, without revealing the age as well as other identity information. This can be done using zk-SNARK. However, the same issue arises that the user needs to generate the proof on the user’s device. It is difficult for the user to download a long SRS as well as run an expensive prover. With a SNARK-friendly hash implementation, the SRS can be shorter, and the prover is lighter, which helps the user to generate the zero-knowledge proof used in zk-DID.

**zk-Bridge (or more generally, a redundancy bridge).** There have been a number of incidents of cross-chain bridges being compromised, and hackers have been able to steal a large number of coins. There are many distinct reasons why a cross-chain bridge was compromised, and a universal solution that can prevent all these attacks and is trustless would be very unlikely to exist. For this reason, a trend in the industry is a new concept called the redundancy bridge, in which we rely on more than one mechanism to secure the bridge, even if these mechanisms have some overlapping. This is closely related to the concept of *N-version programming* mentioned in the original practical Byzantine fault tolerance (PBFT) paper [CL99; PBCWC96]. One of the tools that can be used in building a redundancy bridge is zero-knowledge proofs [Xie+22], in the following ways:

- **Proof of source chain consensus:** Traditional cross-chain bridge uses a small committee to vote on the latest block header, and if this small committee is compromised, an attacker can cause the destination chain smart contract to accept a forged block header, which may enable the attacker to withdraw tokens that do not belong to the attacker. To make it harder for an attacker to perform such an attack, as shown in Figure 2, we have the destination chain smart contract to require a proof of source chain consensus, which works as follows.
  - If the source chain uses proof-of-work (PoW): the proof of source chain consensus proves that the new block is extended from the previous block, and it has an expected amount of proven work based on the current difficulty. The zero-knowledge proof will be mostly checking an evaluation of SHA256 or Equihash.
  - If the source chain uses proof-of-stake (PoS): the proof of source chain consensus proves that the validators have endorsed the new block header with their signatures. In this case, the attacker needs to compromise the validators of the source chain in order to convince the target chain smart contract about a forged new block header. The zero-knowledge proof will be verifying ed25519 (or others) ECDSA/EdDSA signatures.

- **Proof of expected contract executions:** The use of zero-knowledge proofs is not limited in verifying the source chain consensus (i.e., passing the source chain block header). It can naturally be extended to verify the other operations in a cross-chain bridge, such as the withdrawal process. The zero-knowledge proof shows that the execution plan that is calculated by an independent and separate implementation is valid, and this execution plan will be compared with the execution plan created in the existing cross-chain bridge contract.

The redundancy bridge wants to ensure that an attacker needs not only to pass the existing security
mechanisms but also to pass the checks based on zero-knowledge proofs. Only when both of them agree with each other, the bridge will perform the withdrawal. This can be implemented through a shell mechanism [Can01], described as follows.

Figure 2: Comparison between an existing cross-chain bridge and a redundancy bridge.

The shell mechanism by Ran Canetti for universal composability (UC) security is a useful model to describe the redundancy design shown in Figure 2. The contract of the redundancy bridge will be separated into two parts: a body and a shell. The body has restricted access to the blockchain—it can only propose an execution plan to the shell instead of interacting with the blockchain directly. If the shell agrees with the execution plan, the shell executes this plan. The shell is designed to be minimalistic, so it can be treated as a trusted computing base (TCB). In the redundancy bridge, the shell obtains one execution plan from the existing bridge logic, and another execution plan from an independent implementation with a zero-knowledge proof of its correctness (as well as the source chain consensus). Only when these two plans are equal, the shell will perform the operations.

The use of a SNARK-friendly hash function benefits the redundancy bridge. For the zero-knowledge proof to verify the consensus or the withdrawal, it may require access to a persistent state, which can be realized through a Merkle tree built off a SNARK-friendly hash function.

1.4 Rest of the note

The rest of the note is organized as follows.

• Section 2 provides some necessary background on TurboPlonk as well as the components of the Anemoi hash function.
• Section 3 provides our starting point, a TurboPlonk implementation used in production, which does not have customized gates for the Anemoi hash function.
• Section 4 presents a version of the TurboPlonk for the Anemoi hash function, but without any optimization. This is to help readers understand how the customized gates work without involving the tricky and confusing detail about the TurboPlonk optimization.
• Section 5 discusses how to apply standard optimization techniques of TurboPlonk to the straw-
man construction. We believe that this section will be instructive, in that such techniques are
only implicit in the original Plonk paper [GWC19].
• Section 6 puts it all together and provides the detailed construction of the indexer, the prover,
and the verifier of the final TurboPlonk construction.
• Section 7 describes a design and implementation of privacy token transfer based on the Zerocash
construction, which uses a Merkle tree built off the Anemoi hash function.
• Section 8 provides the performance results from our experiments of the Anemoi hash function
and the Rescue hash function, each running over a tailored TurboPlonk implementation.
• Section 9 lists the reference materials that we found useful when we were working on this note.
2 Background

In this section we provide the necessary background of TurboPlonk, Flystel S-box, Anemoi permutation, Jive mode of operation, and Anemoi variable-length hash.

2.1 TurboPlonk

TurboPlonk is Plonk [GWC19] with customized gates. There are many different ways to construct customized gates, and as a result, TurboPlonk does not mean a specific construction, but a family of proof systems based on Plonk. Readers are encouraged to read the original Plonk paper [GWC19], as we will be keeping it high-level here.

Circuit representation. TurboPlonk expresses the statement to be proven in zero knowledge as a circuit. The circuit consists of gates, and all the gates have the same number of wires. Usually, there is one output wire, and the rest are input wires. Here are two examples:

- the textbook TurboPlonk [GWC19] has 2 input wires and 1 output wire, in total 3 wires
- the baseline presented in Section 3 has 4 input wires and 1 output wire, in total 5 wires

Note that the distinction between “input” and “output” wires is indeed artificial, and we expect that it will be deprecated soon, as TurboPlonk does not distinguish them. Customized gates can use these wires in an arbitrary way.

TurboPlonk enforces that these gates in the circuit satisfy certain conditions, which consist of a gate check and a copy check, as follows.

- **Gate check:** Each gate in the circuit has some parameters that are independent of the values on the wires, which uniquely define the gate predicate \( G_i(w_1, w_2, w_3, w_4, w_o) \in \{0, 1\} \) for each gate \( G_i \), as shown in Figure 3, and here, \( w_1, w_2, w_3, w_4, w_o \) are the values on the wires. We call these parameters as “selectors”. Different gates can have very different selectors. We say that the gate check passes if for every gate \( G_i \) in the circuit, it holds that \( G_i(w_1, w_2, w_3, w_4, w_o) = 0 \). A reader may wonder why the equation is written in this way rather than \( G'_i(w_1, w_2, w_3, w_4) = w_o \), which...
is naturally closer to what a “gate” sounds like. We want to point out that the former definition is
indeed more general, as it captures the case when there is more than one possible value for \( w_o \). It
also captures the case when checking if a specific value of \( w_o \) makes \( G_1(w_1, w_2, w_3, w_4, w_o) = 0 \)
is easy, but finding this particular value is expensive (and probably computationally hard).

- **Copy check:** The statement we are proving in TurboPlonk is unlikely going to be described with
  a single gate because one gate can only access a limited number of values. It requires a large
  number of gates. To combine these gates in order to represent the statement that we are proving,
  we need to connect the wires in these gates. In Figure 3 we use colored lines to represent such
  connections. There is a lot of flexibility in connecting the wires. It can connect an output wire to
  an input wire in the same gate or in a different gate. It can connect two input wires in the same
gate or in a different gate. It can connect more than two wires. We call such connections “copy”
as the values in the connected wires should be exactly the same copy. This allows TurboPlonk
to use multiple gates to represent a more complicated statement.

By using the gate check and the copy check, we can represent the statement that we are proving in
the circuit, and the statement can be proven if and only if it passes both checks. Now, we describe
**arithmetization**, i.e., how to represent the circuit and express both checks mathematically.

**Arithmetization.** The arithmetization process expresses the relation as some conditions between
these polynomials. TurboPlonk first interpolates the values on the different kinds of wires into
polynomials. In our case, we have five polynomials, \( w_1(X), w_2(X), w_3(X), w_4(X), \) and \( w_o(X) \).
Then, we represent the gate parameters as “selectors” polynomials. There is a lot of customiz-
bility in this step, and different TurboPlonk implementations can have different ways to define
and use such parameters. In our example in Section 3, we have a few selector polynomials \( q_1(X),
q_2(X), q_3(X), q_4(X), q_m(X), q_c(X), q_ecc(X), \) and \( q_o(X) \). Each selector polynomial is
interpolated from one kind of the gate parameters in each of the gates, and it allows us to express
this part of the gate check as the **existence of a polynomial** \( g(X) \) in the following equation.

\[
q_1(X) \cdot w_1(X) + q_2(X) \cdot w_2(X) + q_3(X) \cdot w_3(X) + q_4(X) \cdot w_4(X) \quad \text{// linear combination}
+ q_{m}(X) \cdot w_1(X) \cdot w_2(X) + q_{m2}(X) \cdot w_3(X) \cdot w_4(X) \quad \text{// multiplication (somewhat)}
+ q_c(X) \quad \text{// constants}
+ PI(X) \quad \text{// inputs}
+ q_{ecc}(X) \cdot w_1(X) \cdot w_2(X) \cdot w_3(X) \cdot w_4(X) \cdot w_o(X) \quad \text{// operations on twisted Edwards curves}
- q_o(X) \cdot w_o(X) \quad \text{// output}
= g(X) \cdot Z_H(X)
\]

where PI(\(X) \) is a separate polynomial that defines the input and \( Z_H(X) \) is the vanishing polynomial
that evaluates to zero in the domain \( H \). The existence of \( g(X) \) means that it passes this part of
the gate check. There are other parts of the gate check that we will defer to Section 3.

Now that we have discussed the gate check, we turn our attention to the copy check. In TurboPlonk,
we express the connections between the wires of different gates (for \( N \) gates, there are in total \( 5N \)
wires) as a permutation \( \sigma \). If \( \sigma(i) = j \ (i, j \in \{1, 2, ..., 5N\}) \), it means that the \( i \)-th wire and the
\( j \)-th wire are connected together and therefore they must have the same value. All the connections
can be expressed in this way. Then, in a domain $H$ of size $N$ with generator $\omega$ (which should also be a quadratic residue), we define a polynomial $S(X)$ with the starting point (and the endpoint) as $S(1) = 1$ and the following recursion.

$$S(\omega^i) = S(\omega^{i-1}) \cdot \frac{H(w_1(\omega^{i-1}), \omega^{i-1}) \cdot H(w_2(\omega^{i-1}), k_1 \cdot \omega^{i-1}) \cdot H(w_3(\omega^{i-1}), k_2 \cdot \omega^{i-1})}{H(w_4(\omega^{i-1}), k_3 \cdot \omega^{i-1}) \cdot H(w_5(\omega^{i-1}), k_4 \cdot \omega^{i-1}) \cdot H(w_6(\omega^{i-1}), \sigma(N + i)) \cdot H(w_7(\omega^{i-1}), \sigma(3N + i)) \cdot H(w_8(\omega^{i-1}), \sigma(4N + i))}$$

where $H(x, y) = x + \beta y + \gamma$ with random $\beta$ and $\gamma$ and works as a universal one-way hash function, $k_1$ to $k_4$ are different quadratic non-residue, and $\sigma^*(\cdot)$ is a special function derived from $\sigma$, which outputs the corresponding $k(i) \cdot \omega(i)$ of the other wire that the corresponding wire connects to.

The requirement that $S(1) = 1$ implies that $S(\omega^N) = 1$. If we assume that the hash function here is a black box, then $S(\omega^N)$ means that all those wires that are connected together must have the same value. This concludes the copy check.

**Perform both checks in polynomial IOPs.** The last step is to find a way to convince the verifier that these polynomials satisfy these conditions without sending the verifier the polynomials. There are two reasons why we do not want to send the polynomials. First, sending the polynomials in their entirety leads to a large proof size. Second, for zero knowledge, the prover cannot reveal the entire polynomial. To solve this issue, in polynomial IOPs, instead of sending the polynomials, we send cryptographic commitments of polynomials that can be opened at an arbitrary point. The verifier checks if the polynomials satisfy the conditions by opening the polynomials on both sides of the equations at a random point and checks if the evaluations on this random point satisfy the equations. If so, then by the Demillo-Lipton-Zippel-Schwartz lemma [DL78; Zip79; Sch80], the verifier knows that the polynomials satisfy these conditions unless with a negligible probability (as long as the finite field is sufficiently large). This is the standard recipe for proof systems from polynomial IOPs (interactive oracle proofs) [RRR16; BCS16], and can be considered the raw version of TurboPlonk. The modern construction of TurboPlonk can be viewed as an optimized version of this raw version. This gives us TurboPlonk, a proof system for such circuits.

**“Supergates”: checking multiple equations in one gate.** An important observation in TurboPlonk is that, in the gate check, the gate function can be very general, in that the gate function can check multiple equations over the polynomial rings. For example, in the proof system that we will present in Section 3, the gate function also enforces three equations.

$$q_0(X) \cdot w_2(X) \cdot (w_2(X) - 1) = 0 \quad // \text{boolean testing on the second wire}$$
$$q_0(X) \cdot w_3(X) \cdot (w_3(X) - 1) = 0 \quad // \text{boolean testing on the third wire}$$
$$q_0(X) \cdot w_4(X) \cdot (w_4(X) - 1) = 0 \quad // \text{boolean testing on the fourth wire}$$

where $q_0(X)$ is a selector that determines if the current gate enforces this boolean condition on its second, third, and fourth wires. In addition to the three equations above, the gate can still enforce the previous equation for linear combination, multiplication, constants, inputs, and others, by appropriately setting the gate parameters.
This observation, which goes beyond the original Plonk paper, suggests a large design space for TurboPlonk: one can use multiple equations, and multiple equations can be “activated” in one gate. We now turn our attention to the different building blocks of the Anemoi hash function. Details of our final TurboPlonk proof will be given in Section 6.

2.2 Flystel S-box: rotating an algebraic butterfly

A new cryptographic tool presented in the Anemoi paper [Bou+22] is the Flystel S-box, which is based on the butterfly structure [PUB16; LTYW18; CP17]. In this note, we assume that the S-box is defined on $\mathbb{F}_p$ where $p$ is a large odd prime.

![Diagram of the Flystel S-box](image)

Figure 4: The open and closed butterfly in the Flystel S-box.

The Flystel S-box is a function that takes as input $(x, y)$ and outputs $(x', y')$. Figure 4a shows the workflow of this function, which is equivalent to the following formulas.

\[
x' = x - \beta y^2 + \beta (y - (x - \beta y^2)^{2/\alpha}) + g^{-1} \\
y' = y - (x - \beta y^2)^{1/\alpha}
\]

Here, $g$ is a constant. The problem with the two formulas above is that computing $(\cdot)^{1/\alpha}$ is expensive and it is a polynomial of a very high degree. The Flystel S-box solves this issue by observing that, if we rotate the butterfly in the counterclockwise direction, we can have the closed form of the butterfly, as shown in Figure 4b, which is equivalent to the following formulas.

\[
x = \beta \cdot y^2 + (y - y')^\alpha \\
x' = \beta \cdot (y')^2 + (y - y')^\alpha + g^{-1}
\]

This is, excitingly, a low-degree polynomial (for example, in BLS12-381, $\alpha = 5$). In other words, although it is slightly expensive to compute it, it is easy to verify it, given $(x, y)$ as well as $(x', y')$. We can then use this easy-to-verify S-box in a classical substitution-permutation network, and obtain a fixed permutation that can be used to construct a collision-resistant hash function as well as a sponge, which we now describe.
2.3 Anemoi permutation: a classical substitution–permutation network

The Anemoi permutation [Bou+22] follows the standard recipe substitution-permutation network: the permutation has \( N \) rounds, and each round consists of three steps: (1) constant addition, (2) MDS matrix, and (3) S-box. We assume the input to the Anemoi permutation is of size \( 2^\ell \). The three steps can be described as follows.

- **Constant addition:** For input \((\vec{x}, \vec{y}) \in (F^\ell, F^\ell)\), the \( r \)-th round has some round-specific constants \( \vec{c}_r \in F^\ell \) and \( \vec{d}_r \in F^\ell \). It outputs \((\vec{x} + \vec{c}_r, \vec{y} + \vec{d}_r)\).

- **MDS matrix:** The MDS matrix in the Anemoi permutation is a fixed matrix \( M \) of size \( F^{\ell \times \ell} \). For input \((\vec{x}, \vec{y}) \in (F^\ell, F^\ell)\), we have \( \vec{u} = M \cdot \vec{x} \) and \( \vec{v} = M \cdot \vec{y}_\omega \) where \( \vec{y}_\omega \) a shifted version of \( \vec{y} \) (in the Anemoi permutation, moving the first element to the end). The Anemoi permutation uses a shift instead of having another MDS matrix for \( \vec{y} \).

- **S-box:** For input \((\vec{x}, \vec{y}) \in (F^\ell, F^\ell)\), and we let the S-box be \( S(x, y) \to (x', y') \), then the output \( \vec{u} \) and \( \vec{v} \) can be computed by letting \( (u[i], v[i]) = S(x[i], y[i]) \) for \( i = 1, ..., \ell \).

The operations listed above will be repeated for \( N \) rounds, followed by an additional MDS matrix step in the end. This gives a permutation with input \( F^{2\ell} \) and output \( F^{2\ell} \). With such a permutation, we can then construct the collision-resistant hash function and a sponge.

2.4 Jive mode of operation: k-to-1 compression for Merkle trees

An important observation in [Bou+22] is that in many zk-SNARK applications we use the SNARK-friendly hash function mostly for Merkle trees (or a Merkle forest). However, when people design a SNARK-friendly hash function, we expect it to be cryptographically secure enough so that it can be modeled as a random oracle. This is, however, an overkill. For a Merkle tree to work, all we need is a collision-resistant hash function.

The Anemoi paper [Bou+22] shows a construction of a collision-resistant hash function \( \text{CRH} \) directly from the permutation \( P(\vec{x}, \vec{y}) \to (\vec{u}, \vec{v}) \), as shown below.

\[
\text{CRH}(\vec{x}, \vec{y}) = P(\vec{x}, \vec{y}) + \sum_{i=1}^{\ell} (x[i] + y[i] + u[i] + v[i])
\]

The cost of the collision-resistant hash function is therefore very close to the cost of the permutation itself, and we use it to instantiate the Merkle tree in zk-SNARK.

2.5 Anemoi variable-length hash: a sponge construction

One can use the Anemoi permutation to construct a sponge. The Anemoi paper [Bou+22] suggests the use of the Hirose variant [Hir18] of the sponge construction. When the field \( F \) is sufficiently large, for a permutation from \( F^{2\ell} \) to \( F^{2\ell} \), we can create a sponge with the rate \((2\ell - 1)\) and capacity 1. A variable-length hash function can be made directly from such a sponge construction. We want to note that the sponge construction is somewhat “naturally immune” to length-extension attacks, so we do not need special treatment about the length.
3 Starting point

Our starting point is a regular TurboPlonk implementation. We can specify a TurboPlonk implementation via the polynomial identity relations and the strategy to open the polynomials. We use the standard copy check, which is the same for the majority of the TurboPlonk constructions. Therefore, for the sake of conciseness, we focus on the gate check here. For any element $x$ in a domain $H$, we have the following in this TurboPlonk system.

\[
q_1(X) \cdot w_1(X) + q_2(X) \cdot w_2(X) + q_3(X) \cdot w_3(X) + q_4(X) \cdot w_4(X) \quad // \text{linear combination}
\]
\[
+ q_{m1}(X) \cdot w_1(X) \cdot w_2(X) + q_{m2}(X) \cdot w_3(X) \cdot w_4(X) \quad // \text{multiplication (somewhat)}
\]
\[
+ q_c(X) \quad // \text{constants}
\]
\[
+ \Pi(X) \quad // \text{inputs}
\]
\[
+ q_{ecc}(X) \cdot w_1(X) \cdot w_2(X) \cdot w_3(X) \cdot w_4(X) \cdot w_o(X) \quad // \text{operations on twisted Edwards curves}
\]
\[
= q_o(X) \cdot w_o(X) \quad // \text{output}
\]
\[
q_b(X) \cdot w_2(X) \cdot (w_2(X) - 1) = 0 \quad // \text{boolean testing on the second wire}
\]
\[
q_b(X) \cdot w_3(X) \cdot (w_3(X) - 1) = 0 \quad // \text{boolean testing on the third wire}
\]
\[
q_b(X) \cdot w_4(X) \cdot (w_4(X) - 1) = 0 \quad // \text{boolean testing on the fourth wire}
\]

In the linearization step, we open some polynomials at some random points, as shown below.

\[
q_1(X) \cdot \boxed{w_1(\zeta)} + q_2(X) \cdot \boxed{w_2(\zeta)} + q_3(X) \cdot \boxed{w_3(\zeta)} + q_4(X) \cdot \boxed{w_4(\zeta)} \quad // \text{linear combination}
\]
\[
+ q_{m1}(X) \cdot \boxed{w_1(\zeta)} \cdot \boxed{w_2(\zeta)} + q_{m2}(X) \cdot \boxed{w_3(\zeta)} \cdot \boxed{w_4(\zeta)} \quad // \text{multiplication (somewhat)}
\]
\[
+ q_c(X) \quad // \text{constants}
\]
\[
+ \Pi(X) \quad // \text{inputs}
\]
\[
+ q_{ecc}(X) \cdot \boxed{w_1(\zeta)} \cdot \boxed{w_2(\zeta)} \cdot \boxed{w_3(\zeta)} \cdot \boxed{w_4(\zeta)} \cdot \boxed{w_o(\zeta)} \quad // \text{operations on twisted Edwards curves}
\]
\[
= q_o(X) \cdot \boxed{w_o(\zeta)} \quad // \text{output}
\]
\[
q_b(X) \cdot \boxed{w_2(\zeta)} \cdot (w_2(\zeta) - 1) = 0 \quad // \text{boolean testing on the second wire}
\]
\[
q_b(X) \cdot \boxed{w_3(\zeta)} \cdot (w_3(\zeta) - 1) = 0 \quad // \text{boolean testing on the third wire}
\]
\[
q_b(X) \cdot \boxed{w_4(\zeta)} \cdot (w_4(\zeta) - 1) = 0 \quad // \text{boolean testing on the fourth wire}
\]

**Binary testing supergates.** The last three equations (for boolean testing) are what we discuss in Section 2 as an example of “supergates”. These three equations enforce that, for those gates whose $q_b(X) \neq 0$, it requires that the 2nd, 3rd, and 4th (input) wires to be *binary*, that is, either 0 or 1.
4 Initial attempt

In the rest of the note we focus specifically on the case where \( \ell = 2 \). We design the customized gates for the Anemoi hash function by representing the equations directly into polynomial identity relations and inlining the linear layer (constant addition and MDS matrix) into these relations. Let us consider the relationship between the state after S-box in a round \((x[1], x[2], y[1], y[2])\) and the state after S-box in the following round \((x'[1], x'[2], y'[1], y'[2])\).

**Apply the constant addition.** Let the \( r \)-th round addition constants be \((c_r[1], c_r[2], d_r[1], d_r[2])\).

Then, after applying the constant addition, we have:

\[
\begin{pmatrix}
x[1] \\
x[2] \\
y[1] \\
y[2]
\end{pmatrix}
\Rightarrow
\begin{pmatrix}
x[1] + c_r[1] \\
\end{pmatrix}
\]

**Apply the MDS matrix.** The next is to apply the MDS matrix. The Anemoi paper \(\text{[Bou+22]}\) suggests that one can simply use the following matrix for \( \ell = 2 \) where \( g \) is a generator.

\[
\begin{bmatrix}
1 & g \\
g & g^2 + 1
\end{bmatrix}
\]

After applying the MDS matrix as described in the Anemoi permutation, we have:

\[
\begin{pmatrix}
x[1] + c_r[1] \\
\end{pmatrix}
\Rightarrow
\begin{pmatrix}
x[1] + g \cdot x[2] + (c_r[1] + g \cdot c_r[2]) \\
g \cdot x[1] + (g^2 + 1) \cdot x[2] + (g \cdot c_r[1] + (g^2 + 1) \cdot c_r[2]) \\
y[2] + g \cdot y[1] + (d_r[2] + g \cdot d_r[1]) \\
g \cdot y[2] + (g^2 + 1) \cdot y[1] + (g \cdot d_r[2] + (g^2 + 1) \cdot d_r[1])
\end{pmatrix}
\]

Note that \( \vec{y} \) needs to be shifted before applying the MDS matrix, as described in Section 2.

**Processed round keys.** Now, for convenience, we define “processed round keys” (PRK), which are the parts highlighted above, and they are round-specific.

\[
\begin{align*}
\text{prk}_{1,r} &:= c_r[1] + g \cdot c_r[2] \\
\text{prk}_{2,r} &:= g \cdot c_r[1] + (g^2 + 1) \cdot c_r[2] \\
\text{prk}_{3,r} &:= d_r[2] + g \cdot d_r[1] \\
\text{prk}_{4,r} &:= g \cdot d_r[2] + (g^2 + 1) \cdot d_r[1]
\end{align*}
\]

And therefore, we know that the input to the S-box is as follows:

\[
\begin{pmatrix}
x[1] + g \cdot x[2] + \text{prk}_{1,r} \\
g \cdot x[1] + (g^2 + 1) \cdot x[2] + \text{prk}_{2,r} \\
y[2] + g \cdot y[1] + \text{prk}_{3,r} \\
g \cdot y[2] + (g^2 + 1) \cdot y[1] + \text{prk}_{4,r}
\end{pmatrix}
\]
And the output should be, by definitions:

\[
\begin{pmatrix}
x'[1] \\
x'[2] \\
y'[1] \\
y'[2]
\end{pmatrix}
\]

This gives us four equations based on the closed Flystel butterfly in Section 2, as follows:

\[
(y[2] + g \cdot y[1] + \text{prk3}_r - y'[1])^5 + g \cdot (y[2] + g \cdot y[1] + \text{prk3}_r)^2 \\
= x[1] + g \cdot x[2] + \text{prk1}_r
\]

\[
(g \cdot y[2] + (g^2 + 1) \cdot y[1] + \text{prk4}_r - y'[2])^5 + g \cdot (g \cdot y[2] + (g^2 + 1) \cdot y[1] + \text{prk4}_r)^2 \\
= g \cdot x[1] + (g^2 + 1) \cdot x[2] + \text{prk2}_r
\]

\[
(y[2] + g \cdot y[1] + \text{prk3}_r - y'[1])^5 + g \cdot (g \cdot y[2] + (g^2 + 1) \cdot y[1] + \text{prk4}_r)^2 \\
= x'[1]
\]

\[
(y[2] + g \cdot y[1] + \text{prk3}_r - y'[2])^5 + g \cdot y'[2]^2 + g^{-1} \\
= x'[2]
\]

We replace \((x[1], x[2], y[1], y[2]), (x'[1], x'[2], y'[1], y'[2]), (\text{prk1}_r, \text{prk2}_r, \text{prk3}_r, \text{prk4}_r)\) as follows.

\[
x[1] \Rightarrow w_1(X) \hspace{1cm} x'[1] \Rightarrow w_1(X) \hspace{1cm} \text{prk1}_r \Rightarrow q_{\text{prk1}}(X) \\
x[2] \Rightarrow w_2(X) \hspace{1cm} x'[2] \Rightarrow w_2(X) \hspace{1cm} \text{prk2}_r \Rightarrow q_{\text{prk2}}(X) \\
y[1] \Rightarrow w_3(X) \hspace{1cm} y'[1] \Rightarrow w_3(X) \hspace{1cm} \text{prk3}_r \Rightarrow q_{\text{prk3}}(X) \\
y[2] \Rightarrow w_4(X) \hspace{1cm} y'[2] \Rightarrow w_4(X) \hspace{1cm} \text{prk4}_r \Rightarrow q_{\text{prk4}}(X)
\]

We add four selector polynomials \(q_{\text{sel1}}(X), q_{\text{sel2}}(X), q_{\text{sel3}}(X), \) and \(q_{\text{sel4}}(X)\) to configure whether an equation applies to a gate. The new polynomial identity relations are as follows.

\[
q_{\text{sel1}}(X) \cdot ((w_1(X) + g \cdot w_3(X) + q_{\text{prk3}}(X) - w_3(X) \omega))^5 \\
+ g \cdot (w_1(X) + g \cdot w_3(X) + q_{\text{prk3}}(X))^2 \\
- (w_1(X) + g \cdot w_2(X) + q_{\text{prk1}}(X))) = 0 \hspace{1cm} \text{// the first equation for Anemoi/Jive}
\]

\[
q_{\text{sel2}}(X) \cdot ((g \cdot w_4(X) + (g^2 + 1) \cdot w_3(X) + q_{\text{prk4}}(X) - w_4(X) \omega))^5 \\
+ g \cdot (g \cdot w_4(X) + (g^2 + 1) \cdot w_3(X) + q_{\text{prk4}}(X))^2 \\
- (g \cdot w_1(X) + (g^2 + 1) \cdot w_2(X) + q_{\text{prk2}}(X))) = 0 \hspace{1cm} \text{// the second equation for Anemoi/Jive}
\]

\[
q_{\text{sel3}}(X) \cdot ((w_1(X) + g \cdot w_3(X) + q_{\text{prk3}}(X) - w_3(X) \omega))^5 \\
+ g \cdot (w_3(X) \omega)^2 + g^{-1} - w_1(X) \omega)) = 0 \hspace{1cm} \text{// the third equation for Anemoi/Jive}
\]

\[
q_{\text{sel4}}(X) \cdot ((g \cdot w_4(X) + (g^2 + 1) \cdot w_3(X) + q_{\text{prk4}}(X) - w_4(X) \omega))^5 \\
+ g \cdot (w_4(X) \omega)^2 + g^{-1} - w_2(X) \omega)) = 0 \hspace{1cm} \text{// the fourth equation for Anemoi/Jive}
\]

A strategy to open the points for linearization is as follows. This strategy opens all the polynomials that we have discussed, and it is suboptimal. Later we will discuss how to optimize this strategy.

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\[
q_{sel1}(\zeta) \cdot ((w_4(\zeta) + g \cdot w_3(\zeta) + q_{prk3}(\zeta) - w_3(\zeta \omega))^5 \\
+ g \cdot (w_4(\zeta) + g \cdot w_3(\zeta) + q_{prk3}(\zeta))^2 \\
- (w_1(\zeta) + g \cdot w_2(\zeta) + q_{prk1}(\zeta)) = 0 // \text{the first equation for Anemoi/Jive}
\]
\[
q_{sel2}(\zeta) \cdot ((g \cdot w_4(\zeta) + (g^2 + 1) \cdot w_3(\zeta) + q_{prk4}(\zeta) - w_4(\zeta \omega))^5 \\
+ g \cdot (g \cdot w_4(\zeta) + (g^2 + 1) \cdot w_3(\zeta) + q_{prk4}(\zeta))^2 \\
- (g \cdot w_1(\zeta) + (g^2 + 1) \cdot w_2(\zeta) + q_{prk2}(\zeta)) = 0 // \text{the second equation for Anemoi/Jive}
\]
\[
q_{sel3}(\zeta) \cdot ((w_4(\zeta) + g \cdot w_3(\zeta) + q_{prk3}(\zeta) - w_3(\zeta \omega))^5 \\
+ g \cdot (w_3(\zeta \omega))^2 + g^{-1} - w_1(\zeta \omega) = 0 // \text{the third equation for Anemoi/Jive}
\]
\[
q_{sel4}(\zeta) \cdot ((g \cdot w_4(\zeta) + (g^2 + 1) \cdot w_3(\zeta) + q_{prk4}(\zeta) - w_4(\zeta \omega))^5 \\
+ g \cdot (w_4(\zeta \omega))^2 + g^{-1} - w_2(\zeta \omega) = 0 // \text{the fourth equation for Anemoi/Jive}
\]

**Cost analysis.** In later sections we will use some optimization tricks on this initial attempt, so it would be useful for us to see what can be improved.

The initial attempt has the following cost, which we describe as 8 + 12.

- additional indexing for the four selector polynomials \(q_{sel1}(X), q_{sel2}(X), q_{sel3}(X), q_{sel4}(X)\) and four processed key polynomials \(q_{prk1}(X), q_{prk2}(X), q_{prk3}(X), q_{prk4}(X)\)
- additional opening for four selector polynomials \(q_{sel1}(\zeta), q_{sel2}(\zeta), q_{sel3}(\zeta), q_{sel4}(\zeta)\), four processed key polynomials \(q_{prk1}(\zeta), q_{prk2}(\zeta), q_{prk3}(\zeta), q_{prk4}(\zeta)\), and four witness polynomials on input wires \(w_1(\zeta \omega), w_2(\zeta \omega), w_3(\zeta \omega), w_4(\zeta \omega)\)

There are other side effects. For example, the use of the shifting trick in \(w_4(\zeta \omega)\) requires us to increase the hiding bound of \(w_4(X)\), or more precisely, \(\hat{w}_4(X)\). We consider such costs to be minor, and will only be focusing on costs relevant to multiscalar multiplication (MSM) in this note.
5 Optimization

In this section we discuss optimization to the construction in Section 4. The optimization presented here is standard and not really novel because these ideas have been used in production. We choose to present the optimization step-by-step as we believe that it is instructive.

5.1 Use processed round key polynomials as selectors

For a gate that the technique is applied to, the two processed round key polynomials $q_{prk3}(X)$ and $q_{prk4}(X)$, with an overwhelming probability, are nonzeros. For a gate that is not related to the Anemoi hash, we can choose whatever we want as the evaluation for $q_{prk3}(X)$ and $q_{prk4}(X)$ at the location of this gate. We proceed by making $q_{prk3}(X)$ and $q_{prk4}(X)$ zeroes in locations not related to the Anemoi hash.

Now, we can replace the four selector polynomials $q_{sel1}(X), q_{sel2}(X), q_{sel3}(X), q_{sel4}(X)$ with any of the processed round key polynomials, $q_{prk3}(X)$ or $q_{prk4}(X)$. We choose to only use $q_{prk3}(X)$ for all four equations. This gives us a TurboPlonk that is defined with the following polynomials. We highlight the difference using an orange box.

\[
q_{prk3}(X) \cdot ((w_4(X) + g \cdot w_3(X) + q_{prk3}(X) - w_3(X\omega))^5
+ g \cdot (w_4(X) + g \cdot w_3(X) + q_{prk3}(X))^2
- (w_1(X) + g \cdot w_2(X) + q_{prk3}(X))) = 0  \quad // \text{the first equation for Anemoi/Jive}
\]
\[
q_{prk3}(X) \cdot ((g \cdot w_4(X) + (g^2 + 1) \cdot w_3(X) + q_{prk4}(X) - w_4(X\omega))^5
+ g \cdot (g \cdot w_4(X) + (g^2 + 1) \cdot w_3(X) + q_{prk4}(X))^2
- (g \cdot w_1(X) + (g^2 + 1) \cdot w_2(X) + q_{prk2}(X))) = 0  \quad // \text{the second equation for Anemoi/Jive}
\]
\[
q_{prk3}(X) \cdot ((w_4(X) + g \cdot w_3(X) + q_{prk3}(X) - w_3(X\omega))^5
+ g \cdot (w_3(X\omega))^2 + g^{-1} - w_1(X\omega)) = 0  \quad // \text{the third equation for Anemoi/Jive}
\]
\[
q_{prk3}(X) \cdot ((g \cdot w_4(X) + (g^2 + 1) \cdot w_3(X) + q_{prk4}(X) - w_4(X\omega))^5
+ g \cdot (w_4(X\omega))^2 + g^{-1} - w_2(X\omega)) = 0  \quad // \text{the fourth equation for Anemoi/Jive}
\]

The corresponding opening strategy is as follows.

\[
q_{prk3}(\zeta) \cdot ((w_4(\zeta) + g \cdot w_3(\zeta) + q_{prk3}(\zeta) - w_3(\zeta\omega))^5
+ g \cdot (w_4(\zeta) + g \cdot w_3(\zeta) + q_{prk3}(\zeta))^2
- (w_1(\zeta) + g \cdot w_2(\zeta) + q_{prk1}(\zeta)) = 0  \quad // \text{the first equation for Anemoi/Jive}
\]
\[
q_{prk3}(\zeta) \cdot ((g \cdot w_4(\zeta) + (g^2 + 1) \cdot w_3(\zeta) + q_{prk4}(\zeta) - w_3(\zeta\omega))^5
+ g \cdot (g \cdot w_4(\zeta) + (g^2 + 1) \cdot w_3(\zeta) + q_{prk4}(\zeta))^2
- (w_4(\zeta\omega))^5
+ q_{prk3}(\zeta) = 0  \quad // \text{the fourth equation for Anemoi/Jive}
\]

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Cost analysis. The construction in Section 5.1 has the following cost, which we call $4 + 8$.

- additional indexing for four processed key polynomials $q_{prk1}(X), q_{prk2}(X), q_{prk3}(X), q_{prk4}(X)$
- additional opening for four processed key polynomials $q_{prk1}(\zeta), q_{prk2}(\zeta), q_{prk3}(\zeta), q_{prk4}(\zeta)$, and four witness polynomials on input wires $w_1(\zeta\omega), w_2(\zeta\omega), w_3(\zeta\omega), w_4(\zeta\omega)$

5.2 Skip unnecessary opening during linearization

Note that our construction has an overlinearization issue. In TurboPlonk, we actually do not need to open all the polynomials in the equation because verifiers can use their corresponding polynomial commitments, which are linearly homomorphic, to check a polynomial commitment equation. An example is the boolean testing constraint, in which $q_b(X)$ was never opened. This also explains what readers can see from many TurboPlonk implementations where the indexer polynomials are generally never opened, and the verifier uses their commitments for a zero test/identity test.

$$q_b(X) \cdot w_2(\zeta) \cdot (w_2(\zeta) - 1) = 0$$

For linearization, we need to lower the degree of the quotient polynomial to $n$ by opening these polynomials at specific points and replacing the polynomials with their corresponding evaluations. It is important to first identify what polynomials must be opened.

Looking at the equations in Section 5.1, we can see that two of the processed round key polynomials, $q_{prk3}(X)$ and $q_{prk4}(X)$, are inside a bracket of $(\cdot)^5$. The verifier cannot perform the identity test over these commitments because the commitments are, although homomorphic, only linearly homomorphic. As a result, we first know that $q_{prk3}(X)$ and $q_{prk4}(X)$ must be opened.

Then, we look at the other two processed round key polynomials, $q_{prk1}(X)$ and $q_{prk2}(X)$. They are a linear component of the equation, and therefore, we can use the same treatment as other indexer polynomials. This gives us the following opening strategy, while the quotient polynomial remains unchanged.

$$q_{prk3}(\zeta) \cdot (\left(w_4(\zeta) + g \cdot w_3(\zeta) + q_{prk3}(\zeta) - w_3(\zeta\omega)\right)^5$$

$$+ g \cdot (w_1(\zeta) + g \cdot w_3(\zeta) + q_{prk3}(\zeta))^2$$

$$- \left(w_1(\zeta) + g \cdot w_2(\zeta) + q_{prk1}(X)\right) = 0 \quad // \text{the first equation for Anemoi/Jive}$$

$$q_{prk3}(\zeta) \cdot (\left((g \cdot w_4(\zeta) + (g^2 + 1) \cdot w_3(\zeta) + q_{prk4}(\zeta) - w_4(\zeta\omega)\right)^5$$
\[ + g \cdot (g \cdot w_4(\zeta) + (g^2 + 1) \cdot w_3(\zeta) + q_{prk4}(\zeta))^2 \\
- (g \cdot w_1(\zeta) + (g^2 + 1) \cdot w_2(\zeta) + q_{prk2}(X)) = 0 \] // the second equation for Anemoi/Jive

\[ q_{prk3}(\zeta) \cdot ((w_4(\zeta) + g \cdot w_3(\zeta) + q_{prk3}(\zeta) - w_3(\zeta)) )^5 \\
+ g \cdot (w_3(\zeta))^2 + g^{-1} - w_1(\zeta) ) = 0 \] // the third equation for Anemoi/Jive

\[ q_{prk3}(\zeta) \cdot ((g \cdot w_4(\zeta) + (g^2 + 1) \cdot w_3(\zeta) + q_{prk4}(\zeta) - w_4(\zeta) )^5 \\
+ g \cdot (w_1(\zeta))^2 + g^{-1} - w_2(\zeta) ) = 0 \] // the fourth equation for Anemoi/Jive

**Cost analysis.** The construction in Section 5.2 has the following cost, which we call \( 4 + 6 \).

- additional indexing for four processed key polynomials \( q_{prk1}(X), q_{prk2}(X), q_{prk3}(X), q_{prk4}(X) \)
- additional opening for two of the processed key polynomials \( q_{prk3}(\zeta), q_{prk4}(\zeta) \), and four witness polynomials on input wires \( w_1(\zeta), w_2(\zeta), w_3(\zeta), w_4(\zeta) \)

5.3 **Connect the output wire to the next gate**

We now describe the last optimization that we apply to give us \( 4 + 5 \). Looking at the equations shown in Section 5.1 and Section 5.2, we have used \( w_1(\zeta), w_2(\zeta), w_3(\zeta), w_4(\zeta), w_1(\zeta), w_2(\zeta), w_3(\zeta), w_4(\zeta) \), but we have never used the output wire \( w_o(\zeta) \). To further optimize the TurboPlonk instantiation, we want to further reduce the number of polynomials being opened, since this can reduce the proof size.

To do that, we want to substitute \( w_4(\zeta) \) with \( w_o(\zeta) \). We can also use \( w_o(\zeta) \) to replace the other three \( w_1(\zeta), w_2(\zeta), w_3(\zeta) \), here, without loss of generality, we choose to replace \( w_4(\zeta) \).

Simply replacing \( w_4(\zeta) \) with \( w_o(\zeta) \) is not sufficient, because we also need to enforce that the output is correctly connected to the fourth input wire of the next gate. This is, in fact, very easy to achieve, by using the copy check. With the assumption that the copy check has been done, we have the following equations.

\[ q_{prk3}(X) \cdot ((w_4(X) + g \cdot w_3(X) + q_{prk3}(X) - w_3(X\zeta))^5 \\
+ g \cdot (w_4(X) + g \cdot w_3(X) + q_{prk3}(X))^2 \\
- (w_1(X) + g \cdot w_2(X) + q_{prk1}(X)) = 0 \] // the first equation for Anemoi/Jive

\[ q_{prk3}(X) \cdot ((g \cdot w_4(X) + (g^2 + 1) \cdot w_3(X) + q_{prk4}(X) - w_o(X))^5 \\
+ g \cdot (g \cdot w_4(X) + (g^2 + 1) \cdot w_3(X) + q_{prk4}(X))^2 \\
- (g \cdot w_1(X) + (g^2 + 1) \cdot w_2(X) + q_{prk2}(X))) = 0 \] // the second equation for Anemoi/Jive

\[ q_{prk3}(X) \cdot ((w_4(X) + g \cdot w_3(X) + q_{prk3}(X) - w_3(X\zeta))^5 \\
+ g \cdot (w_3(X\zeta))^2 + g^{-1} - w_1(X\zeta) ) = 0 \] // the third equation for Anemoi/Jive

\[ q_{prk3}(X) \cdot ((g \cdot w_4(X) + (g^2 + 1) \cdot w_3(X) + q_{prk4}(X) - w_o(X))^5 \\
+ g \cdot (w_o(X))^2 + g^{-1} - w_2(X\zeta) ) = 0 \] // the fourth equation for Anemoi/Jive
The opening strategy is similar. Note that $w_o(\zeta)$ is already opened for the other part of TurboPlonk, so this optimization allows us to reduce the number of openings by one. In addition, $w_4(X)$, or more precisely, $\overline{w}_4(X)$, does not need a higher hiding degree.

$$
q_{prk3}(\zeta) \cdot ((w_4(\zeta) + g \cdot w_3(\zeta) + q_{prk3}(\zeta) - w_3(\zeta \omega))^5
+ g \cdot (w_1(\zeta) + g \cdot w_3(\zeta) + q_{prk3}(\zeta))^2
- (w_1(\zeta) + g \cdot w_2(\zeta) + q_{prk1}(X))) = 0 \quad // \text{the first equation for Anemoi/Jive}
$$

$$
q_{prk3}(\zeta) \cdot ((g \cdot w_4(\zeta) + (g^2 + 1) \cdot w_3(\zeta) + q_{prk4}(\zeta) - w_o(\zeta))^5
+ g \cdot (g \cdot w_4(\zeta) + (g^2 + 1) \cdot w_3(\zeta) + q_{prk4}(\zeta))^2
- (g \cdot w_1(\zeta) + (g^2 + 1) \cdot w_2(\zeta) + q_{prk2}(X))) = 0 \quad // \text{the second equation for Anemoi/Jive}
$$

$$
q_{prk3}(\zeta) \cdot ((w_4(\zeta) + g \cdot w_3(\zeta) + q_{prk3}(\zeta) - w_3(\zeta \omega))^5
+ g \cdot (w_3(\zeta \omega)^2 + g^{-1} - w_1(\zeta \omega)) = 0 \quad // \text{the third equation for Anemoi/Jive}
$$

$$
q_{prk3}(\zeta) \cdot ((g \cdot w_4(\zeta) + (g^2 + 1) \cdot w_3(\zeta) + q_{prk4}(\zeta) - w_o(\zeta))^5
+ g \cdot (w_o(\zeta)^2 + g^{-1} - w_2(\zeta \omega)) = 0 \quad // \text{the fourth equation for Anemoi/Jive}
$$

**Cost analysis.** The construction in Section 5.3 has the following cost, which we call 4 + 5.

- additional indexing for four processed key polynomials $q_{prk1}(X)$, $q_{prk2}(X)$, $q_{prk3}(X)$, $q_{prk4}(X)$
- additional opening for two of the processed key polynomials $q_{prk3}(\zeta)$, $q_{prk4}(\zeta)$, and three of the witness polynomials on input wires $w_1(\zeta \omega)$, $w_2(\zeta \omega)$, $w_3(\zeta \omega)$.

### 5.4 Other unexplored optimization

We did not explore more optimization techniques, as we consider the rest to be too aggressive and can have some side effects.

**Explicitly structured round keys.** One observation is that the round keys are generated from an open butterfly structure in Anemoi. There can be other ways to generate the round keys, and may be able to reduce the number of processed round key polynomials. We did not explore such directions because even if we can reduce the number of processed round key polynomials, it seems to only benefit the verifier, in that the verifier may do one or two fewer scalar multiplication.

**More than five wires.** A construction with more wires may be able to reduce the number of gates further. We choose to stick to five because, assuming that the number of gates (after padding) is $N = 2^k$, then the FFT space for computing the quotient polynomial is of size larger than $5N$ but smaller than $6N$ (for sufficiently large $N$), and for BLS12-381, the curve that we are using, we can use a mixed-radix FFT of size $2^{k+1} \cdot 3$. When we increase the number of wires further, we may have to use an FFT space of size $2^{k+3}$, i.e., $8N$. Not only does the FFT cost go up, but we also need to commit more polynomials, as the quotient polynomial needs to be split into more size-$N$ polynomials and we need to commit more witness polynomials. We think implementing the
Anemoi hash function over a different curve or with different numbers of wires to be an interesting work on its own.

**Curve where \( \alpha = 3 \) is permissible.** Let the constraint field prime be \( q \). If \( 3 \nmid (q - 1) \), then we can use \( \alpha = 3 \) in the S-box. This is beneficial because, assuming that we do not have other gates that cause the quotient polynomial to go to very high degrees (which is not the case in Section 3, as we have the customized gate for twisted Edwards curves, and we also have five wires), having \( \alpha = 3 \) allows us to have a quotient polynomial of lower degrees. This not only reduces the cost of FFT but also reduces the cost of MSM and the proof size. The increase in the number of rounds necessary for security appears to be minor. Unfortunately, for BLS12-381, \( \alpha = 3 \) is not permissible since \( 3 \mid (q - 1) \).

**Using a higher \( \alpha \) in the S-box.** Another direction is to use \( \alpha \) that is larger in the hope that it can reduce the number of rounds. This seems to not be worthwhile in general, as the reduction to the number of rounds is minor, but it increases the work of FFT and MSM and increases the proof size, unless there are already other high-degree customized gates, or there are more wires.
6 Final protocol

For completeness we present the final protocol in this section. The polynomial identity relations are as follows.

\[
q_1(X) \cdot w_1(X) + q_2(X) \cdot w_2(X) + q_3(X) \cdot w_3(X) + q_4(X) \cdot w_4(X) \quad // \text{linear combination}
\]
\[
+ q_{m1}(X) \cdot w_1(X) \cdot w_2(X) + q_{m2}(X) \cdot w_3(X) \cdot w_4(X) \quad // \text{multiplication (somewhat)}
\]
\[
+ q_c(X) \quad // \text{constants}
\]
\[
+ \mathcal{P}(X) \quad // \text{inputs}
\]
\[
+ q_{\text{ecc}}(X) \cdot w_1(X) \cdot w_2(X) \cdot w_3(X) \cdot w_4(X) \cdot w_o(X) \quad // \text{operations on twisted Edwards curves}
\]
\[
= q_o(X) \cdot w_o(X) \quad // \text{output}
\]
\[
q_b(X) \cdot w_2(X) \cdot (w_2(X) - 1) = 0 \quad // \text{boolean testing on the second wire}
\]
\[
q_b(X) \cdot w_3(X) \cdot (w_3(X) - 1) = 0 \quad // \text{boolean testing on the third wire}
\]
\[
q_b(X) \cdot w_4(X) \cdot (w_4(X) - 1) = 0 \quad // \text{boolean testing on the fourth wire}
\]
\[
q_{prk3}(X) \cdot ((w_4(X) + g \cdot w_3(X) + q_{prk3}(X) - w_3(X \omega))^5
+ g \cdot (w_4(X) + g \cdot w_3(X) + q_{prk3}(X))^2
- (w_1(X) + g \cdot w_2(X) + q_{prk1}(X))) = 0 \quad // \text{the first equation for Anemoi/Jive}
\]
\[
q_{prk3}(X) \cdot ((g \cdot w_4(X) + (g^2 + 1) \cdot w_3(X) + q_{prk4}(X) - w_o(X))^5
+ g \cdot (g \cdot w_4(X) + (g^2 + 1) \cdot w_3(X) + q_{prk4}(X))^2
- (g \cdot w_1(X) + (g^2 + 1) \cdot w_2(X) + q_{prk2}(X))) = 0 \quad // \text{the second equation for Anemoi/Jive}
\]
\[
q_{prk3}(X) \cdot ((w_4(X) + g \cdot w_3(X) + q_{prk3}(X) - w_3(X \omega))^5
+ g \cdot (w_3(X \omega))^2 + g^{-1} - w_1(X \omega)) = 0 \quad // \text{the third equation for Anemoi/Jive}
\]
\[
q_{prk3}(X) \cdot ((g \cdot w_4(X) + (g^2 + 1) \cdot w_3(X) + q_{prk4}(X) - w_o(X))^5
+ g \cdot (w_o(X))^2 + g^{-1} - w_2(X \omega)) = 0 \quad // \text{the fourth equation for Anemoi/Jive}
\]

We now describe the indexer, the prover, and the verifier.

6.1 Indexer

The indexer in TurboPlonk computes the commitments and openings for indexer polynomials, which consist of the following:

- The fourteen selector polynomials:
  \[
  q_1(X), q_2(X), q_3(X), q_4(X), q_o(X), q_m1(X), q_m2(X), q_c(X), q_{\text{ecc}}(X), q_b(X),
  q_{prk1}(X), q_{prk2}(X), q_{prk3}(X), q_{prk4}(X)
  \]

- The five permutation polynomials:
  \[
  S_{\sigma_1}(X), S_{\sigma_2}(X), S_{\sigma_3}(X), S_{\sigma_4}(X), S_{\sigma_o}(X)
  \]
First of all, let the number of gates be $n$, the constraint system should have indicated a permutation $\sigma : [5n] \rightarrow [5n]$, which fulfills the following requirements.

- Let $w(X)$ be the concatenated witness polynomial of $w_1(X), w_2(X), w_3(X), w_4(X), w_5(X)$. The concatenation is over the evaluation representation, not the coefficient representation.

- The evaluation of $w(X)$ remains unchanged after applying $\sigma$ as a permutation over the evaluation of $w(X)$ itself.

Now, given four different quadratic non-residues $(k_1, k_2, k_3, k_4)$ and a generator $\omega$ with order $n$, we define a mapping as follows.

$$\sigma_0(i) = \begin{cases} 
\omega^{i-1} & i \in \{1, 2, ..., n\} \\
 k_1 \cdot \omega^{i-1-n} & i \in \{n + 1, n + 2, ..., 2n\} \\
 k_2 \cdot \omega^{i-1-2n} & i \in \{2n + 1, 2n + 2, ..., 3n\} \\
 k_3 \cdot \omega^{i-1-3n} & i \in \{3n + 1, 3n + 2, ..., 4n\} \\
 k_4 \cdot \omega^{i-1-4n} & i \in \{4n + 1, 4n + 2, ..., 5n\} 
\end{cases}$$

where $i = 1, 2, ..., n$. And we apply this mapping to each element in $\sigma$, obtaining a map $\sigma^*(x) : [5n] \rightarrow \mathbb{F}$. We split this map into five permutation polynomials, as follows.

- $S_{\sigma_1}(X)$’s evaluation on $1, \omega, ..., \omega^{n-1}$ equals $\sigma^*(x)$’s evaluation on $1, 2, ..., n$.
- $S_{\sigma_2}(X)$’s evaluation on $1, \omega, ..., \omega^{n-1}$ equals $\sigma^*(x)$’s evaluation on $n + 1, n + 2, ..., 2n$.
- $S_{\sigma_3}(X)$’s evaluation on $1, \omega, ..., \omega^{n-1}$ equals $\sigma^*(x)$’s evaluation on $2n + 1, 2n + 2, ..., 3n$.
- $S_{\sigma_4}(X)$’s evaluation on $1, \omega, ..., \omega^{n-1}$ equals $\sigma^*(x)$’s evaluation on $3n + 1, 3n + 2, ..., 4n$.
- $S_{\sigma_5}(X)$’s evaluation on $1, \omega, ..., \omega^{n-1}$ equals $\sigma^*(x)$’s evaluation on $4n + 1, 4n + 2, ..., 5n$.

**Step 1: commit all polynomials.** We first commit all these indexer polynomials. The commitments are included in the verifier parameters. We then perform some precomputation: we prepare a representation of these polynomials that are easy to be use later for proving, by doing a coset FFT over them. The prepared polynomials are included in the prover parameters.

**Step 2: precompute the two helper polynomials.** Compute the following polynomial defined on a domain $H$ of size $n$:

$$L_1(X) = \frac{X^n - 1}{X - 1}$$

and store its coset FFT representation. This is done by first observing that $L_1(X)$ evaluates to $n$ on $X = 1$ and 0 otherwise in $H$. We can perform an inverse FFT to convert it back to the coefficient representation (which indeed looks nontrivial). Then, we perform a coset FFT, which gives us the prepared version of this polynomial.
Another polynomial we precompute is the vanishing polynomial of domain $H$ of size $n$:

$$Z_H(X) = X^n - 1$$

and we want to store its coset FFT representation. This is done by a coset FFT over the coefficient representation above. The representations for the two helper polynomials are included in the prover parameters.

**Step 3: compute the Lagrange interpolation constants.** Recall that the Lagrange interpolation from $(1, y_0), (\omega, y_1), \ldots, (\omega^n, y_n)$ where $\omega$ is the generator for a domain $H$, into a polynomial $f(X)$ of degree $n$ is as follows:

$$f(X) = \sum_{j=0}^{n} y_j \left( \prod_{0 \leq m \leq n \atop m \neq j} \frac{X - \omega^m}{\omega^j - \omega^m} \right)$$

We can rewrite it as follows.

$$f(X) = \sum_{j=0}^{n} y_j \left( \prod_{0 \leq m \leq n \atop m \neq j} \frac{X - \omega^m}{\omega^j - \omega^m} \right)$$

$$= \left( \prod_{0 \leq m \leq n} (X - \omega^m) \right) \left( \sum_{j=0}^{n} \frac{y_j}{X - \omega^j} \left( \prod_{0 \leq m \leq n \atop m \neq j} \frac{1}{\omega^j - \omega^m} \right) \right)$$

Now, precompute $c_j$ for every $j \in \{0, 1, \ldots, n\}$.

$$c_j = \prod_{0 \leq m \leq n \atop m \neq j} \frac{1}{\omega^j - \omega^m}$$

This allows us to simplify $f(X)$ as follows.

$$f(X) = \left( \prod_{0 \leq m \leq n} (X - \omega^m) \right) \sum_{j=0}^{n} \frac{c_j \cdot y_j}{X - \omega^j}$$

$$= (X^n - 1) \sum_{j=0}^{n} \frac{c_j \cdot y_j}{X - \omega^j}$$

These constants $c_j$ ($j \in \{0, 1, \ldots, n\}$) are included in the verifier parameters. We conclude the description of the indexer.
6.2 Prover

The prover in TurboPlonk uses the prover parameters from the indexer and a complete constraint system with all the gate values and copy check information ready. It follows the following steps.

Step 1: assemble public inputs. The prover parameters have indicated which witness value indeed belongs to public inputs. The prover finds those witness values and stores them in a vector of length \( n_{in} \), which is the number of field elements in public inputs. This is to enable us to calculate the state of the verifier.

Step 2: instantiate the verifier. For the purpose of the Fiat-Shamir transform, we create a cryptographic sponge, which will absorb the verifier’s state as well as the messages that the verifier would receive from the prover in an interactive proof protocol.

After we create the sponge, we put the following two things into the sponge: (1) verifier parameters and (2) public inputs.

Step 3: commit witness polynomials with hiding. Given the witness polynomials \( w_1(X), w_2(X), w_3(X), w_4(X), w_5(X) \), we add a random blinding polynomial over each of them. The prover samples \( b_1, b_2, b_3, ..., b_{13} \in \mathbb{F} \) and computes blinded witness polynomials.

\[
\begin{align*}
\widetilde{w}_1(X) &= w_1(X) + Z_H(X) \cdot (b_1 \cdot X^2 + b_2 \cdot X + b_3) \\
\widetilde{w}_2(X) &= w_2(X) + Z_H(X) \cdot (b_4 \cdot X^2 + b_5 \cdot X + b_6) \\
\widetilde{w}_3(X) &= w_3(X) + Z_H(X) \cdot (b_7 \cdot X^2 + b_8 \cdot X + b_9) \\
\widetilde{w}_4(X) &= w_4(X) + Z_H(X) \cdot (b_{10} \cdot X + b_{11}) \\
\widetilde{w}_5(X) &= w_5(X) + Z_H(X) \cdot (b_{12} \cdot X + b_{13})
\end{align*}
\]

We commit each of the polynomial above and put the polynomial commitments \( cm_{w_1}, cm_{w_2}, cm_{w_3}, cm_{w_4}, cm_{w_5} \in \mathbb{G}_1 \) into the sponge.

Step 4: build the sigma polynomial, for wiring. The prover squeezes out two challenges \( \beta, \gamma \in \mathbb{F} \) from the sponge. We now need to build the sigma polynomial. It helps for us to first compute:

\[
S_i := \frac{(w_i + \beta \cdot \omega^{i-1} + \gamma) \cdot (w_{n+i} + \beta \cdot k_1 \cdot \omega^{i-1} + \gamma) \cdot (w_{2n+i} + \beta \cdot k_2 \cdot \omega^{i-1} + \gamma) \cdot (w_{3n+i} + \beta \cdot k_3 \cdot \omega^{i-1} + \gamma) \cdot (w_{4n+i} + \beta \cdot k_4 \cdot \omega^{i-1} + \gamma)}{(w_{i} + \sigma^*(i) \cdot \beta + \gamma) \cdot (w_{n+i} + \sigma^*(n+i) \cdot \beta + \gamma) \cdot (w_{2n+i} + \sigma^*(2n+i) \cdot \beta + \gamma) \cdot (w_{3n+i} + \sigma^*(3n+i) \cdot \beta + \gamma) \cdot (w_{4n+i} + \sigma^*(4n+i) \cdot \beta + \gamma)}
\]

We can then define the permutation polynomial \( z(X) \) with the following evaluations:

\[
z(\omega^{i-1}) = \begin{cases} 
1 & i = 1 \\
\prod_{j=1}^{i-1} S_j & i = 2, 3, ..., n
\end{cases}
\]

Step 5: commit the sigma polynomial, with hiding. The prover first samples \( b_{14}, b_{15}, b_{16} \in \mathbb{F} \) and apply them as blinding factors to the polynomial \( z(X) \).

\[
\widetilde{z}(X) = z(X) + Z_H(X) \cdot (b_{14}X^2 + b_{15}X + b_{16})
\]
We commit this polynomial and put the polynomial commitment $cm_1 \in \mathbb{G}_1$ into the sponge.

**Step 6: compute the quotient polynomial.** The prover squeezes out a challenge $\alpha$ from the sponge. This is used to construct the following polynomial.

$$t(X) = t_{sat}(X) \cdot \frac{1}{Z_H(X)} + t_{\sigma_1}(X) \cdot \frac{\alpha}{Z_H(X)} - t_{\sigma_2}(X) \cdot \frac{\alpha}{Z_H(X)} + t_{\sigma_3}(X) \cdot \frac{\alpha^2}{Z_H(X)}$$

$$+ t_{b_1}(X) \cdot \frac{\alpha^3}{Z_H(X)} + t_{b_2}(X) \cdot \frac{\alpha^4}{Z_H(X)} + t_{b_3}(X) \cdot \frac{\alpha^5}{Z_H(X)} - t_{h_1}(X) \cdot \frac{\alpha^6}{Z_H(X)}$$

$$- t_{h_2}(X) \cdot \frac{\alpha^7}{Z_H(X)} - t_{h_3}(X) \cdot \frac{\alpha^8}{Z_H(X)} - t_{h_4}(X) \cdot \frac{\alpha^9}{Z_H(X)}$$

where

$$t_{sat}(X) = q_1(X) \cdot \tilde{w}_1(X) + q_2(X) \cdot \tilde{w}_2(X) + q_3(X) \cdot \tilde{w}_3(X) + q_4(X) \cdot \tilde{w}_4(X)$$

$$+ q_{m_1}(X) \cdot \tilde{w}_1(X) \cdot \tilde{w}_2(X) + q_{m_2}(X) \cdot \tilde{w}_3(X) \cdot \tilde{w}_4(X) + q_c(X) + PI(X)$$

$$+ q_{ecc}(X) \cdot \tilde{w}_1(X) \cdot \tilde{w}_2(X) \cdot \tilde{w}_3(X) \cdot \tilde{w}_4(X) \cdot \tilde{w}_o(X)$$

$$- q_o(X) \cdot \tilde{w}_o(X)$$

and

$$t_{\sigma_1}(X) = (\tilde{w}_1(X) + \beta \cdot X + \gamma) \cdot (\tilde{w}_2(X) + \beta \cdot k_1 \cdot X + \gamma) \cdot (\tilde{w}_3(X) + \beta \cdot k_2 \cdot X + \gamma)$$

$$\cdot (\tilde{w}_4(X) + \beta \cdot k_3 \cdot X + \gamma) \cdot (\tilde{w}_o(X) + \beta \cdot k_4 \cdot X + \gamma)$$

$$\cdot \tilde{z}(X)$$

and

$$t_{\sigma_2}(X) = (\tilde{w}_1(X) + \beta \cdot S_{\sigma_1}(X) + \gamma) \cdot (\tilde{w}_2(X) + \beta \cdot S_{\sigma_2}(X) + \gamma) \cdot (\tilde{w}_3(X) + \beta \cdot S_{\sigma_3}(X) + \gamma)$$

$$\cdot (\tilde{w}_4(X) + \beta \cdot S_{\sigma_4}(X) + \gamma) \cdot (\tilde{w}_o(X) + \beta \cdot S_{\sigma_o}(X) + \gamma)$$

$$\cdot \tilde{z}(X) \omega \text{ // the shifting trick}$$

and

$$t_{\sigma_3}(X) = (\tilde{z}(X) - 1) \cdot L_1(X)$$

and

$$t_{b_1}(X) = q_b(X) \cdot \tilde{w}_2(X) \cdot (1 - \tilde{w}_2(X))$$

$$t_{b_2}(X) = q_b(X) \cdot \tilde{w}_3(X) \cdot (1 - \tilde{w}_3(X))$$

$$t_{b_3}(X) = q_b(X) \cdot \tilde{w}_4(X) \cdot (1 - \tilde{w}_4(X))$$
and

\[ t_{h1}(X) = q_{prk3}(X) \cdot \left( (\hat{w}_1(X) + g \cdot \hat{w}_3(X) + q_{prk3}(X) - \hat{w}_3(X \omega))^5 \right) 
+ g \cdot (\hat{w}_1(X) + g \cdot \hat{w}_3(X) + q_{prk3}(X))^2 
\left( -\hat{w}_1(X) + g \cdot \hat{w}_2(X) + q_{prk3}(X) \right) \]

\[ t_{h2}(X) = q_{prk3}(X) \cdot \left( (g \cdot \hat{w}_1(X) + (g^2 + 1) \cdot \hat{w}_3(X) + q_{prk4}(X) - \hat{w}_3(X \omega))^5 \right) 
+ g \cdot (g \cdot \hat{w}_1(X) + (g^2 + 1) \cdot \hat{w}_3(X) + q_{prk4}(X))^2 
\left( -g \cdot \hat{w}_1(X) + (g^2 + 1) \cdot \hat{w}_2(X) + q_{prk3}(X) \right) \]

\[ t_{h3}(X) = q_{prk3}(X) \cdot \left( (\hat{w}_1(X) + g \cdot \hat{w}_3(X) + q_{prk3}(X) - \hat{w}_3(X \omega))^5 \right) 
+ g \cdot (\hat{w}_1(X \omega))^2 + g^{-1} - \hat{w}_1(X \omega) \]

\[ t_{h2}(X) = q_{prk3}(X) \cdot \left( (g \cdot \hat{w}_1(X) + (g^2 + 1) \cdot \hat{w}_3(X) + q_{prk4}(X) - \hat{w}_3(X \omega))^5 \right) 
+ g \cdot (g \cdot \hat{w}_1(X \omega))^2 + g^{-1} - \hat{w}_2(X \omega) \]

Then, in the coefficient representations, we split the polynomial into five parts: \( t_1(X), t_2(X), t_3(X), t_4(X), \) and \( t_5(X) \), where each polynomial has degree \( n - 2 \). This is because \( t(X) \) is expected to have degree \( 3 \cdot (n + 2) + 2 \cdot (n + 1) + (n + 2) - n = 5n + 10, \) and \( 5n + 10 = 5 \cdot (n + 2) \).

**Step 7: commit the split quotient polynomials, without hiding.** We commit all these polynomials and put the commitments c_{m1}, c_{m2}, c_{m3}, c_{m4}, and c_{m5} into the sponge.

**Step 8: open the polynomials at a random point.** The prover squeezes a random challenge \( \zeta \in \mathbb{F} \) and compute the following opening evaluations:

\[ \hat{w}_1(\zeta), \hat{w}_2(\zeta), \hat{w}_3(\zeta), \hat{w}_4(\zeta), \hat{w}_o(\zeta), S_{\sigma_1}(\zeta), S_{\sigma_2}(\zeta), S_{\sigma_3}(\zeta), S_{\sigma_4}(\zeta), \]

\[ q_{prk3}(\zeta), q_{prk4}(\zeta), \hat{w}_1(\zeta \omega), \hat{w}_2(\zeta \omega), \hat{w}_3(\zeta \omega), \hat{w}_o(\zeta \omega), \zbar(\zeta \omega) \]

And we remind the readers that some evaluations are over \( \zeta \omega \) instead of \( \zeta \). This is common in entry product arguments. The prover puts these, which are elements in \( \mathbb{F} \), into the sponge.

**Step 9: compute the linearization polynomial.** The linearization polynomial is, at a high level, to replace \( \hat{w}_1(X), \hat{w}_2(X), \hat{w}_3(X), \hat{w}_4(X \omega), \hat{w}_5(X \omega), \hat{w}_6(X \omega), S_{\sigma_1}(X), S_{\sigma_2}(X), S_{\sigma_3}(X), S_{\sigma_4}(X), q_{prk3}(X), q_{prk4}(X), \) and \( \zbar(X \omega) \) with the evaluations over that random point. More specifically, \( r(X) \) reads as follows. Note that the locations of \( Z_H(X) \) are different now. The constant terms of \( r(X) \) are removed here.

\[ r(X) = r_{sat}(X) + r_1(X) \cdot \alpha - r_2(X) \cdot \alpha + r_3(X) \cdot \alpha^2 + r_4(X) \cdot \alpha^3 
+ r_5(X) \cdot \alpha^4 + r_6(X) \cdot \alpha^5 + r_7(X) \cdot \alpha^6 + r_8(X) \cdot \alpha^7 
-Z_H(\zeta)(t_1(X) + \zeta^{n+2} \cdot t_2(X) + \zeta^{2(n+2)} \cdot t_3(X) + \zeta^{3(n+2)} \cdot t_4(X) + \zeta^{4(n+2)} \cdot t_5(X)) \]

where

\[
\begin{align*}
  r_{sat}(X) &= \hat{w}_1(\zeta) \cdot q_1(X) + \hat{w}_2(\zeta) \cdot q_2(X) + \hat{w}_3(\zeta) \cdot q_3(X) + \hat{w}_4(\zeta) \cdot q_4(X) \\
  &+ \hat{w}_1(\zeta) \cdot \hat{w}_2(\zeta) \cdot q_{m1}(X) + \hat{w}_3(\zeta) \cdot \hat{w}_4(\zeta) \cdot q_{m2}(X) + q_c(X) \\
  &+ q_{exc}(X) \cdot \hat{w}_1(\zeta) \cdot \hat{w}_2(\zeta) \cdot \hat{w}_3(\zeta) \cdot \hat{w}_4(\zeta) \cdot \hat{w}_o(\zeta) \\
  &- \hat{w}_5(\zeta) \cdot q_0(X)
\end{align*}
\]
and

\[ r_1(X) = (\bar{w}_1(\zeta) + \beta \cdot \zeta + \gamma) \cdot (\bar{w}_2(\zeta) + \beta \cdot k_1 \cdot \zeta + \gamma) \cdot (\bar{w}_3(\zeta) + \beta \cdot k_2 \cdot \zeta + \gamma) \cdot (\bar{w}_4(\zeta) + \beta \cdot k_3 \cdot \zeta + \gamma) \cdot (\bar{w}_5(\zeta) + \beta \cdot k_4 \cdot \zeta + \gamma) \cdot \bar{z}(X) \]

and

\[ r_2(X) = (\bar{w}_1(\zeta) + \beta S_{\sigma_1}(\zeta) + \gamma) \cdot (\bar{w}_2(\zeta) + \beta S_{\sigma_2}(\zeta) + \gamma) \cdot (\bar{w}_3(\zeta) + \beta S_{\sigma_3}(\zeta) + \gamma) \cdot (\bar{w}_4(\zeta) + \beta S_{\sigma_4}(\zeta) + \gamma) \cdot \beta S_{\sigma_5}(X) \cdot \bar{z}(\zeta) \]

Note that \( S_{\sigma_5}(X) \) is not being replaced by its evaluation, and

\[ r_3(X) = \bar{z}(X) \cdot L_1(\zeta) \]

and

\[ r_4(X) = \theta_b(X) \cdot \bar{w}_2(\zeta) \cdot (\bar{w}_2(\zeta) - 1) \]
\[ r_5(X) = \theta_b(X) \cdot \bar{w}_3(\zeta) \cdot (\bar{w}_3(\zeta) - 1) \]
\[ r_6(X) = \theta_b(X) \cdot \bar{w}_4(\zeta) \cdot (\bar{w}_4(\zeta) - 1) \]

and

\[ r_7(X) = \theta_{prk_3}(\zeta) \cdot \theta_{prk_1}(X) \]
\[ r_8(X) = \theta_{prk_3}(\zeta) \cdot \theta_{prk_2}(X) \]

Now we have the linearization polynomial. Although this polynomial is not linear, its degree is down from \( \approx 5n \) to \( \approx n \), and one can see that the polynomial collapses to a small one.

We note that the evaluation of \( r(X) \) at point \( \zeta \) is as follows. The prover does not need to include this number in the proof because it can be computed by the verifier.

\[ r(\zeta) = -\Pi(\zeta) + \alpha \cdot (\bar{w}_1(\zeta) + \beta S_{\sigma_1}(\zeta) + \gamma) \cdot (\bar{w}_2(\zeta) + \beta S_{\sigma_2}(\zeta) + \gamma) \cdot (\bar{w}_3(\zeta) + \beta S_{\sigma_3}(\zeta) + \gamma) \cdot (\bar{w}_4(\zeta) + \beta S_{\sigma_4}(\zeta) + \gamma) \cdot \bar{z}(\zeta) + \alpha^2 L_1(\zeta) \]
\[ + \alpha^6 \cdot \theta_{prk_3}(\zeta) \left( (\bar{w}_4(\zeta) + g \cdot \bar{w}_3(\zeta) + \theta_{prk_3}(\zeta) - \bar{w}_3(\zeta)) + g \cdot (\bar{w}_4(\zeta) + g \cdot \bar{w}_3(\zeta) + \theta_{prk_3}(\zeta))^2 \right) \]
\[ + \alpha^7 \cdot \theta_{prk_3}(\zeta) \left( (g \cdot \bar{w}_4(\zeta) + (g^2 + 1) \cdot \bar{w}_3(\zeta) + \theta_{prk_4}(\zeta) - \bar{w}_3(\zeta)) + g \cdot (g \cdot \bar{w}_4(\zeta) + (g^2 + 1) \cdot \bar{w}_3(\zeta) + \theta_{prk_4}(\zeta))^2 \right) \]
\[ + \alpha^8 \cdot \theta_{prk_3}(\zeta) \left( (g \cdot \bar{w}_4(\zeta) + (g^2 + 1) \cdot \bar{w}_3(\zeta) + \theta_{prk}(\zeta) - \bar{w}_3(\zeta))^5 \right) \]
\[ + \alpha^9 \cdot \theta_{prk_3}(\zeta) \left( (g \cdot \bar{w}_4(\zeta) + (g^2 + 1) \cdot \bar{w}_3(\zeta) + \theta_{prk}(\zeta) - \bar{w}_3(\zeta))^5 \right) \]
Step 10: compute the opening proof polynomials. Now we want to prove that the previous openings are correct, as well as $r(X)$ is actually vanishing in $H$. We first squeeze out a challenge $v \in \mathbb{F}$ from the sponge. This is done by showing that one can find a polynomial $W_\zeta(X)$ such that:

$$
W_\zeta(X) = \frac{1}{X - \zeta} \begin{pmatrix}
\tilde{w}_1(X) - \tilde{w}_1(\zeta) \\
v(\tilde{w}_2(X) - \tilde{w}_2(\zeta)) \\
v^2(\tilde{w}_3(X) - \tilde{w}_3(\zeta)) \\
v^3(\tilde{w}_4(X) - \tilde{w}_4(\zeta)) \\
v^4(\tilde{w}_5(X) - \tilde{w}_5(\zeta)) \\
v^5(S_{\sigma_1}(X) - S_{\sigma_1}(\zeta)) \\
v^6(S_{\sigma_2}(X) - S_{\sigma_2}(\zeta)) \\
v^7(S_{\sigma_3}(X) - S_{\sigma_3}(\zeta)) \\
v^8(S_{\sigma_4}(X) - S_{\sigma_4}(\zeta)) \\
v^9(q_{prk3}(X) - q_{prk3}(\zeta)) \\
v^{10}(q_{prk4}(X) - q_{prk4}(\zeta)) \\
v^{11}(r(X) - r(\zeta))
\end{pmatrix}
$$

and similarly, another polynomial $W_{\zeta\omega}(X)$ as follows.

$$
W_{\zeta\omega}(X) = \frac{1}{X - \zeta\omega} \begin{pmatrix}
\tilde{z}(X) - \tilde{z}(\zeta\omega) \\
v(\tilde{w}_1(X) - \tilde{w}_1(\zeta\omega)) \\
v^2(\tilde{w}_2(X) - \tilde{w}_2(\zeta\omega)) \\
v^3(\tilde{w}_3(X) - \tilde{w}_3(\zeta\omega))
\end{pmatrix}
$$

We commit these two polynomials as $cm_\zeta$ and $cm_{\zeta\omega}$.

Step 11: output the full proof. After the Fiat-Shamir transform, the final proof reads as follows.

$$
\begin{pmatrix}
\text{cm}_{w_1}, \text{cm}_{w_2}, \text{cm}_{w_3}, \text{cm}_{w_4}, \text{cm}_{w_0}, \\
\text{cm}_z, \\
\text{cm}_{t_1}, \text{cm}_{t_2}, \text{cm}_{t_3}, \text{cm}_{t_4}, \text{cm}_{t_5}, \\
\tilde{w}_1(\zeta), \tilde{w}_2(\zeta), \tilde{w}_3(\zeta), \tilde{w}_4(\zeta), \tilde{w}_5(\zeta), \\
S_{\sigma_1}(\zeta), S_{\sigma_2}(\zeta), S_{\sigma_3}(\zeta), S_{\sigma_4}(\zeta), \\
q_{prk3}(\zeta), q_{prk4}(\zeta), \tilde{w}_1(\zeta\omega), \tilde{w}_2(\zeta\omega), \tilde{w}_3(\zeta\omega), \\
\tilde{z}(\zeta\omega), \\
\text{cm}_\zeta, \text{cm}_{\zeta\omega}
\end{pmatrix}
$$

6.3 Verifier

The verifier reads the full proof above and proceeds as follows.

Step 1: compute all the challenges. For convenience, the verifier first computes all the random challenges in this protocol execution: $\beta$, $\gamma$, $\alpha$, $\zeta$, $v$, and $u$.

- Initialize the same cryptographic sponge.
- Put $\text{cm}_{w_1}, \text{cm}_{w_2}, \text{cm}_{w_3}, \text{cm}_{w_4}, \text{cm}_{w_0}$ into the sponge and squeeze out $\beta, \gamma \in \mathbb{F}$ from the sponge.
• Put $cm_1$ into the sponge and squeeze out $\alpha \in \mathbb{F}$ from the sponge.
• Put $cm_{t1}, cm_{t2}, cm_{t3}, cm_{t4}, cm_{t5}$ into the sponge and squeeze out $\zeta \in \mathbb{F}$ from the sponge.
• Put $\tilde{w}_1(\zeta), \tilde{w}_2(\zeta), \tilde{w}_3(\zeta), \tilde{w}_4(\zeta), \tilde{w}_5(\zeta), S_{\sigma 1}(\zeta), S_{\sigma 2}(\zeta), S_{\sigma 3}(\zeta), S_{\sigma 4}(\zeta), q_{prk3}(\zeta), q_{prk4}(\zeta), \tilde{w}_1(\zeta \omega), \tilde{w}_2(\zeta \omega), \tilde{w}_3(\zeta \omega), \tilde{z}(\zeta \omega)$ into the sponge and squeeze out $v \in \mathbb{F}$ from the sponge.
• Put $cm_{\zeta}$ and $cm_{\zeta \omega}$ into the sponge and squeeze out $u \in \mathbb{F}$ from the sponge.

Step 2: compute $r(\zeta)$. Recall from the Step 9 of the prover, $r(\zeta)$ can indeed be computed by the verifier.

$$r(\zeta) = -PI(\zeta) + \alpha \cdot (\tilde{w}_1(\zeta) + \beta S_{\sigma 1}(\zeta \gamma) \cdot (\tilde{w}_2(\zeta) + \beta S_{\sigma 2}(\zeta \gamma) \cdot (\tilde{w}_3(\zeta) + \beta S_{\sigma 3}(\zeta \gamma) \cdot (\tilde{w}_4(\zeta) + \beta S_{\sigma 4}(\zeta \gamma) \cdot (\tilde{w}_5(\zeta) + \beta S_{\sigma 5}(\zeta \gamma) \cdot (\tilde{w}_6(\zeta) + \beta S_{\sigma 6}(\zeta \gamma) \cdot (\tilde{w}_7(\zeta) + \beta S_{\sigma 7}(\zeta \gamma) \cdot \tilde{z}(\zeta \omega) + \alpha^2 L_1(\zeta)$$

$$- \alpha^6 \cdot q_{prk3}(\zeta) \begin{pmatrix} (\tilde{w}_4(\zeta) + g \cdot \tilde{w}_5(\zeta) + q_{prk3}(\zeta) - \tilde{w}_3(\zeta \omega))^5 \\
+ g \cdot (\tilde{w}_4(\zeta) + g \cdot \tilde{w}_5(\zeta) + q_{prk3}(\zeta))^2 \\
- (\tilde{w}_1(\zeta) + g \cdot \tilde{w}_2(\zeta)) \end{pmatrix}$$

$$- \alpha^7 \cdot q_{prk3}(\zeta) \begin{pmatrix} (g \cdot \tilde{w}_4(\zeta) + (g^2 + 1) \cdot \tilde{w}_3(\zeta) + q_{prk4}(\zeta) - \tilde{w}_5(\zeta))^5 \\
+ g \cdot (g \cdot \tilde{w}_4(\zeta) + (g^2 + 1) \cdot \tilde{w}_3(\zeta) + q_{prk4}(\zeta))^2 \\
- (g \cdot \tilde{w}_1(\zeta) + (g^2 + 1) \cdot \tilde{w}_2(\zeta)) \end{pmatrix}$$

$$- \alpha^8 \cdot q_{prk3}(\zeta) \begin{pmatrix} (\tilde{w}_4(\zeta) + g \cdot \tilde{w}_5(\zeta) + q_{prk3}(\zeta) - \tilde{w}_3(\zeta \omega))^5 \\
+ g \cdot (\tilde{w}_3(\zeta))^2 + g^{-1} - \tilde{w}_1(\zeta \omega) \end{pmatrix}$$

$$- \alpha^9 \cdot q_{prk3}(\zeta) \begin{pmatrix} (g \cdot \tilde{w}_4(\zeta) + (g^2 + 1) \cdot \tilde{w}_3(\zeta) + q_{prk4}(\zeta) - \tilde{w}_5(\zeta))^5 \\
+ g \cdot (\tilde{w}_5(\zeta))^2 + g^{-1} - \tilde{w}_2(\zeta \omega) \end{pmatrix}$$

Step 3: assemble $cm_r$. The verifier can also assemble the commitment of $r(X)$ from available commitments, as follows.

$$cm_{r} = cm_{sat} + cm_{r1} \cdot \alpha - cm_{r2} \cdot \alpha + cm_{r3} \cdot \alpha^2$$

$$+ Z_{H}(\zeta) \cdot (cm_{t1} + \zeta^{n+2} \cdot cm_{t2} + \zeta^{2(n+2)} \cdot cm_{t3} + \zeta^{3(n+2)} \cdot cm_{t4} + \zeta^{4(n+2)} \cdot cm_{t5})$$

$$+ cm_{b} \cdot (\tilde{w}_2(\zeta) - 1) \cdot \alpha^3 + \tilde{w}_3(\zeta) \cdot (\tilde{w}_3(\zeta) - 1) \cdot \alpha^4 + \tilde{w}_4(\zeta) \cdot (\tilde{w}_4(\zeta) - 1) \cdot \alpha^5$$

$$+ cm_{prk1} \cdot q_{prk3}(\zeta) \cdot \alpha^6 + cm_{prk2} \cdot q_{prk3}(\zeta) \cdot \alpha^7$$

where

$$cm_{sat} = \tilde{w}_1(\zeta) \cdot cm_{q1} + \tilde{w}_2(\zeta) \cdot cm_{q2} + \tilde{w}_3(\zeta) \cdot cm_{q3} + \tilde{w}_4(\zeta) \cdot cm_{q4}$$

$$+ \tilde{w}_1(\zeta) \cdot cm_{m1} + \tilde{w}_2(\zeta) \cdot cm_{m2} + cm_{qc}$$

$$+ \tilde{w}_1(\zeta) \cdot \tilde{w}_2(\zeta) \cdot \tilde{w}_3(\zeta) \cdot \tilde{w}_4(\zeta) \cdot \tilde{w}_5(\zeta) \cdot \tilde{w}_6(\zeta) \cdot \tilde{w}_7(\zeta) \cdot \tilde{z}(\zeta \omega) \cdot cm_{ecc}$$

$$- \tilde{w}_o(\zeta) \cdot cm_o$$

and

$$cm_{r1} = (\tilde{w}_1(\zeta) + \beta \cdot \zeta + \gamma) \cdot (\tilde{w}_2(\zeta) + \beta \cdot k_1 \cdot \zeta + \gamma) \cdot (\tilde{w}_3(\zeta) + \beta \cdot k_2 \cdot \zeta + \gamma) \cdot (\tilde{w}_4(\zeta) + \beta \cdot k_3 \cdot \zeta + \gamma) \cdot (\tilde{w}_5(\zeta) + \beta \cdot k_4 \cdot \zeta + \gamma) \cdot cm_z$$

35
and
\[ \text{cm}_{r2} = (\overline{w}_1(\zeta) + \beta S_{\sigma_1}(\zeta) + \gamma) \cdot (\overline{w}_2(\zeta) + \beta S_{\sigma_2}(\zeta) + \gamma) \cdot (\overline{w}_3(\zeta) + \beta S_{\sigma_3}(\zeta) + \gamma) \cdot (\overline{w}_4(\zeta) + \beta S_{\sigma_4}(\zeta) + \gamma) \cdot \beta \cdot \overline{z}(\omega) \cdot \text{cm}_{\sigma o} \]

and
\[ \text{cm}_{r3} = L_1(\zeta) \cdot \text{cm}_z \]

**Step 4: compute linear combination of commitments.** Let the cumulative commitment \text{cm} be as follows. Note that the last one has a coefficient \( u \).

\[
\text{cm} = \text{cm}_{w1} + v \cdot \text{cm}_{w2} + v^2 \cdot \text{cm}_{w3} + v^3 \cdot \text{cm}_{w4} + v^4 \cdot \text{cm}_{wo} + v^5 \cdot \text{cm}_{\sigma1} + v^6 \cdot \text{cm}_{\sigma2} + v^7 \cdot \text{cm}_{\sigma3} + v^8 \cdot \text{cm}_{\sigma4} + v^9 \cdot \text{cm}_{prk3} + v^{10} \cdot \text{cm}_{prk4} + v^{11} \cdot \text{cm}_r + u \cdot (\text{cm}_{z} + v \cdot \text{cm}_{w1} + v^2 \cdot \text{cm}_{w2} + v^3 \cdot \text{cm}_{w3})
\]

**Step 5: compute linear combination of evaluations.** Let the cumulative evaluation \( s \) be as follows. Also, note the last one.

\[
s = \overline{w}_1(\zeta) + v \cdot \overline{w}_2(\zeta) + v^2 \cdot \overline{w}_3(\zeta) + v^3 \cdot \overline{w}_4(\zeta) + v^4 \cdot \overline{w}_o(\zeta) + v^5 \cdot S_{\sigma1}(\zeta) + v^6 \cdot S_{\sigma2}(\zeta) + v^7 \cdot S_{\sigma3}(\zeta) + v^8 \cdot S_{\sigma4}(\zeta) + v^9 \cdot q_{prk3}(\zeta) + v^{10} \cdot q_{prk4}(\zeta) + v^{11} \cdot r(\zeta) + u \cdot (\overline{z}(\omega) + v \cdot \overline{w}_1(\zeta \omega) + v^2 \cdot \overline{w}_2(\zeta \omega) + v^3 \cdot \overline{w}_3(\zeta \omega))
\]

**Step 6: pairing.** Compute \( L, R \) as follows.

\[
L = e((\text{cm}_{\zeta} + u \cdot \text{cm}_{\zeta \omega}), x \cdot H)
\]

where \( H \) is the generator of \( \mathbb{G}_2 \) in the SRS and \( x \) is the secret in the SRS. The element \( x \cdot H \) here is part of the SRS.

\[
R = e((\zeta \cdot \text{cm}_{\zeta} + u \cdot \zeta \cdot \omega \cdot \text{cm}_{\zeta \omega} + \text{cm} - s \cdot G), H)
\]

where \( G \) is the generator of \( \mathbb{G}_1 \) in the SRS.

**Step 7: decision.** The verifier accepts the proof if \( L = R \) and rejects otherwise.
7 Design and implementation

In this section we present an application of the Anemoi hash function and the proof system that we have constructed so far. The application that we present is an anonymous privacy token transfer, which is based on the Zerocash construction [Ben+14]. We focus on the Merkle tree membership proof that is needed in zk-SNARK for the user to claim the ownership of an unspent token. The code for this anonymous privacy token transfer can be found in the Noah library.

https://github.com/FindoraNetwork/noah/

7.1 3-ary Merkle tree

Coin commitments in Noah are organized into a 3-ary Merkle tree of depth 20, which can store about $2^{32}$ coin commitments. All the coin commitments are at the leaf level. The internal node’s value is the hash of their left child, middle child, and right child, which is:

$$h = H \text{ (left hash } \parallel \text{ middle hash } \parallel \text{ right hash)}$$

As can be seen, we only need a 3-to-1 compression function here. We implement this with the Jive CRH mentioned in Section 2 with $\ell = 2$. Note that $\ell = 2$ gives us actually a compression function that is 4-to-1. We could have considered a 4-ary tree, but we find the improvement to be limited, and there is an additional cost for the user to retrieve the Merkle tree path. Given that we will only do 3-to-1, we will use a constant for the 4th input into the Jive CRH, for domain separation.

7.2 Domain separation in the Jive CRH

The Anemoi hash function uses the first 200 digits of $\pi$ to generate the round keys. We apply the opened Flystel structure to the next 200 digits of $\pi$ and generate 20 padding constants, which will be used as the 4th input to the Jive CRH. For the $i$-th level of the Merkle tree, the $i$-th padding constant will be used during the compression.

We are aware that such a domain separation is not actually necessary in theory, as one can construct a Merkle tree using the CRH without such domain separation. We choose to do this because we feel that leaving the 4th input zeroes is, although okay theoretically, usually not a good design.

Relation with salts and peppers. The padding constants in the domain separation may look like salts or peppers. Salts and peppers refer to a random string that is hashed together with a user’s password, in an effort to make it difficult to create a rainbow table. The difference between salts and peppers is that the salts are stored along with the hash, while the peppers are stored in a private location. Our padding constants here are not salts and peppers, as they are the same for the Merkle tree, rather than generated randomly every time a new coin commitment is inserted.

7.3 Concrete instantiation for the Jive CRH

We present the details of the constraint system for the Jive CRH.
• Let the current state be \( (x[1], x[2], y[1], y[2]) \) and the next state be \( (x'[1], x'[2], y'[1], y'[2]) \) (immediately after the S-box)
• Set \( x[1], x[2], y[1] \) to be the input
• Set \( y[2] \) to be the salt
• **1st round:** Create a gate with \( w_1 = x[1], w_2 = x[2], w_3 = y[1], w_4 = y[2], \) and \( w_o = y'[2] \)
  – Apply the Anemoi hash function constraint to this gate, by setting:
    
    \[
    q_{prk1} = prk_1, \quad q_{prk2} = prk_2, \quad q_{prk3} = prk_3, \quad q_{prk4} = prk_4
    \]
  – Enforce \( w_4 = \text{salt} \) by setting:
    
    \[
    q_1 = 0, \quad q_2 = 0, \quad q_3 = 0, \quad q_4 = 1, \quad q_m1 = 0, \quad q_m2 = 0, \quad q_c = -\text{salt}, \quad PI = 0, \quad q_{ecc} = 0
    \]
• **each of the 2nd to 12nd rounds:** Let \( x[1], x[2], y[1], y[2], x'[1], x'[2], y'[1], y'[2] \) progress to the next round. Create a gate with \( w_1 = x[1], w_2 = x[2], w_3 = y[1], w_4 = y[2], \) and \( w_o = y'[2] \)
  – Apply the Anemoi hash function constraint to this gate, by setting:
    
    \[
    q_{prk1} = prk_1, \quad q_{prk2} = prk_2, \quad q_{prk3} = prk_3, \quad q_{prk4} = prk_4
    \]
  – Suppress the rest of the constraints by setting:
    
    \[
    q_1 = 0, \quad q_2 = 0, \quad q_3 = 0, \quad q_4 = 0, \quad q_m1 = 0, \quad q_m2 = 0, \quad q_c = 0, \quad PI = 0, \quad q_{ecc} = 0
    \]
• **sum of the output:** Let \( x[1], x[2], y[1], \) and \( y[2] \) be the final state. The sum of the output of the permutation can be calculated by creating a gate with \( w_1 = x[1], w_2 = x[2], w_3 = y[1], w_4 = y[2], \) and \( w_o \) be a variable that stores the supposed sum of the output, denoted by output-sum
  – Suppress the Anemoi hash function constraint, by setting:
    
    \[
    q_{prk1} = q_{prk2} = q_{prk3} = q_{prk4} = 0
    \]
  – Enforce that output-sum is the sum of \( x[1], x[2], y[1], \) and \( y[2] \) by setting:
    
    \[
    q_1 = 2g, \quad q_2 = g^2 + g + 1, \quad q_3 = g^2 + g + 1, \quad q_4 = 2g, \quad q_m1 = 0, \quad q_m2 = 0, \quad q_c = 0, \quad PI = 0, \quad q_{ecc} = 0
    \]
• **sum of the input and output:** Note that the output of the Jive CRH is the sum of the input and the output. Let \( x[1], x[2], y[1] \) be the input again. Create a gate with \( w_1 = x[1], w_2 = x[2], w_3 = x[3], w_4 = \text{output-sum}, \) and \( w_o \) be the Jive CRH output, denoted by result
– Suppress the Anemoi hash function constraint, by setting:
\[
q_{prk1} = q_{prk2} = q_{prk3} = q_{prk4} = 0
\]
– Enforce that result is the sum of \( x[1], x[2], y[1], \) output-sum, and the salt by setting:
\[
\begin{align*}
q_1 &= 1 & q_2 &= 1 & q_3 &= 1 \\
q_4 &= 1 & q_{m1} &= 0 & q_{m2} &= 0 \\
q_c &= \text{salt} & PI &= 0 & q_{ecc} &= 0 \\
q_o &= 1 & q_b &= 0
\end{align*}
\]
• The Jive CRH output is the result variable
Note that in the construction above, an invocation of the Jive CRH takes 14 gates.

### 7.4 Concrete instantiation for the Merkle tree membership proof

We now describe how to verify a Merkle tree membership proof in zk-SNARK, given a leaf, the path, and a root hash.

• Compute the leaf hash by applying the Anemoi variable-length hash function (for which we omit the detail here) to the leaf
• Going up in the tree path, we compute three variables: is-left-child, is-mid-child is-right-child
  – if the present node is the left child of its parent
    * is-left-child = 1, is-mid-child = 0, is-right-child = 0
  – if the present node is the middle child of its parent
    * is-left-child = 0, is-mid-child = 1, is-right-child = 0
  – if the present node is the right child of its parent
    * is-left-child = 0, is-mid-child = 0, is-right-child = 1
• Check that is-left-child, is-mid-child, and is-right-child are binary, and only one of them is one, by creating a gate with \( w_1 = \phi, w_2 = \text{is-left-child}, w_3 = \text{is-mid-child}, w_4 = \text{is-right-child}, \) and \( w_o = \phi, \) where \( \phi \) is a zero variable, and setting:
  – Suppress the Anemoi hash function constraint, by setting:
\[
q_{prk1} = q_{prk2} = q_{prk3} = q_{prk4} = 0
\]
  – Enforce the desired relation by setting:
\[
\begin{align*}
q_1 &= 0 & q_2 &= 1 & q_3 &= 1 \\
q_4 &= 1 & q_{m1} &= 0 & q_{m2} &= 0 \\
q_c &= -1 & PI &= 0 & q_{ecc} &= 0 \\
q_o &= 0 & q_b &= 1
\end{align*}
\]
• In each layer, given the left child hash $h_{\text{left}}$, the middle child hash $h_{\text{mid}}$, and the right child hash $h_{\text{right}}$, we check if the present node (whose hash is $h_{\text{node}}$) is in the correct location by creating two gates, as follows:
  – The first gate has $w_1 = h_{\text{left}}$, $w_2 = \text{is-left-child}$, $w_3 = h_{\text{mid}}$, $w_4 = \text{is-mid-child}$, and $w_o$ be $h_{\text{left}}$ if is-left-child, $h_{\text{mid}}$ if is-mid-child, and zero if is-right-child. We use partial-sum to denote $w_o$ here. Suppress the Anemoi constraint and enforce the desired relation by setting:

$$
q_{\text{prk}1} = q_{\text{prk}2} = q_{\text{prk}3} = q_{\text{prk}4} = 0
$$

$$
q_1 = 0 \quad q_2 = 0 \quad q_3 = 0
$$

$$
q_4 = 0 \quad q_{m1} = 1 \quad q_{m2} = 1
$$

$$
q_o = 1 \quad q_b = 0
$$

– The second gate has $w_1 = h_{\text{right}}$, $w_2 = \text{is-right-child}$, $w_3 = \text{partial-sum}$, $w_4 = \psi$, and $w_o = h_{\text{node}}$. Suppress the Anemoi constraint and enforce the desired relation by setting:

$$
q_{\text{prk}1} = q_{\text{prk}2} = q_{\text{prk}3} = q_{\text{prk}4} = 0
$$

$$
q_1 = 0 \quad q_2 = 0 \quad q_3 = 1
$$

$$
q_4 = 0 \quad q_{m1} = 1 \quad q_{m2} = 0
$$

$$
q_c = 0 \quad PI = 0 \quad q_{ecc} = 0
$$

$$
q_o = 1 \quad q_b = 0
$$

• Use the Jive CRH to check the hashes layer by layer
8 Evaluation

In this section we present the results from our experiments of the previous construction (which uses Rescue and a TurboPlonk implementation tailored for Rescue) and the new construction (which uses Anemoi and a TurboPlonk implementation tailored for Anemoi).

8.1 Experiment setup

We run our experiment on a c5.xlarge instance on AWS, which has 4 virtual CPU cores and 8GB of memory, with an all core Turbo frequency of up to 3.4GHz. The operating system is Ubuntu 22.04, and the Rust version is 1.64.0.

8.2 Cost of a Merkle tree

We count the number of constraints in TurboPlonk and present the results in Table 1. Note that the end-to-end performance is mostly dependent on the number of gates, as multiscalar multiplication (MSM) is the dominating cost, and MSM depends mostly on the number of gates, even though the old and new proof systems have other differences.

<table>
<thead>
<tr>
<th>hash function</th>
<th>Old</th>
<th>New</th>
</tr>
</thead>
<tbody>
<tr>
<td>Rescue</td>
<td>3248</td>
<td>353</td>
</tr>
</tbody>
</table>

Table 1: Number of gates in the corresponding TurboPlonk constraint systems.

8.3 Cost of an end-to-end privacy token transfer

To show how the use of the Anemoi hash function benefits a concrete application, we measure the overhead of an end-to-end privacy token transfer with the Rescue hash function (and a corresponding TurboPlonk system) and with the Anemoi hash function (and the corresponding system).

<table>
<thead>
<tr>
<th>number of gates</th>
<th>2-in 2-out</th>
<th>4-in 4-out</th>
<th>6-in 6-out</th>
<th>2-in 2-out</th>
<th>4-in 4-out</th>
<th>6-in 6-out</th>
</tr>
</thead>
<tbody>
<tr>
<td>Old</td>
<td>11589</td>
<td>20317</td>
<td>29245</td>
<td>3895</td>
<td>5323</td>
<td>6951</td>
</tr>
<tr>
<td>New</td>
<td>1368</td>
<td>1368</td>
<td>1368</td>
<td>1.41</td>
<td>2.55</td>
<td>2.68</td>
</tr>
</tbody>
</table>

Table 2: Cost of an end-to-end privacy token transfer.

We present the results in Table 2, and we can see that the Anemoi hash function can reduce the number of gates, indexer time, reindexer time, prover time, and verifier time, but with a slightly
larger proof. Readers may wonder why the 2-in 2-out privacy token transfer still has three thousand gates. This is due to a delegated Schnorr protocol in Findora’s Zerocash construction, which performs some nonnative field arithmetics in order to support secp256k1 keys for anonymous transfer (and soon, ed25519 keys as well). Nevertheless, we see that the number of additional gates due to an increase in the number of inputs goes down significantly, allowing us to support 6-in 6-out with an SRS of length only 8192.
9 Reference materials

In this section we list the reference materials we found useful when working on this note.

SNARK-friendly hash functions. Cryptographically secure hash functions have become increasingly researched in the past decade. By applying well-studied techniques and designs from hash functions over a characteristic-2 field (e.g., SHA3) to a prime field, there have been many SNARK-friendly hash functions. To list a few, we have MiMC [AGRRT16], Poseidon [GKRRS21], Rescue [AABSDS20], and Anemoi [Bou+22]. These SNARK-friendly hash functions commonly have two applications in zk-SNARK: one is to serve as a collision-resistant hash function to construct a Merkle tree, and another one is to instantiate the Fiat-Shamir sponge used for proof recursion.

TurboPlonk variants. Since the invention of TurboPlonk [Tur], we have seen many new variants being deployed in the industry, and they have contributed to our knowledge of efficient proof systems. This includes Matter Labs’s Franklin crypto library [Fra], Polygon Zero (formerly, Mir)’s Plonky2 [Plo], Dusk Network’s PLONK [Dus], and most notably Zcash’s Halo2 [Hal]. Some of these libraries also use lookup arguments, and TurboPlonk proof systems with lookup are often referred to as UltraPlonk.

Verifiable data structures. There are four common types of verifiable data structures used in zk-SNARK: (1) Merkle tree [Mer87], (2) RSA accumulators [BM93; LLX07; BBF19], (3) offline memory checking [BEGKN91; CDDGS03; SAGL18; Set20], and (4) table lookup [ZBKMNS22; PK22; GK22]. There are pros and cons between these verifiable data structures, and their asymptotic efficiency varies. In practice, we consider the concrete efficiency, and this paper suggests that the Anemoi hash function, as a SNARK-friendly hash function, may have significantly helped the Merkle tree in this competition.
Acknowledgments

We had a discussion with the authors of [Bou+22] and learned a lot about Anemoi. We would like to thank Rami Akeela, Daira Hopwood, Yuncong Hu, Dmitry Khovratovich, Pratyush Mishra, Robin Salen, Markus Schofnegger, Danny Willems, and Zhenfei Zhang for discussions that eventually help make this work. We would also like to thank the researchers who have pioneered the study of SNARK-friendly hash functions, despite the fact that it is known among researchers that these constructive cryptanalysis papers have a harder time in the peer-review process due to biased opinions. Nevertheless, we believe that the adoption of these hash functions in the blockchain industry and the important applications that they have demonstrated their values, and it is now only a matter of time.
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