HyperPlonk: Plonk with Linear-Time Prover and High-Degree Custom Gates

Binyi Chen  
Espresso Systems  

Benedikt Bünz  
Stanford University,  
Espresso Systems  

Dan Boneh  
Stanford University  

Zhenfei Zhang  
Espresso Systems

January 9, 2023

Abstract

Plonk is a widely used succinct non-interactive proof system that uses univariate polynomial commitments. Plonk is quite flexible: it supports circuits with low-degree “custom” gates as well as circuits with lookup gates (a lookup gate ensures that its input is contained in a predefined table). For large circuits, the bottleneck in generating a Plonk proof is the need for computing a large FFT.

We present HyperPlonk, an adaptation of Plonk to the boolean hypercube, using multilinear polynomial commitments. HyperPlonk retains the flexibility of Plonk but provides several additional benefits. First, it avoids the need for an FFT during proof generation. Second, and more importantly, it supports custom gates of much higher degree than Plonk without harming the running time of the prover. Both of these can dramatically speed up the prover’s running time. Since HyperPlonk relies on multilinear polynomial commitments, we revisit two elegant constructions: one from Orion and one from Virgo. We show how to reduce the Orion opening proof size to less than 10kb (an almost factor 1000 improvement) and show how to make the Virgo FRI-based opening proof simpler and shorter.
# Contents

1 Introduction  
   1.1 Technical overview ........................................... 6  
   1.2 Additional related work ....................................... 10  

2 Preliminaries  
   2.1 Proofs and arguments of knowledge. ........................... 11  
   2.2 Multilinear polynomial commitments. ........................ 13  
   2.3 PIOP compilation .............................................. 15  

3 A toolbox for multivariate polynomials  
   3.1 SumCheck PIOP for high degree polynomials ................. 16  
   3.2 ZeroCheck PIOP ............................................... 19  
   3.3 ProductCheck PIOP ........................................... 21  
   3.4 Multiset Check PIOP .......................................... 22  
   3.5 Permutation PIOP .............................................. 24  
   3.6 Another permutation PIOP for small fields ................... 25  
   3.7 Lookup PIOP .................................................. 27  
   3.8 Batch openings ................................................ 30  
      3.8.1 A more efficient batching scheme in a special setting .. 32  

4 HyperPlonk: Plonk on the boolean hypercube  
   4.1 Constraint systems ............................................ 34  
   4.2 The PolyIOP protocol ......................................... 35  

5 HyperPlonk+: HyperPlonk with Lookup Gates  
   5.1 Constraint systems ............................................ 37  
   5.2 The PolyIOP protocol ......................................... 38  

6 Instantiation and evaluation  
   6.1 Implementation ................................................. 40  
   6.2 Evaluation ...................................................... 40  
   6.3 MultiThreading performance ................................... 41  
   6.4 High degree gates .............................................. 42  
   6.5 Comparisons ..................................................... 42  

7 Orion+: a linear-time multilinear PCS with constant proof size  

A Zero Knowledge PIOPs and zk-SNARKs  
   A.1 Definition ..................................................... 59  
   A.2 Polynomial masking ............................................ 59  
   A.3 Zero knowledge SumCheck ...................................... 60  
   A.4 Zero knowledge compilation for SumCheck-based PIOPs ...... 61  
   A.5 zk-SNARKs from PIOPs .......................................... 63  

B The FRI-based multilinear polynomial commitment ............... 63
1 Introduction

Proof systems [49, 6] have a long and rich history in cryptography and complexity theory. In recent years, the efficiency of proof systems has dramatically improved and this has enabled a multitude of new real-world applications that were not previously possible. In this paper, we focus on succinct non-interactive arguments of knowledge, also called SNARKs [16]. Here, succinct refers to the fact that the proof is short and verification time is fast, as explained below. Recent years have seen tremendous progress in improving the efficiency of the prover [75, 62, 79, 2, 12, 84, 35, 27, 44, 68, 24, 50, 80].

Let us briefly review what a (preprocessing) SNARK is. We give a precise definition in Section 2. Fix a finite field \( \mathbb{F} \), and consider the relation \( R(C, x, w) \) that is true whenever \( x \in \mathbb{F}^n \), \( w \in \mathbb{F}^m \), and \( C(x, w) = 0 \), where \( C \) is the description of an arithmetic circuit over \( \mathbb{F} \) that takes \( n + m \) inputs. A SNARK enables a prover \( P \) to non-interactively and succinctly convince a verifier \( V \) that \( P \) knows a witness \( w \in \mathbb{F}^m \) such that \( R(C, x, w) \) holds, for some public circuit \( C \) and \( x \in \mathbb{F}^n \).

In more detail, a SNARK is a tuple of four algorithms \( (\text{Setup}, I, P, V) \), where \( \text{Setup}(1^\lambda) \) is a randomized algorithm that outputs parameters \( gp \), and \( I(gp, C) \) is a deterministic algorithm that pre-processes the circuit \( C \) and outputs prover parameters \( pp \) and verifier parameters \( vp \). The prover \( P(pp, x, w) \) is a randomized algorithm that outputs a proof \( \pi \), and the verifier \( V(vp, x, \pi) \) is a deterministic algorithm that outputs 0 or 1. The SNARK must be complete, knowledge sound, and succinct, as defined in Section 2. Here succinct means that if \( C \) contains \( s \) gates, and \( x \in \mathbb{F}^n \), then the size of the proof should be \( O_\lambda(\log s) \) and the verifier's running time should be \( \tilde{O}_\lambda(n + \log s) \).

A SNARK is often set in the random oracle model where all four algorithms can query the oracle. If the \( \text{Setup} \) algorithm is randomized, then we say that the SNARK requires a trusted setup; otherwise, the SNARK is said to be transparent because \( \text{Setup} \) only has access to public randomness via the random oracle. Optionally, we might want the SNARK to be zero-knowledge, in which case it is called a zkSNARK.

Modern SNARKs are constructed by compiling an information-theoretic object called an Interactive Oracle Proof (IOP) [13] to a SNARK using a suitable cryptographic commitment scheme. There are several examples of this paradigm. Some SNARKs use a univariate polynomial commitment scheme to compile a Polynomial-IOP to a SNARK. Examples include Sonic [62], Marlin [35], and Plonk [44]. Other SNARKs use a multivariate linear (multilinear) commitment scheme to compile a multilinear-IOP to a SNARK. Examples include Hyrax [75], Libra [79], Spartan [68], Quarks [69], and Gemini [24]. Yet other SNARKs use a vector commitment scheme (such as a Merkle tree) to compile a vector-IOP to a SNARK. The STARK system [10] is the prime example in this category, but other examples include Aurora [12], Virgo [84], Brakedown [50], and Orion [80]. While STARKs are post-quantum secure, require no trusted setup, and have an efficient prover, they generate a relatively long proof (tens of kilobytes in practice). The paradigm of compiling an IOP to a SNARK using a suitable commitment scheme lets us build universal SNARKs where a single trusted setup can support many circuits. In earlier SNARKs, such as [52, 47, 18], every circuit required a new trusted setup.

The Plonk system. Among the IOP-based SNARKs that use a Polynomial-IOP, the Plonk system [44] has emerged as one of the most widely adopted in industry. This is because Plonk
Proofs are very short (about 400 bytes in practice) and fast to verify. Moreover, Plonk supports custom gates, as we will see in a minute. An extension of Plonk, called PlonKup [66], further extends Plonk to incorporate lookup gates using the Plookup IOP of [44].

One difficulty with Plonk, compared to some other schemes, is the prover’s complexity. For a circuit $C$ with $s$ arithmetic gates, the Plonk prover runs in time $O(\lambda s \log s)$. The primary bottlenecks come from the fact that the prover must commit to and later open several degree $O(s)$ polynomials. When using the KZG polynomial commitment scheme [56], the prover must (i) compute a multi-exponentiation of size $O(s)$ in a pairing-friendly group where discrete log is hard, and (ii) compute several FFTs and inverse-FFTs of dimension $O(s)$. When using a FRI-based polynomial commitment scheme [9, 57, 84], the prover computes an $O(cs)$-sized FFT and $O(cs)$ hashes, where $1/c$ is the rate of a certain Reed-Solomon code. The performance further degrades for circuits that contain high-degree custom gates, as some FFTs and multi-exponentiations have size proportional to the degree of the custom gates.

In practice, when the circuit size $s$ is bigger than $2^{20}$, the FFTs become a significant part of the running time. This is due to the quasi-linear running time of the FFT algorithm, while other parts of the prover scale linearly in $s$. The reliance on FFT is a direct result of Plonk’s use of univariate polynomials. We note that some proof systems eliminate the need for an FFT by moving away from Plonk altogether [68, 24, 50, 80, 41].

**Hyperplonk.** In this paper, we introduce HyperPlonk, an adaptation of the Plonk IOP and its extensions to operate over the boolean hypercube $B_{\mu} := \{0, 1\}^\mu$. We present HyperPlonk as a multilinear-IOP, which means that it can be compiled using a suitable multilinear commitment scheme to obtain a SNARK (or a zkSNARK) with an efficient prover.

HyperPlonk inherits the flexibility of Plonk to support circuits with custom gates, but presents several additional advantages. First, by moving to the boolean hypercube we eliminate the need for an FFT during proof generation. We do so by making use of the classic SumCheck protocol [61], and this reduces the prover’s running time from $O(\lambda s \log s)$ to $O(\lambda s)$. The efficiency of SumCheck is the reason why many of the existing multilinear SNARKs [75, 79, 68, 69, 24] use the boolean hypercube. Here we show that Plonk can similarly benefit from the SumCheck protocol.

Second, and more importantly, we show that the hypercube lets us incorporate custom gates more efficiently into HyperPlonk. A custom gate is a function $G : \mathbb{F}^{\ell} \rightarrow \mathbb{F}$, for some $\ell$. An arithmetic circuit $C$ with a custom gate $G$, denoted $C[G]$, is a circuit with addition and multiplication gates along with a custom gate $G$ that can appear many times in the circuit. The circuit may contain multiple types of custom gates, but for now, we will restrict to one type to simplify the presentation. These custom gates can greatly reduce the circuit size needed to compute a function, leading to a faster prover. For example, if one needs to implement the S-box in a block cipher, it can be more efficient to implement it as a custom gate.

Custom gates are not free. Let $G : \mathbb{F}^{\ell} \rightarrow \mathbb{F}$ be a custom gate that computes a multivariate polynomial of total degree $d$. Let $C[G]$ be a circuit with a total of $s$ gates. In the Plonk IOP, the circuit $C[G]$ results in a prover that manipulates univariate polynomials of degree $O(s \cdot d)$. Consequently, when compiling Plonk using KZG [56], the prover needs to do a group multi-exponentiation of size $O(sd)$ as well as FFTs of this dimension. This restricts custom gates in Plonk to gates of low degree.

We show that the prover’s work in HyperPlonk is much lower. Let $G : \mathbb{F}^{\ell} \rightarrow \mathbb{F}$ be a custom gate that can be evaluated using $k$ arithmetic operations. In HyperPlonk, the bulk of the prover’s work
when processing $C[G]$ is only $O(sk \log^2 k)$ field operations. Moreover, when using KZG multilinear commitments [65], the total number of group exponentiations is only $O(s + d \log s)$, where $d$ is the total degree of $G$. This is much lower than Plonk’s $O(sd)$ group exponentiations. It lets us use custom gates of much higher degree in HyperPlonk.

Making Plonk and its Plonkup extension work over the hypercube raises interesting challenges, as discussed in Section 1.1. In particular, adapting the Plookup IOP [44], used to implement table lookups, requires changing the protocol to make it work over the hypercube (see Section 3.7). The resulting version of HyperPlonk that supports lookup gates is called HyperPlonk+ and is described in Section 5. There are also subtleties in making HyperPlonk zero knowledge. In Appendix A, we describe a general compiler to transform a multilinear-IOP into one that is zero knowledge.

**Batch openings and commit-and-prove SNARKs.** The prover in HyperPlonk needs to open several multilinear polynomials at random points. We present a new sum-check-based batch-opening protocol (Section 3.8) that can batch many openings into one, significantly reducing the prover time, proof size, and verifier time. Our protocol takes $O(k \cdot 2^\mu)$ field operations for the prover for batching $k$ of $\mu$-variate polynomials, compared to $O(k^2 \mu \cdot 2^\mu)$ for the previously best protocol [73]. Under certain conditions, we also obtain a more efficient batching scheme with complexity $O(2^\mu)$, which yields a very efficient commit-and-prove protocol.

**Improved multilinear commitments.** Since HyperPlonk relies on a multilinear commitment scheme, we revisit two approaches to constructing multilinear commitments and present significant improvements to both.

First, in Section 7 we use our commit-and-prove protocol to improve the Orion multilinear commitment scheme [80]. Orion is highly efficient: the prover time is strictly linear, taking only $O(2^\mu)$ field operations and hashes for a multilinear polynomial in $\mu$ variables (no group exponentiations are used). The proof size is $O(\lambda \mu^2)$ hash and field elements, and the verifier time is proportional to the proof size. In Section 7 we describe Orion+, that has the same prover complexity, but has $O(\mu)$ proof size and $O(\mu)$ verifier time, with good constants. In particular, for security parameter $\lambda = 128$ and $\mu = 25$ the proof size with Orion+ is only about 7 KBs, compared with 5.5 MB with Orion, a nearly 1000x improvement. Using Orion+ in HyperPlonk gives a strictly linear time prover.

Second, in Appendix B, we show how to generically transform a univariate polynomial commitment scheme into a multilinear commitment scheme using the tensor-product univariate Polynomial-IOP from [24]. This yields a new construction for multilinear commitments from FRI [9] by applying the transformation to the univariate FRI-based commitment scheme from [57]. This approach leads to a more efficient FRI-based multilinear commitment scheme compared to the prior construction in [84], which uses recursive techniques. Using this commitment scheme in HyperPlonk gives a quantum-resistant quasilinear-time prover.

**Another permutation PIOP for small fields.** Looking ahead, the HyperPlonk IOP builds upon a linear-time polynomial IOP for the permutation check relation. However, for $\mu$-variate polynomials, the linear-time permutation PIOP has soundness error $O(2^\mu/|F|)$, which limits its usage with polynomial-sized fields [23]. To remedy this, we also propose another permutation PIOP with a much smaller soundness error, that is, $O(\mu^2/|F|)$. The tradeoff is that the PIOP has quasi-linear (rather than linear) prover time.
Table 1: The prover runtime of Hyperplonk, Spartan [68], and Jellyfish Plonk, for popular applications. The first column (next to the column of the applications) shows the number of R1CS constraints for each application. The third column shows the corresponding number of constraints in HyperPlonk+. Note that the Zexe and the Rollup applications are using the BW6-761 curve. For more detail see Section 6.5.

<table>
<thead>
<tr>
<th>Application</th>
<th>$R_{R1CS}$</th>
<th>Spartan</th>
<th>$R_{PLONK+}$</th>
<th>Jellyfish</th>
<th>HyperPlonk</th>
</tr>
</thead>
<tbody>
<tr>
<td>3-to-1 Rescue Hash</td>
<td>288 [1]</td>
<td>422 ms</td>
<td>144 [71]</td>
<td>40 ms</td>
<td>88 ms</td>
</tr>
<tr>
<td>Zexe’s recursive circuit</td>
<td>$2^{22}$ [81]</td>
<td>6 min</td>
<td>$2^{17}$ [81]</td>
<td>13.1s</td>
<td>5.1s</td>
</tr>
<tr>
<td>Rollup of 50 private tx</td>
<td>$2^{20}$ [71]</td>
<td>39 min</td>
<td>$2^{20}$ [71]</td>
<td>110 s</td>
<td>38.2 s</td>
</tr>
</tbody>
</table>

Evaluation results. After applying the optimizations in Appendix C, when instantiated with the pairing-based multilinear commitment scheme of [65], the proof size of Hyperplonk is $\mu + 5$ group elements and $4\mu + 29$ field elements\(^1\). Using BLS12-381 as the pairing group, we obtain 4.7KB proofs for $\mu = 20$ and 5.5KB proofs for $\mu = 25$. For comparison, Kopis [69] and Gemini [24], which also have linear-time provers, report proofs of size 39KB and 18KB respectively for $\mu = 20$. In Table 1 and Table 6 we show that our prototype HyperPlonk implementation outperforms an optimized commercial-strength Plonk system for circuits with more than $2^{14}$ gates. It also shows the effects of PLONK arithmetization compared to R1CS by comparing the prover runtime for several important applications. Hyperplonk outperforms Spartan [68] for these applications by a factor of over 60. We discuss the evaluation further in Section 6.

1.1 Technical overview

In this section we give a high level overview of how to make Plonk and its extensions work over the hypercube. We begin by describing Plonk in a modular way, breaking it down into a sequence of elementary components shown in Figure 1. In Section 3 we show how to instantiate each component over the hypercube.

Some components of Plonk in Figure 1 rely on the simple linear ordering of the elements of a finite cyclic group induced by the powers of a generator. On the hypercube there is no natural simple ordering, and this causes a problem in the Plookup protocol [44] that is used to implement a lookup gate. To address this we modify the Plookup argument in Section 5 to make it work over the hypercube. We give an overview of our approach below.

A review of Plonk. Let us briefly review the Plonk SNARK. Let $C[G]: F^{n+m} \rightarrow F$ be a circuit with a total of $s$ gates, where each gate has fan-in two and can be one of addition, multiplication, or a custom gate $G: F^2 \rightarrow F$. Let $x \in F^n$ be a public input to the circuit. Plonk represents the resulting computation as a sequence of $n + s + 1$ triples\(^2\):

$$\hat{M} := \{(L_i, R_i, O_i) \in F^3\}_{i=0,...,n+s}.$$  

\(^1\)The constants depend linearly on the degree of the custom gates. These numbers are for simple degree 2 arithmetic circuits.

\(^2\)A more general Plonkish arithmetization [83] supports wider tuples, but triples are sufficient here.
This $\hat{M}$ is a matrix with three columns and $n+s+1$ rows. The first $n$ rows encode the $n$ public input; the next $s$ rows represent the left and right inputs and the output for each gate; and the final row enforces that the final output of the circuit is zero. We will see how in a minute.

In basic (univariate) Plonk, the prover encodes the cells of $\hat{M}$ using a cyclic subgroup $\Omega \subseteq \mathbb{F}$ of order $3(n+s+1)$. Specifically, let $\omega \in \Omega$ be a generator. Then the prover interpolates and commits to a polynomial $M \in \mathbb{F}[X]$ such that

$$M(\omega^{3i}) = L_i, \quad M(\omega^{3i+1}) = R_i, \quad M(\omega^{3i+2}) = O_i \quad \text{for} \quad i = 0, \ldots, n+s.$$ 

Now the prover needs to convince the verifier that the committed $M$ encodes a valid computation of the circuit $C$. This is the bulk of Plonk system.

**Hyperplonk.** In HyperPlonk we instead use the boolean hypercube to encode $\hat{M}$. From now on, suppose that $n+s+1$ is a power of two, so that $n+s+1 = 2^\mu$. The prover interpolates and commits to a multilinear polynomial $M \in \mathbb{F}[X^{\mu+2}] = \mathbb{F}[X_1, \ldots, X_{\mu+2}]$ such that

$$M(0,0,\langle i \rangle) = L_i, \quad M(0,1,\langle i \rangle) = R_i, \quad M(1,0,\langle i \rangle) = O_i, \quad \text{for} \quad i = 0, \ldots, n+s. \quad (2)$$

Here $\langle i \rangle$ is the $\mu$-bit binary representation of $i$. Note that a multilinear polynomial on $\mu+2$ variables is defined by a vector of $2^{\mu+2} = 4 \times 2^\mu$ coefficients. Hence, it is always possible to find a multilinear polynomial that satisfies the $3 \times 2^\mu$ constraints in (2). Next, the prover needs to convince the verifier that the committed $M$ encodes a valid computation of the circuit $C$. To do so, we need to adapt Plonk to work over the hypercube.

Let us start with the pre-processing algorithm $I(gp,C)$ that outputs prover and verifier parameters $vp$ and $pp$. The verifier parameters $vp$ encode the circuit $C[G]$ as a commitment to four multilinear polynomials $(S_1, S_2, S_3, \sigma)$, where $S_1, S_2, S_3 \in \mathbb{F}[X^n]$ and $\sigma \in \mathbb{F}[X^{\mu+2}]$. The first three are called selector polynomials and $\sigma$ is called the wiring polynomial. We will see how they are defined in a minute. There is one more auxiliary multilinear polynomial $I \in \mathbb{F}[X^n]$ that encodes the input $x \in \mathbb{F}^n$. This polynomial is defined as $I(\langle i \rangle) = x_i$ for $i = 0, \ldots, n-1$, and is zero on the rest of the boolean cube $B_\mu$. The verifier, on its own, computes a commitment to the polynomial $I$ to ensure that the correct input $x \in \mathbb{F}^n$ is being used in the proof. Computing a commitment to $I$ can be done in time $O_\lambda(n)$, which is within the verifier’s time budget.

With this setup, the Plonk prover $P$ convinces the verifier that the committed $M$ satisfies two polynomial identities:

**The gate identity:** Let $S_1, S_2, S_3 : \mathbb{F}^\mu \rightarrow \{0,1\}$ be the three selector polynomials that the pre-processing algorithm $I(gp,C)$ committed to in $vp$. To prove that all gates were evaluated correctly, the prover convinces the verifier that the following identity holds for all $x \in B_\mu := \{0,1\}^\mu$:

$$0 = S_1(x) \cdot \left( \frac{M(0,0,x)}{L[x]} + \frac{M(0,1,x)}{R[x]} \right) + S_2(x) \cdot \frac{M(0,0,x)}{L[x]} \cdot \frac{M(0,1,x)}{R[x]}$$

$$+ \quad S_3(x) \cdot G \left( \frac{M(0,0,x)}{L[x]}, \frac{M(0,1,x)}{R[x]} \right) - \frac{M(1,0,x)}{O[x]} + I(x) \quad (3)$$

where $[x] = \sum_{i=0}^{\mu-1} x_i 2^i$ is the integer whose binary representation is $x \in B_\mu$. For each $i = 0, \ldots, n+s$, the selector polynomials $S_1, S_2, S_3$ are defined to do the “right” thing:
• for an addition gate: \( S_1(i) = 1, \quad S_2(i) = S_3(i) = 0 \) (so \( O_i = L_i + R_i \ )

• for a multiplication gate: \( S_1(i) = S_3(i) = 0, \quad S_2(i) = 1 \) (so \( O_i = L_i \cdot R_i \ )

• for a \( G \) gate:
\[ S_1(i) = S_2(i) = S_3(i) = 1 \] (so \( O_i = G(L_i, R_i) \ )

• when \( i < n \) or \( i = n + s \):
\[ S_1(i) = S_2(i) = S_3(i) = 0 \] (so \( O_i = I(i) \ ))

The last bullet ensures that \( O_i \) is equal to the \( i \)-th input for \( i = 0, \ldots, n - 1 \), and that the final output of the circuit, \( O_{n+s} \), is equal to zero.

**The wiring identity:** Every wire in the circuit \( C \) induces an equality constraint on two cells in the matrix \( M \). In HyperPlonk, the wiring constraints are captured by a permutation \( \sigma : B_{\mu+2} \to B_{\mu+2} \). The prover needs to convince the verifier that
\[ M(x) = M(\sigma(x)) \quad \text{for all} \quad x \in B_{\mu+2} := \{0, 1\}^{\mu+2}. \] (4)

To do so, the pre-processing algorithm \( I(gp, C) \) commits to a multilinear polynomial \( \sigma : \mathbb{F}^{\mu+2} \to \mathbb{F} \) that satisfies \( \sigma(x) = [\sigma(x)] \) for all \( x \in B_{\mu+2} \) (recall that \([\sigma(x)]\) is the integer whose binary representation is \( \sigma(x) \in B_{\mu+2} \)). The prover then convinces the verifier that the following two sets are equal (both sets are subsets of \( \mathbb{F}^2 \)):
\[ \{ ([x], M(x)) \}_{x \in B_{\mu+2}} = \{ ([\sigma(x)], M(x)) \}_{x \in B_{\mu+2}}. \] (5)

This equality of sets implies that (4) holds.

**Proving the gate identity.** The prover convinces the verifier that the Gate identity holds by proving that the polynomial defined by the right hand side of (3) is zero for all \( x \in B_\mu \). This is done using a ZeroCheck IOP, defined in Section 3.2. If the custom gate \( G \) has total degree \( d \) and there are \( s \) gates in the circuit, then the total degree of the polynomial in (3) is \((d+1)(s+n+1)\) which is about \((d \cdot s)\). If this were a univariate polynomial, as in Plonk, then a ZeroCheck would require a multi-exponentiation of dimension \((d \cdot s)\) and an FFT of the same dimension. When the polynomial is defined over the hypercube, the ZeroCheck is implemented using the SumCheck protocol in Section 3.1, which requires no FFTs. In that section we describe two optimizations to the SumCheck protocol for the settings where the multivariate polynomial has a high degree \( d \) in every variable:

• First, in every round of SumCheck the prover sends a polynomial commitment to a univariate polynomial of degree \( d \), instead of sending the polynomial in the clear as in regular SumCheck. This greatly reduces the proof size.

• Second, in standard SumCheck, the prover opens the univariate polynomial commitment at three points: at 0, 1, and at a random \( r \in \mathbb{F} \). We optimize this step by showing that opening the commitment at a single point is sufficient. This further shortens the final proof.

The key point is that the resulting ZeroCheck requires the prover to do only about \( s + d \cdot \mu \) group exponentiations, which is much smaller than \( d \cdot s \) in Plonk. The additional arithmetic work that the prover needs to do depends on the number of multiplication gates in the circuit implementing the custom gate \( G \), not on the total degree of \( G \), as in Plonk. As such, we can support much larger custom gates than Plonk.

In summary, proof generation time is reduced for two reasons: (i) the elimination of the FFTs, and (ii) the better handling of high-degree custom gates.
Proving the wiring identity. The prover convinces the verifier that the Wiring identity holds by proving the set equality in (5). We describe a set equality protocol over the hypercube in Section 3.4. Briefly, we use a technique from Bayer and Groth [8], that is also used in Plonk, to reduce this problem to a certain ProductCheck over the hypercube (Section 3.3). We then use an idea from Quarks [69] to reduce the hypercube ProductCheck to a ZeroCheck, which then reduces to a SumCheck. This sequence of reductions is shown in Figure 1. Again, no FFTs are needed.

Table lookups. An important extension to Plonk supports circuits with table lookup gates. The table is represented as a fixed vector $t \in \mathbb{F}_2^\mu - 1$. A table lookup gate ensures that a specific cell in the matrix $\hat{M}$ is contained in $t$. For example, one can set $t$ to be the field elements in $\{0, 1, \ldots, B\}$ for some $B$ (padding the vector by 0 as needed). Now, checking that a cell in $\hat{M}$ is contained in $t$ is a simple way to implement a range check.

Let $f, t : B_\mu \to \mathbb{F}$ be two multilinear polynomials. Here the polynomial $t$ encodes the table $t$, where the table values are $t(B_\mu)$. The polynomial $f$ encodes the cells of $\hat{M}$ that need to be checked. An important step in supporting lookup gates in Plonk is a way for the prover to convince the verifier that $f(B_\mu) \subseteq t(B_\mu)$, when the verifier has commitments to $f$ and $t$. The Plookup proof system by Gabizon and Williamson [44] is a way for the prover to do just that. More recently preprocessed alternatives to lookup have been developed [82, 67]. These perform better if the table is known, e.g. a range of values but are in general orthogonal to Plookup.

The problem is that Plookup is designed to work when the polynomials are defined over a cyclic subgroup $G \subseteq \mathbb{F}^*$ of order $q$ with generator $\omega \in G$. In particular, Plookup requires a function $\text{next} : \mathbb{F} \to \mathbb{F}$ that induces an ordering of $G$. This function must satisfy two properties: (i) the sequence

$$\omega, \text{next}(\omega), \text{next}(\text{next}(\omega)), \ldots, \text{next}^{(q-1)}(\omega)$$

should traverse all of $G$, and (ii) the function $\text{next}$ should be a linear function. This is quite easy in a cyclic group: simply define $\text{next}(x) := \omega x$.

To adapt Plookup to the hypercube we need a linear function $\text{next} : \mathbb{F}_\mu \to \mathbb{F}_\mu$ that traverses all of $B_\mu$ as in (6), starting with some element $x_0 \in B_\mu$. However, such an $\mathbb{F}$-linear function
does not exist. Nevertheless, we construct in Section 3.7 a quadratic function from $\mathbb{F}^\mu$ to $\mathbb{F}^\mu$ that traverses $B_\mu$. The function simulates $B_\mu$ using a binary extension and has a beautiful connection to similar techniques used in early PCP work\[15\]. We then show how to linearize the function by modifying some of the building blocks that Plookup uses. This gives an efficient Plookup protocol over the hypercube. Finally, in Section 5 we use this hypercube Plookup protocol to support lookup gates in HyperPlonk. The resulting protocol is called HyperPlonk+.

### 1.2 Additional related work

The origins of SNARKs date back to the work of Kilian \[58\] and Micali \[64\] based on the PCP theorem. Many of the SNARK constructions cited in the previous sections rely on techniques introduced in the proof of the PCP theorem.

Recursive SNARKs \[74\] are an important technique for building a SNARK for a long computation. Early recursive SNARKs \[37, 17, 14, 36\] built a prover for the entire SNARK circuit and then repeatedly used this prover. More recent recursive SNARKs rely on accumulation schemes \[26, 29, 19, 28, 59\] where the bulk of the SNARK verifier runs outside of the prover.

Many practical SNARKs rely on the random oracle model and often use a non-falsifiable assumption. Indeed, a separation result due to Gentry and Wichs \[48\] suggests that a SNARK requires either an idealized model or a non-falsifiable assumption. An interesting recent direction is the construction of batch proofs \[38, 39, 76\] in the standard model from standard assumptions. These give succinct proofs for computations in $\mathcal{P}$, namely succinct proofs for computations that do not rely on a hidden witness. SNARKs give succinct proofs for computations in $\mathcal{NP}$.

### 2 Preliminaries

**Notation:** We use $\lambda$ to denote the security parameter. For $n \in \mathbb{N}$ let $[n]$ be the set $\{1, 2, \ldots, n\}$; for $a, b \in \mathbb{N}$ let $[a, b)$ denote the set $\{a, a+1, \ldots b-1\}$. A function $f(\lambda)$ is poly($\lambda$) if there exists a $c \in \mathbb{N}$ such that $f(\lambda) = O(\lambda^c)$. If for all $c \in \mathbb{N}$, $f(\lambda)$ is $o(\lambda^{-c})$, then $f(\lambda)$ is in negl($\lambda$) and is said to be negligible. A probability that is $1 - \text{negl}(\lambda)$ is overwhelming. We use $\mathbb{F}$ to denote a field of prime order $p$ such that $\log(p) = \Omega(\lambda)$.

A multiset is an extension of the concept of a set where every element has a positive multiplicity. Two finite multisets are equal if they contain the same elements with the same multiplicities.

Recall that a relation is a set of pairs $(x, w)$. An indexed relation is a set of triples $(i, x; w)$. The index $i$ is fixed at setup time.

In defining the syntax of the various protocols, we use the following convention concerning public values (known to both the prover and the verifier) and secret ones (known only to the prover). In any list of arguments or returned tuple $(a, b, c; d, e)$, those variables listed before the semicolon are public, and those listed after it are secret. When there is no secret information, the semicolon is omitted.

**Useful facts. ** We next list some facts that will be used throughout the paper.

**Lemma 2.1 (Multilinear extensions).** For every function $f : \{0, 1\}^\mu \to \mathbb{F}$, there is a unique multilinear polynomial $\tilde{f} \in \mathbb{F}[X_1, \ldots, X_\mu]$ such that $\tilde{f}(b) = f(b)$ for all $b \in \{0, 1\}^\mu$. We call $\tilde{f}$ the
multilinear extension of $f$, and $\bar{f}$ can be expressed as

$$\bar{f}(X) = \sum_{b \in \{0,1\}^\mu} f(b) \cdot eq(b, X)$$

where $eq(b, X) := \prod_{i=1}^\mu (b_i X_i + (1 - b_i)(1 - X_i))$.

**Lemma 2.2** (Schwartz-Zippel Lemma). Let $f \in F[X_1, \ldots, X_\mu]$ be a non-zero polynomial of total degree $d$ over field $F$. Let $S$ be any finite subset of $F$, and let $r_1, \ldots, r_\mu$ be $\mu$ field elements selected independently and uniformly from set $S$. Then

$$\Pr[f(r_1, \ldots, r_\mu) = 0] \leq \frac{d}{|S|}.$$  

**Linear codes.** We review the definition of linear code.

**Definition 2.1** (Linear Code). An $(n,k,\delta)$-linear error-correcting code $E : F^k \to F^n$ is an injective mapping from $F^k$ to a linear subspace $C$ in $F^n$, such that (i) the injective mapping can be computed in linear time in $k$; (ii) any linear combination of codewords is still a codeword; and (iii) the relative hamming distance $\Delta(u,v)$ between any two different codewords $u,v \in F^k$ is at least $\delta$. The rate of the code $E$ is defined as $k/n$.

2.1 Proofs and arguments of knowledge.

We define interactive proofs of knowledge, which consist of a non-interactive preprocessing phase run by an indexer as well as an interactive online phase between a prover and a verifier.

**Definition 2.2** (Interactive Proof and Argument of Knowledge). An interactive protocol $\Pi = (\text{Setup},I,\mathcal{P},\mathcal{V})$ between a prover $\mathcal{P}$ and verifier $\mathcal{V}$ is an argument of knowledge for an indexed relation $R$ with knowledge error $\delta : \mathbb{N} \to [0,1]$ if the following properties hold, where given an index $i$, common input $x$ and prover witness $w$, the deterministic indexer outputs $(vp,pp) \leftarrow I(i)$ and the output of the verifier is denoted by the random variable $\langle \mathcal{P}(pp,x,w),\mathcal{V}(vp,x) \rangle$:

- **Perfect Completeness:** for all $(i,x,w) \in R$
  
  $$\Pr\left[\langle \mathcal{P}(pp,x,w),\mathcal{V}(vp,x) \rangle = 1 \ \bigg| \ (vp,pp) \leftarrow I(gp,i) \right]\right] = 1$$

- **$\delta$-Soundness (adaptive):** Let $\mathcal{L}(R)$ be the language corresponding to the indexed relation $R$ such that $(i,x) \in \mathcal{L}(R)$ if and only if there exists $w$ such that $(i,x,w) \in R$. $\Pi$ is $\delta$-sound if for every pair of probabilistic polynomial time adversarial prover algorithm $(A_1,A_2)$ the following holds:
  
  $$\Pr\left[\langle A_2(i,x,\text{st}),\mathcal{V}(vp,x) \rangle = 1 \wedge (i,x) \notin \mathcal{L}(R) \ \bigg| \ (vp,pp) \leftarrow I(gp,i) \right]\right] \leq \delta(|i| + |x|).$$
We say a protocol is computationally sound if $\delta$ is negligible. If $A_1, A_2$ are unbounded and $\delta$ is negligible, then the protocol is statistically sound. If $A = (A_1, A_2)$ is unbounded, the soundness definition becomes for all $(i, x) \notin L(R)$

$$\Pr \left[ \langle A_2(i, x, gp), V(vp, x) \rangle = 1 \mid \begin{array}{l}
gp \leftarrow \text{Setup}(1^\lambda) \\
(vp, pp) \leftarrow I(gp, i)
\end{array} \right] \leq \delta(|i| + |x|)$$

- **$\delta$-Knowledge Soundness**: There exists a polynomial poly$(\cdot)$ and a probabilistic polynomial-time oracle machine $E$ called the extractor such that given oracle access to any pair of probabilistic polynomial time adversarial prover algorithm $(A_1, A_2)$ the following holds:

$$\Pr \left[ \langle A_2(i, x, st), V(vp, x) \rangle = 1 \land (i, x, w) \notin R \mid \begin{array}{l}
gp \leftarrow \text{Setup}(1^\lambda) \\
(i, x, st) \leftarrow A_1(gp) \\
(vp, pp) \leftarrow I(gp, i) \\
w \leftarrow E^{A_1, A_2}(gp, i)
\end{array} \right] \leq \delta(|i| + |x|)$$

An interactive protocol is “knowledge sound”, or simply an “argument of knowledge”, if the knowledge error $\delta$ is negligible in $\lambda$. If the adversary is unbounded, then the argument is called an interactive proof of knowledge.

- **Public coin** An interactive protocol is considered to be public coin if all of the verifier messages (including the final output) can be computed as a deterministic function given a random public input.

- **Zero knowledge**: An interactive protocol $\langle P, V \rangle$ is considered to be zero-knowledge if there is a PPT simulator $S$ such that for every PPT adversary $A = (A_1, A_2)$, auxiliary input $z \in \{0, 1\}^{poly(\lambda)}$, it holds that

$$\Pr \left[ \langle P(pp, x, w), A_2(st, i, x) \rangle = 1 \land (i, x, w) \in R \mid \begin{array}{l}
gp \leftarrow \text{Setup}(1^\lambda) \\
(i, x, w, st) \leftarrow A_1(z, gp) \\
(vp, pp) \leftarrow I(gp, i)
\end{array} \right] - \Pr \left[ \langle S(\sigma, z, pp, x), A_2(st, i, x) \rangle = 1 \land (i, x, w) \in R \mid \begin{array}{l}
gp \leftarrow \text{Setup}(1^\lambda) \\
(i, x, w, st) \leftarrow A_1(z, gp) \\
(vp, pp) \leftarrow I(gp, i)
\end{array} \right] \leq \negl(\lambda) .$$

We say that $\langle P, V \rangle$ is statistically zero knowledge if $A$ is unbounded; and say it perfectly zero knowledge if $\negl(\lambda)$ is replaced with zero. $\langle P, V \rangle$ is honest-verifier zero knowledge (HVZK) if the adversary $A_2$ honestly follows the verifier algorithm.

We introduce both notions of soundness and knowledge soundness. Knowledge soundness implies soundness, as the existence of an extractor implies that $(i, x) \in L(R)$. Furthermore, we show in Lemma 2.3 that soundness directly implies knowledge soundness for certain oracle relations and oracle arguments.

**PolyIOPs.** SNARKs can be constructed from information-theoretic proof systems that give the verifier oracle access to prover messages. The information-theoretic proof is then compiled using a cryptographic tool, such as a polynomial commitment. We now define a specific type of information-theoretic proof system called polynomial interactive oracle proofs.
Definition 2.3. A polynomial interactive oracle proof (PIOP) is a public-coin interactive proof for a polynomial oracle relation \( R = \{(i, x; w)\} \). The relation is an oracle relation in that \( i, x \) can contain oracles to \( \mu \)-variate polynomials over some field \( \mathbb{F} \). The oracles specify \( \mu \) and the degree in each variable. These oracles can be queried at arbitrary points in \( \mathbb{F}^\mu \) to evaluate the polynomial at these points. The actual polynomials corresponding to the oracles are contained in the \( \mathbb{P} \) and the \( w \), respectively. We denote an oracle to a polynomial \( f \) by \([f]\). In every protocol message, the \( P \) sends multi-variate polynomial oracles. The verifier in every round sends a random challenge.

We measure the following parameters for the complexity of a PIOP:

- The prover time measures the runtime of the prover.
- The verifier time measures the runtime of the verifier.
- The query complexity is the number of queries the verifier performs to the oracles.
- The round complexity measures the number of rounds. In our protocols, it is always equivalent to the number of oracles sent.
- The size of the proof oracles is the length of the transmitted polynomials.
- The size of the witness is the length of the witness polynomial.

Proof of Knowledge. As a proof system, the PIOP satisfies perfect completeness and unbounded knowledge-soundness with knowledge-error \( \delta \). Note that the extractor can query the oracle at arbitrary points to efficiently recover the entire polynomial.

Non-interactive arguments. Interactive public-coin arguments can be made non-interactive using the Fiat-Shamir transform. The Fiat-Shamir transform replaces the verifier challenges with hashes of the transcript up to that point. The works by [5, 78] show that this is secure for multi-round special-sound protocols and multi-round oracle proofs.

Soundness and knowledge soundness.

Lemma 2.3 (Sound PIOPs are knowledge sound). Consider a \( \delta \)-sound PIOP for oracle relations \( R \) such that for all \( (i, x, w) \in R \), \( w \) consists only of polynomials such that the instance contains oracles to these polynomials. The PIOP has \( \delta \) knowledge-soundness, and the extractor runs in time \( O(|w|) \).

Proof. We will show that we can construct an extractor \( E \) that can produce \( w^* \) such that \( (i, x, w^*) \in R \) if and only if \( (i, x) \in L(R) \). This implies that the soundness error exactly matches the knowledge soundness error. For each oracle of a \( \mu \)-variate polynomial with degree \( d \) in each variable, the extractor queries the polynomial at \( (d+1)^\mu \) distinct points to extract the polynomial inside the oracle and thus \( w^* \). If \( (i, x, w^*) \in R \) then by definition \( (i, x) \in L(R) \). Additionally assume that \( (i, x) \in L(R) \) but \( (i, x, w^*) \not\in R \). Then there must exists \( w' \neq w^* \) such that \( (i, x, w') \in R \). Since the relation only admits polynomials as witnesses and these polynomials are degree \( d \) and \( \mu \)-variate, then there cannot be two distinct witnesses that agree on \( (d+1)^\mu \) oracle queries. Therefore \( w' = w^* \) which leads to a contradiction. The extractor, therefore, outputs the unique, valid witness for every \( (i, x) \) in the language, and thus, the soundness and knowledge soundness error are the same.

2.2 Multilinear polynomial commitments.

Definition 2.4 (Commitment scheme). A commitment scheme \( \Gamma \) is a tuple \( \Gamma = (\text{Setup}, \text{Commit}, \text{Open}) \) of PPT algorithms where:
• **Setup**$(1^λ) → gp$ generates public parameters $gp$;

• **Commit**$(gp; x) → (C; r)$ takes a secret message $x$ and outputs a public commitment $C$ and (optionally) a secret opening hint $r$ (which might or might not be the randomness used in the computation).

• **Open**$(gp, C, x, r) → b ∈ \{0, 1\}$ verifies the opening of commitment $C$ to the message $x$ provided with the opening hint $r$.

A commitment scheme $Γ$ is **binding** if for all PPT adversaries $A$

\[
\Pr \left[ b_0 = b_1 \neq 0 \land x_0 \neq x_1 : \begin{array}{c}
gp ← \text{Setup}(1^λ) \\
(C, x_0, x_1, r_0, r_1) ← A(gp) \\
b_0 ← \text{Open}(gp, C, x_0, r_0) \\
b_1 ← \text{Open}(gp, C, x_1, r_1)
\end{array} \right] ≤ \negl(λ)
\]

A commitment scheme $Γ$ is **hiding** if for any polynomial-time adversary $A$

\[
\Pr \left[ b = b' : \begin{array}{c}
gp ← \text{Setup}(1^λ) \\
(C_0; r_0) ← \text{Commit}(gp; x_0) \\
(C_1; r_1) ← \text{Commit}(gp; x_1) \\
b' ← A(gp, st, C_b)
\end{array} \right] = 1/2 = \negl(λ)
\]

If the adversary is unbounded, then we say the commitment is statistically hiding. We additionally define polynomial commitment schemes for multi-variate polynomials.

**Definition 2.5.** (Polynomial commitment) A polynomial commitment scheme is a tuple of protocols $Γ = (\text{Setup}, \text{Commit}, \text{Open}, \text{Eval})$ where $(\text{Setup}, \text{Commit}, \text{Open})$ is a binding commitment scheme for a message space $R[X]$ of polynomials over some ring $R$, and

• **Eval**$( (vp, pp), C, z, y, d, µ; f) → b ∈ \{0, 1\}$ is an interactive public-coin protocol between a PPT prover $P$ and verifier $V$. Both $P$ and $V$ have as input a commitment $C$, points $z \in F^µ$ and $y \in F$, and a degree $d$. The prover has prover parameters $pp$, and the verifier has verifier parameters $vp$. The prover additionally knows the opening of $C$ to a secret polynomial $f ∈ F^{(≤d)}_{µ}$. The protocol convinces the verifier that $f(z) = y$.

A polynomial commitment scheme is **correct** if an honest committer can successfully convince the verifier of any evaluation. Specifically, if the prover is honest, then for all polynomials $f ∈ F^{(≤d)}_{µ}$ and all points $z ∈ F^µ$,

\[
\Pr \left[ b = 1 : \begin{array}{c}
gp ← \text{Setup}(1^λ) \\
(C; r) ← \text{Commit}(gp, f) \\
y ← f(z) \\
b ← \text{Eval}(gp, c, z, y, d, µ; f, r)
\end{array} \right] = 1
\]

We require that $\text{Eval}$ is an interactive argument of knowledge and has knowledge soundness, which ensures that we can extract the committed polynomial from any evaluation.

Multi-variate polynomial commitments can be instantiated from random oracles using the FRI protocol [84], bilinear groups [65], groups of unknown order [30] and discrete logarithm groups. We give a table of polynomial commitments with their different properties in Table 2:
<table>
<thead>
<tr>
<th>Scheme</th>
<th>Prover time: Commit+ Eval</th>
<th>Verifier time: log(n) ( P )</th>
<th>Proof size: ( \log(n) G _1 )</th>
<th>( n = 2^{25} )</th>
<th>Setup</th>
<th>Add.</th>
</tr>
</thead>
<tbody>
<tr>
<td>KZG-based [65]</td>
<td>BL</td>
<td>( n G _1 )</td>
<td>log(n) ( P )</td>
<td>0.8KB</td>
<td>Univ.</td>
<td>Yes</td>
</tr>
<tr>
<td>Dory [60]</td>
<td>BL</td>
<td>( n G _1 + \sqrt{n} P )</td>
<td>( n G _1 )</td>
<td>30KB</td>
<td>Trans.</td>
<td>Yes</td>
</tr>
<tr>
<td>Bulletproofs [27]</td>
<td>DL</td>
<td>( n G )</td>
<td>( n G _1 )</td>
<td>1.6KB</td>
<td>Trans.</td>
<td>Yes</td>
</tr>
<tr>
<td>FRI-based (§B)</td>
<td>RO</td>
<td>( n \log(n)/\rho \mathbb{F} + n/\rho H )</td>
<td>( \log^2(n) H )</td>
<td>0.8KB</td>
<td>Univ.</td>
<td>No</td>
</tr>
<tr>
<td>Orion</td>
<td>RO</td>
<td>( n H + \frac{n}{k} ) k rec.</td>
<td>( \lambda \log^2 n H )</td>
<td>5.5MB</td>
<td>Trans.</td>
<td>No</td>
</tr>
<tr>
<td>Orion + (§7)</td>
<td>BL</td>
<td>( n/k G _1 + n H + (k \lambda H + \frac{n}{k} \mathbb{F}) ) rec.</td>
<td>( 4 \log(n) P )</td>
<td>7KB</td>
<td>Univ.</td>
<td>No</td>
</tr>
</tbody>
</table>

Table 2: Multi-linear polynomial commitment schemes for \( \mu \)-variate linear polynomials and \( n = 2^\mu \). The prover time measures the complexity of committing to a polynomial and evaluating it once. The commitment size is constant for all protocols. Unless constants are mentioned, the metrics are assumed to be asymptotic. In the 4th row, \( \rho \) denotes the rate of Reed-Solomon codes. In the 5th and 6th rows, \( k \) denotes the number of rows of the matrix that represents the polynomial coefficients. The 6th column measures the concrete proof size for \( n = 2^{25} \), i.e. \( \mu = 25 \) and 128-bit security. Legend: BL=Bilinear Group, DL=Discrete Logarithm, RO=Random Oracle, H=Hashes, P=pairings, G=group scalar multiplications, rec.=Recursive circuit size, univ.=universal setup, trans.=transparent setup, Add.=Additive.

**Virtual oracles and commitments.** Given multiple polynomial oracles, we can construct virtual oracles to the functions of these polynomials. An oracle to \( g([f_1], \ldots, [f_k]) \) for some function \( g \) is simply the list of oracles \( \{[f_1], \ldots, [f_k]\} \) as well as a description of \( g \). In order to evaluate \( g([f_1], \ldots, [f_k]) \) at some point \( x \) we compute \( y_i = f_i(x) \forall i \in [k] \) and output \( g(y_1, \ldots, y_k) \). Equivalently, given commitments to polynomials, we can construct a virtual commitment to a function of these polynomials in the same manner. If \( g \) is an additive function and the polynomial commitment is additively homomorphic, then we can use the homomorphism to do the evaluation. A common example is that given additive commitments \( C_f, C_g \) to polynomials \( f(X), g(X) \), we want to construct a commitment to \( (1 - Y)f + Yg \). Then \( (C_f, C_g) \) serves as such a commitment and we can evaluate it at \( (y, x) \) by evaluating \( (1 - y)C_f + y \cdot C_g \) at \( x \).

2.3 PIOP compilation

PIOP compilation transforms the interactive oracle proof into an interactive argument of knowledge (without oracles) \( \Pi \). The compilation replaces the oracles with polynomial commitments. Every query by the verifier is replaced with an invocation of the \textsf{Eval} protocol at the query point \( z \). The compiled verifier accepts if the PIOP verifier accepts and if the output of all \textsf{Eval} invocations is 1. If \( \Pi \) is public-coin, then it can further be compiled to a non-interactive argument of knowledge (or NARK) using the Fiat-Shamir transform.

**Theorem 2.4** (PIOP Compilation [30, 35]). If the polynomial commitment scheme \( \Gamma \) has witness-extended emulation, and if the \( t \)-round Polynomial IOP for \( \mathcal{R} \) has negligible knowledge error, then \( \Pi \), the output of the PIOP compilation, is a secure (non-oracle) argument of knowledge for \( \mathcal{R} \). The compilation also preserves zero knowledge. If \( \Gamma \) is hiding and \textsf{Eval} is honest-verifier zero-knowledge, then \( \Pi \) is honest-verifier zero-knowledge. The efficiency of the resulting argument of knowledge \( \Pi \) depends on the efficiency of both the PIOP and \( \Gamma \).
• **Prover time** The prover time is equal to the sum of (i) prover time of the PIOP, (ii) the oracle length times the commitment time, and (iii) the query complexity times the prover time of \( \Gamma \).

• **Verifier time** The verifier time is equal to the sum of (i) the verifier time of the PIOP and (ii) the verifier time for \( \Gamma \) times the query complexity of the PIOP.

• **Proof size** The proof size is equal to sum of (i) the message complexity of the PIOP times the commitment size and (ii) the query complexity times the proof size of \( \Gamma \). If the proof size is \( O(\log^c(|w|)) \), then we say the proof is succinct.

**Batching.** The prover time, verifier time, and proof size can be significantly reduced using batch openings of the polynomial commitments. After batching, the proof size only depends on the number of oracles plus a single polynomial commitment opening.

### 3 A toolbox for multivariate polynomials

We begin by reviewing several important PolyIOPs that will serve as building blocks for HyperPlonk. Some are well-known, and some are new. Figure 1 serves as a guide for this section: we define the PolyIOPs listed in the figure following the dependency order.

**Notation.** From here on, we let \( B_\mu := \{0, 1\}^\mu \subseteq \mathbb{F}^\mu \) be the boolean hypercube. We use \( \mathcal{F}_\mu^{(\leq d)} \) to denote the set of multivariate polynomials in \( \mathbb{F}[X_1, \ldots, X_\mu] \) where the degree in each variable is at most \( d \); moreover, we require that each polynomial in \( \mathcal{F}_\mu^{(\leq d)} \) can be expressed as a virtual oracle to \( c = O(1) \) multilinear polynomials. that is, with the form \( f(X) := g(h_1(X), \ldots, h_c(X)) \) where \( h_i \in \mathcal{F}_\mu^{(\leq 1)} \) \( (1 \leq i \leq c) \) is multilinear and \( g \) is a \( c \)-variate polynomial of total degree at most \( d \). Looking ahead, we restrict ourselves to this kind of polynomials so that we can have sumchecks for the polynomials with linear-time provers.

For polynomials \( f, g \in \mathcal{F}_\mu^{(\leq d)} \), we denote \( \text{merge}(f, g) \in \mathcal{F}_{\mu+1}^{(\leq d)} \) as

\[
\text{merge}(f, g) := h(X_0, \ldots, X_\mu) := (1 - X_0) \cdot f(X_1, \ldots, X_\mu) + X_0 \cdot g(X_1, \ldots, X_\mu)
\]

so that \( h(0, X) = f(X) \) and \( h(1, X) = g(X) \). In the following definitions, we omit the public parameters \( \gamma_p := (\mathbb{F}, d, \mu) \) when the context is clear. We use \( \delta_{\text{check}}^{ed, \mu} \) to denote the soundness error of the PolyIOP for relation \( R_{\text{check}} \) with public parameter \( (\mathbb{F}, d, \mu) \), where \( \text{check} \in \{\text{sum}, \text{zero}, \text{prod}, \text{mset}, \text{perm}, \text{lkup}\} \).

#### 3.1 SumCheck PIOP for high degree polynomials

In this section, we describe a PIOP for the sumcheck relation using the classic sumcheck protocol [61]. However, we modify the protocol and adapt it to our setting of high-degree polynomials.

**Definition 3.1 (SumCheck relation).** The relation \( R_{\text{SUM}} \) is the set of all tuples \( (x; w) = ((v, [[f]]); f) \) where \( f \in \mathcal{F}_\mu^{(\leq d)} \) and \( \sum_{b \in B_\mu} f(b) = v \).

**Construction.** The classic SumCheck protocol [61] is a PolyIOP for the relation \( R_{\text{SUM}} \). When applying the protocol to a polynomial \( f \in \mathcal{F}_\mu^{(\leq d)} \), the protocol runs in \( \mu \) rounds where in every round, the prover sends a univariate polynomial of degree at most \( d \) to the verifier. The verifier
Sending \( r \) as an oracle. Unlike in the classic sumcheck protocol, we send an oracle to \( r_i \) in each round, instead of the actual polynomial. This does not change the soundness analysis, as the soundness is still proportional to the degree of the univariate polynomials sent in each round. However, it reduces the communication and verifier complexity, especially if the degree of \( r \) is large, as in our application of Hyperplonk with custom gates.

Moreover, the verifier has to evaluate \( r_i \) at three points: 0, 1, and \( \alpha_i \). As a useful optimization, the prover can instead send an oracle for the degree \( d - 2 \) polynomial

\[
r_i'(X) := \frac{r_i(X) - (1 - X) \cdot r_i(0) - X \cdot r_i(1)}{X \cdot (1 - X)},
\]

along with \( r_i(0) \). The verifier then computes \( r_i(1) \leftarrow v - r_i(0) \) and

\[
r_i(\alpha_i) \leftarrow r_i'(\alpha) \cdot (1 - \alpha_i) \cdot \alpha_i + (1 - \alpha_i) \cdot r_i(0) + \alpha_i \cdot r_i(1).
\]

This requires only one query to the oracle of \( r_i' \) at \( \alpha_i \) and one field element per round.
Computing sumcheck for high-degree polynomials. Consider a multi-variate polynomial \( f(X) := h(g_1(X), \ldots, g_c(X)) \) such that \( h \) is degree \( d \) and can be evaluated through an arithmetic circuit with \( O(d) \) gates. In the sumcheck protocol, the prover has to compute a univariate polynomial \( r_i(X) \) in each round using the previous verifier messages \( \alpha_1, \ldots, \alpha_{i-1} \). We adapt the algorithm by \([72,79]\) that showed how the sumcheck prover can be run in time linear in \( 2^\mu \) using dynamic programming. The algorithm takes as input a description of \( f \) as well as the sumcheck round challenges \( \alpha_1, \ldots, \alpha_\mu \). It outputs the round polynomials \( r_1, \ldots, r_\mu \). The sumcheck prover runs the algorithm in parallel to the sumcheck protocol, taking each computed \( r_i \) as that rounds message:

Algorithm 1 Computing \( r_1, \ldots, r_\mu \) \([72,79]\)

1: procedure \textsc{Sumcheck prover} \((h, g_1(X), \ldots, g_c(X))\)
2: For each \( g_j \) build table \( A_j : \{0,1\}^\mu \rightarrow \mathbb{F} \) of all evaluations over \( B_\mu \)
3: for \( i \leftarrow \mu \ldots 1 \) do
4: For each \( b \in B_{i-1} \) and each \( j \in [c] \), define \( r^{(j,b)}(X) := (1-X)A_j[b,0] + XA_j[b,1] \).
5: Compute \( r^{(b)}(X) := h(r^{(1,b)}(X), \ldots, r^{(c,b)}(X)) \) for all \( b \in B_{i-1} \) using Algorithm 2.
6: \( r_i(X) \leftarrow \sum_{b \in B_{i-1}} r^{(b)}(X) \).
7: Send \( r_i(X) \) to \( V \).
8: Receive \( \alpha_i \) from \( V \).
9: Set \( A_j[b] \leftarrow r^{(j,b)}(\alpha_i) \) for each \( b \in B_{i-1} \).
10: end for
11: end procedure

In \([72,79]\), \( r^{(b)}(X) := h(r^{(1,b)}(X), \ldots, r^{(c,b)}(X)) \) is computed by evaluating \( h \) on \( d \) distinct values for \( X \), e.g. \( X \in \{0,\ldots,d\} \) and interpolating the output. This works as \( h \) is a degree \( d \) polynomial and each \( r^{(j,b)} \) is linear. Evaluating \( r^{(j,b)} \) on \( d \) points can be done in \( d \) steps. So the total time to evaluate all \( r^{(j,b)} \) for \( j \in [c] \) is \( c \cdot d \). Furthermore, the circuit has \( O(d) \) gates, and evaluating it on \( d \) inputs, takes time \( O(d^2) \). Assuming that \( c \approx d \) the total time to compute \( r^{(b)} \) with this algorithm is \( O(d^2) \) and the time to run Algorithm 1 is \( O(2^\mu d^2) \).

We show how this can be reduced to \( O(2^\mu \cdot d \log^2 d) \) for certain low depth circuits, such as \( h := \prod X \cdot r(X) \). The core idea is that evaluating the circuit for \( h \) symbolically, instead of at \( d \) individual points, is faster if fast polynomial multiplication algorithms are used.

We will present the algorithm for computing \( h(X) := \prod_{j=1}^d r_j(X) \), then we will discuss how to extend this for more general \( h \). Assume w.l.o.g. that \( d \) is a power of 2.

In round \( i \) there are \( d/2^i \) polynomial multiplications for polynomials of degree \( 2^{i-1} \). In FFT-friendly\(^3\) fields, polynomial multiplication can be performed in time \( O(d \log d) \).\(^4\) The total running time of the algorithm is therefore \( \sum_{i=1}^{\log_2(d)} \frac{d}{2^i} 2^i \log(2^i-1) = \sum_{i=1}^{\log_2(d)} O(d \cdot i) = O(d \log^2 d) \).

Algorithm 2 naturally extends to more complicated, low-depth circuits. Addition gates are performed directly through polynomial addition, which takes \( O(d) \) time for degree \( d \) polynomials. As long as the circuit is low-depth and has \( O(d) \) multiplication gates, the complexity remains \( O(d \log^2 d) \). Furthermore, we can compute \( r^k(X) \) for \( k \leq d \) using only a single FFT of length \( \deg(r) \cdot k \) for an input polynomial \( r \). The FFT evaluates \( r \) at \( \deg(r) \cdot k \) points. Then we raise each

---

\(^3\)These are fields where there exists an element that has a smooth order of at least \( d \).

\(^4\)Recent breakthrough results have shown that polynomial multiplication is \( O(d \log d) \) over arbitrary finite fields \([54]\) and there have been efforts toward building practical, fast multiplication algorithms for arbitrary fields \([11]\). In practice, and especially for low-degree polynomials, using Karatsuba multiplication might be faster.
Algorithm 2 Evaluating $h := \prod_{j=1}^{d} r_j$

Require: $r_1, \ldots, r_d$ are linear functions

1: procedure $h(r_1(X), \ldots, r_d(X))$
2:   $t_{1,j} \leftarrow r_j$ for all $j \in [d]$.
3: for $i \leftarrow 1 \ldots \log d$ do
4:   for $j \in [d/2^i]$ do
5:     $t_{i+1,j}(X) \leftarrow t_{i,2j-1}(X) \cdot t_{i,2j}(X)$ \qquad \triangleright Using fast polynomial multiplication
6:   end for
7: end for
8: return $h = t_{\log_2(d),1}$
9: end procedure

point to the power of $k$. This takes time $O(\deg(r) \cdot k (\deg(r)) + \log(k))$ and saves a factor of $\log(k)$ over a repeated squaring implementation.

Batching. Multiple sumcheck instances, e.g. $(s, [[f]])$ and $(s', [[g]])$ can easily be batched together. This is done using a random-linear combination, i.e. showing that $(s + \alpha s', [[f]] + \alpha [[g]]) \in \mathcal{L}(\mathcal{R}_{\text{SUM}})$ for a random verifier-generated $\alpha$ [75, 34]. The batching step has soundness $\frac{1}{\mathcal{P}}$.

Complexity. Overall, Algorithm 1 calls Algorithm 2 for each point in the boolean hypercube and then on each point in a cube of half the size. The total runtime of Algorithm 1 is, therefore, $O(2^\mu d \log^2 d)$ if $h$ is degree $d$ and low-depth. We summarize the complexity of the PIOP for $\mathcal{R}_{\text{SUM}}$ with respect to $f \in \mathcal{F}_\mu^{(\leq d)}$, below:

- The prover time is $t_{\text{p}}^{f_{\text{sum}}} = O(2^\mu \cdot d \log^2 d)$ $\mathbb{F}$-ops (for low-depth $f$ that can be evaluated in time $O(d)$).
- The verifier time is $t_{\text{v}}^{f_{\text{sum}}} = O(\mu)$.
- The query complexity is $q_{\text{sum}}^f = \mu + 1$, $\mu$ queries to univariate oracles, one to multi-variate $f$.
- The round complexity and the number of proof oracles is $r_{\text{sum}}^f = \mu$.
- The number of field elements sent by $\mathcal{P}$ is $\mu$.
- The size of the proof oracles is $p_{\text{sum}}^f = d \cdot \mu$; the size of the witness is $c \cdot 2^\mu$.

3.2 ZeroCheck PIOP

In this section, we describe a PIOP showing that a multivariate polynomial evaluates to zero everywhere on the boolean hypercube. The PIOP builds upon the sumcheck PIOP in Section 3.1 and is a key building block for product-check PIOP in Section 3.3. The zerocheck PIOP is also helpful in HyperPlonk for proving the gate identity.

Definition 3.2 (ZeroCheck relation). The relation $\mathcal{R}_{\text{ZERO}}$ is the set of all tuples $(x; w) = (([f]); f)$ where $f \in \mathcal{F}_\mu^{(\leq d)}$ and $f(x) = 0$ for all $x \in B_\mu$.

We use an idea from [68] to reduce a ZeroCheck to a SumCheck.
Construction. Given a tuple \((x; w) = (|[f]|; f)\), the protocol is the following:

- \(\mathcal{V}\) sends \(\mathcal{P}\) a random vector \(\mathbf{r} \leftarrow \mathbb{F}^\mu\)
- Let \(\hat{f}(\mathbf{X}) := f(\mathbf{X}) \cdot eq(\mathbf{X}, \mathbf{r})\) where \(eq(x, y) := \prod_{i=1}^{\mu} (x_i y_i + (1-x_i)(1-y_i))\).
- Run a sumcheck PolyIOP to convince the verifier that \(((0, [\hat{f}])); \hat{f}) \in \mathcal{R}_{\text{SUM}}\).

Batching. It is possible to batch two instances \(((|[f]|); f) \in \mathcal{R}_{\text{ZERO}}\) and \(((|[g]|); g) \in \mathcal{R}_{\text{ZERO}}\) by running a zerocheck on \(((|[f + \alpha g]|); f + \alpha g)\) for a random \(\alpha \in \mathbb{F}\). The soundness error of the batching protocol \(\frac{1}{2}\).

Theorem 3.2. The PIOP for \(\mathcal{R}_{\text{ZERO}}\) is perfectly complete and has knowledge error \(\delta_{\text{zero}} := \frac{d\mu}{|\mathbb{F}|} + \delta_{\text{sum}}^{d+1, \mu} = O(d\mu/|\mathbb{F}|)\).

Proof. Completeness. For every \(((|[f]|); f) \in \mathcal{R}_{\text{ZERO}}\), \(\hat{f}\) is also zero everywhere on the boolean hypercube, thus the sumcheck of \(\hat{f}\) is zero, and completeness follows from sumcheck’s completeness.

Knowledge soundness. By Lemma 2.3, it is sufficient to argue the soundness error of the protocol. We note that \([|f]|) \in \mathcal{L}(\mathcal{R}_{\text{ZERO}})\) (i.e., \(((|[f]|); f) \in \mathcal{R}_{\text{ZERO}}\)) if and only if the following auxiliary polynomial

\[
g(Y) := \sum_{x \in B^\mu} f(x) \cdot eq(x, Y)
\]

is identically zero. This is because \(eq(x, y)\) for a \(x, y \in B^\mu\) is 1 if \(x = y\) and 0 otherwise. So \(g(y) = f(y)\) for all \(y \in B^\mu\). Therefore, for any \([|f]|) \notin \mathcal{L}(\mathcal{R}_{\text{ZERO}}),\) the corresponding \(g\) is a non-zero polynomial and by Lemma 2.2,

\[
g(r) = \sum_{x \in B^\mu} f(x) \cdot eq(x, r) = 0
\]

with probability \(d\mu/|\mathbb{F}|\) over the choice of \(r\), thus the probability that the verifier accepts is at most \(d\mu/|\mathbb{F}|\) plus the probability that the SumCheck PIOP verifier accepts when \(((0, [\hat{f}])); \hat{f}) \notin \mathcal{R}_{\text{SUM}},\) which is \(d\mu/|\mathbb{F}| + \delta_{\text{sum}}^{d+1, \mu}\) as desired. \(\square\)

Complexity. We analyze the complexity of the PIOP for \(\mathcal{R}_{\text{ZERO}}\) with respect to \(f \in \mathcal{F}_{\mu}^{(\leq d)}\).

- The prover time is \(t_{\text{prover}} = t_{\text{sum}} = O(d \log^2 d \cdot 2^\mu)\) \(\mathbb{F}\)-ops.
- The verifier time is \(t_{\text{verifier}} = O(\mu)\).
- The query complexity is \(q_{\text{prover}} = q_{\text{sum}} = \mu + 1\).
- The round complexity and the number of proof oracles is \(r_{\text{prover}} = r_{\text{sum}} = \mu\).
- The number of field elements sent by \(\mathcal{P}\) is \(n_{\text{prover}} = n_{\text{sum}} = \mu\).
- The size of the proof oracles is \(p_{\text{prover}} = p_{\text{sum}} = d\mu;\) the size of the witness is \(O(2^\mu)\).
3.3 ProductCheck PIOP

We describe a PIOP for the product check relation, that is, for a rational polynomial (where both the nominator and the denominator are multivariate polynomials), the product of the evaluations on the boolean hypercube is a claimed value \( s \). The PIOP uses the idea from the Quark system [69, §5], we adapt it to build upon the zerocheck PIOP in Section 3.2. Product check PIOP is a key building block for the multiset equality check PIOP in Section 3.4.

**Definition 3.3 (ProductCheck relation).** The relation \( \mathcal{R}_{\text{PROD}} \) is the set of all tuples \( (x; w) = ((s, [[f_1]], [[f_2]]); f_1, f_2) \) where \( f_1 \in \mathcal{F}_{\mu}^{\leq d} \), \( f_2 \in \mathcal{F}_{\mu}^{\leq d} \), \( f_2(b) \neq 0 \ \forall b \in B_\mu \) and \( \prod_{x \in B_\mu} f'(x) = s \), where \( f' \) is the rational polynomial \( f' := f_1/f_2 \). In the case that \( f_2 = c \) is a constant polynomial, we directly set \( f := f_1/c \) and write \( (x; w) = ((s, [[f]]); f) \).

**Construction.** The Quark system [69, §5] constructs a proof system for the \( \mathcal{R}_{\text{PROD}} \) relation. The proof system uses an instance of the \( \mathcal{R}_{\text{ZERO}} \) PolyIOP on \( \mu + 1 \) variables. Given a tuple \( (x; w) = ((s, [[f_1]], [[f_2]]); f_1, f_2) \), we denote by \( f'' := f_1/f_2 \). The protocol is the following:

- **\( P \)** sends an oracle \( \tilde{v} \in \mathcal{F}_{\mu+1}^{\leq 1} \) such that for all \( x \in B_\mu \),
  \[
  \tilde{v}(0, x) = f''(x), \quad \tilde{v}(1, x) = \tilde{v}(x, 0) \cdot \tilde{v}(x, 1).
  \]

- Define \( \hat{h} := \text{merge}(\hat{f}, \hat{g}) \in \mathcal{F}_{\mu+1}^{\leq \max(2, d+1)} \) where
  \[
  \hat{f}(X) := \tilde{v}(1, X) - \tilde{v}(X, 0) \cdot \tilde{v}(X, 1), \quad \hat{g}(X) := f_2(X) \cdot \tilde{v}(0, X) - f_1(X) .
  \]

Run a ZeroCheck PolyIOP for \( ([\hat{h}] ; \hat{h}) \in \mathcal{R}_{\text{ZERO}} \), i.e., the polynomial \( \tilde{v} \) is computed correctly.

- **\( \mathcal{V} \)** queries \( [\tilde{v}] \) at point \((1, \ldots, 1, 0) \in \mathbb{F}^{\mu+1} \), and checks that the evaluation is \( s \).

**Theorem 3.3.** Let \( d' := \max(2, d + 1) \). The PIOP for \( \mathcal{R}_{\text{PROD}} \) is perfectly complete and has knowledge error \( \delta^d_{\text{prod}} := \delta^d_{\text{zero}} = \mathcal{O}(d' \mu/|\mathbb{F}|) \).

**Proof. Completeness.** First, if the prover honestly generates \( \tilde{v} \), it holds that \( ([\hat{h}] ; \hat{h}) \in \mathcal{R}_{\text{ZERO}} \), and the verifier accepts in the sub-PIOP, given that ZeroCheck is complete. Second, if \( (s, [[f_1]], [[f_2]]); f_1, f_2) \in \mathcal{R}_{\text{PROD}} \), the evaluation \( \tilde{v}(1, \ldots, 1, 0) \) is exactly the product of \( f \)’s evaluations on the boolean hypercube \( B_\mu \) (c.f. [69, §5]), which is \( s \) as desired.

**Knowledge soundness.** By Lemma 2.3, it is sufficient to argue the soundness error of the protocol. For any \( (s, [[f_1]], [[f_2]]) \notin \mathcal{L}(\mathcal{R}_{\text{PROD}}) \) and any \( \tilde{v} \) sent by a malicious prover, it holds that either \( \tilde{v} \) is not computed correctly (i.e., \( ([\hat{h}] ; \hat{h}) \notin \mathcal{R}_{\text{ZERO}} \)), or the evaluation \( \tilde{v}(1, \ldots, 1, 0) \neq s \) and \( \mathcal{V} \) rejects. Hence the probability that \( \mathcal{V} \) accepts is at most \( \max(\delta^d_{\text{zero}} + 1, 0) = \delta^d_{\text{zero}} + 1 \) as claimed.

**Complexity.** Let \( \hat{h} \) be the polynomials described in the construction, we analyze the complexity of the PIOP for \( \mathcal{R}_{\text{PROD}} \) with respect to \( f'' := f_1/f_2 \) where \( f_1, f_2 \in \mathcal{F}_{\mu}^{\leq d} \).

- The prover time is \( t_{\text{pro}}^f = t_{\text{pro}}^h + 2^\mu = \mathcal{O}(d \log^2 d \cdot 2^\mu) \) \( \mathbb{F} \)-ops. The term \( 2^\mu \) is for computing the product polynomial \( \tilde{v} \).
- The verifier time is \( t_{\text{ver}}^f = t_{\text{ver}}^h = \mathcal{O}(\mu) \).
- The query complexity is \( q_{\text{pro}}^f = q_{\text{zero}}^h + 1 = \mu + 2 \), the additional query is for \( \tilde{v}(1, \ldots, 1, 0) \).
• The round complexity and the number of proof oracles is $rc_{prod}' = r_{hzero} + 1 = \mu + 1$.
• The number of field elements sent by $P$ is $nf_{prod}' = n_{hzero} = \mu$.
• The size of the proof oracles is $pl_{prod}' = 2^\mu + p_{hzero} = O(2^\mu)$; the size of the witness is $O(2^\mu)$.

3.4 Multiset Check PIOP

We describe a multivariate PIOP checking that two multisets are equal. The PIOP builds upon the product-check PIOP in Section 3.3. The multiset check PIOP is a key building block for the permutation PIOP in Section 3.5 and the lookup PIOP in Section 3.7. A similar idea has been proposed in the univariate polynomial setting by Gabizon in a blogpost [43].

Definition 3.4 (Multiset Check relation). For any $k \geq 1$, the relation $R_{MSET}^k$ is the set of all tuples

$$(x; w) = (\{[f_1], \ldots, [f_k], [g_1], \ldots, [g_k]\}; (f_1, \ldots, f_k, g_1, \ldots, g_k))$$

where $f_i, g_i \in \mathcal{F}_\mu(\leq d)$ ($1 \leq i \leq k$) and the following two multisets of tuples are equal:

$$\left\{ f_x := [f_1(x), \ldots, f_k(x)] \right\}_{x \in B_\mu} = \left\{ g_x := [g_1(x), \ldots, g_k(x)] \right\}_{x \in B_\mu}$$

Basic construction. We start by describing a PolyIOP for $R_{MSET}^1$. The protocol can be obtained from a protocol for $R_{PROD}$. Given a tuple $(([[f]], [[g]]); (f, g))$, the protocol is the following:

• $V$ samples and sends $P$ a challenge $r \leftarrow \mathbb{F}$.
• Set $f' := f + r$ and $g' := g + r$, run a ProductCheck PolyIOP for $(([[f']], [[g']]); (f', g')) \in R_{PROD}$.

Theorem 3.4. The PIOP for $R_{MSET}^1$ is perfectly complete and has knowledge error $\delta_{mset,1}^{d,\mu} := 2^\mu/|\mathbb{F}| + \delta_{prod}^{d,\mu} = O((2^\mu + d\mu)/|\mathbb{F}|)$.

Proof. Completeness. For any $(([[f]], [[g]]); (f, g)) \in R_{MSET}^1$, it holds that

$$\prod_{x \in B_\mu} (r + f(x)) = \prod_{x \in B_\mu} (r + g(x)),$$

thus $\prod_{x \in B_\mu} (r + f(x))/(r + g(x)) = 1$, i.e., $((1, [r + f], [r + g]); r + f, r + g) \in R_{PROD}$. Therefore completeness holds given that the PolyIOP for $R_{PROD}$ is complete.

Knowledge soundness. By Lemma 2.3, it is sufficient to argue the soundness error of the protocol. For any $(([[f]], [[g]]) \notin L(R_{MSET}^1)$ (i.e., $(([[f]], [[g]]); (f, g)) \notin R_{MSET}^1$), it holds that

$$F(Y) := \prod_{x \in B_\mu} (Y + f(x)) \neq G(Y) := \prod_{x \in B_\mu} (Y + g(x)).$$

By Lemma 2.2, $F(r) \neq G(r)$ with probability at least $1 - (2^\mu/|\mathbb{F}|)$. Conditioned on $F(r) \neq G(r)$, it holds that $(1, [r + f], [r + g]); r + f, r + g) \notin R_{PROD}$. Hence the probability that $V$ accepts conditioned on $F(r) \neq G(r)$ is at most $\delta_{prod}^{d,\mu}$. In summary, the probability that $V$ accepts is at most $2^\mu/|\mathbb{F}| + \delta_{prod}^{d,\mu}$ as claimed. □
The final construction. Next we describe the protocol for $R_{\text{MSET}}^k$ for any $k \geq 1$. Given a tuple $\left(\{[f_1], \ldots, [f_k], [g_1], \ldots, [g_k]\}; (f_1, \ldots, f_k, g_1, \ldots, g_k)\right)$, the protocol is the following:

- $V$ samples and sends $P$ challenges $r_2, \ldots, r_k \leftarrow \mathbb{F}$.
- Run a Multiset Check PolyIOP for $\left(\{[\hat{f}], [\hat{g}]\}; (\hat{f}, \hat{g})\right)$ where $\hat{f}, \hat{g} \in \mathcal{F}_{\mu}^{(\leq d)}$ are defined as $\hat{f} := f_1 + r_2 \cdot f_2 + \cdots + r_k \cdot f_k$ and $\hat{g} := g_1 + r_2 \cdot g_2 + \cdots + r_k \cdot g_k$.

**Theorem 3.5.** The PIOP for $R_{\text{MSET}}^k$ is perfectly complete and has knowledge error $\delta_{\text{mset},k}^{d,\mu} := 2^\mu / |\mathbb{F}| + \delta_{\text{mset},1}^{d,\mu} = O((2^\mu + d\mu) / |\mathbb{F}|)$.

**Proof.** Completeness. Completeness holds since the PolyIOP for $\left(\{[f], [g]\}; (f, g)\right) \in R_{\text{MSET}}^1$ is complete.

Knowledge soundness. By Lemma 2.3, it is sufficient to argue the soundness error of the protocol. Given any $\left(\{[f_1], \ldots, [f_k], [g_1], \ldots, [g_k]\}\right) \notin L\left(R_{\text{MSET}}^k\right)$, let $U := \{f_x := [f_1(x), \ldots, f_k(x)]\}_{x \in B_\mu}, \quad V := \{g_x := [g_1(x), \ldots, g_k(x)]\}_{x \in B_\mu}$ denote the corresponding multisets. Let $W$ be the maximal multiset such that $W \subseteq U$ and $W \subseteq V$. We set $U' := U \setminus W, V' := V \setminus W$.

We observe that $|U'| = |V'| > 0$ as $U \neq V$, and $U' \cap V' = \emptyset$ by definition of $W$. Thus there exists an element $x \in \mathbb{F}^k$ where $x \in U'$ but $x \notin V'$. It is well-known that the map $\phi_r : (x_1, \ldots, x_k) \rightarrow x_1 + r_2x_2 + \cdots + r_kx_k$ is a universal hash family [33, 77, 70], that is, for any $x, y \in \mathbb{F}^k, x \neq y$, it holds that

$$\Pr_r[\phi_r(x) = \phi_r(y)] \leq \frac{1}{|\mathbb{F}|}.$$ 

Thus by union bound, the probability (over the choice of $r$) that

$$\phi_r(x) \in \{\phi_r(y) : y \in V'\}$$

is at most $|V'| / |\mathbb{F}| \leq 2^\mu / |\mathbb{F}|$. Conditioned on that above does not happen, we have that $\left(\{[\hat{f}], [\hat{g}]\}\right) \notin L(R_{\text{MSET}}^1)$ and the probability that $V$ accepts in the PolyIOP for $R_{\text{MSET}}^1$ is at most $\delta_{\text{mset},1}^{d,\mu}$. In summary, the soundness error is at most $2^\mu / |\mathbb{F}| + \delta_{\text{mset},1}^{d,\mu}$ as claimed. \hfill \Box

**Complexity.** We analyze the complexity of the PIOP for $R_{\text{MSET}}^k$ with respect to

$$F := (f_1, \ldots, f_k, g_1, \ldots, g_k) \in \left(\mathcal{F}_{\mu}^{(\leq d)}\right)^{2k}.$$ 

- The prover time is $\text{tp}_{F,\text{mset}} = \text{tp}_{\hat{f},g,\text{mset}} = \text{tp}_{\hat{f}',g'} = O\left(d\log^2 d \cdot 2^\mu\right)$ $\mathbb{F}$-ops (for $k$ where $\hat{f} := f_1 + r_2 \cdot f_2 + \cdots + r_k \cdot f_k$ and $\hat{g} := g_1 + r_2 \cdot g_2 + \cdots + r_k \cdot g_k$ can be evaluated in time $O(d)$).

\footnote{E.g., if $k = 1$ and $U = \{1, 1, 1, 2\}$ and $V = \{1, 1, 2, 2\}$, then $W = \{1, 1, 2\}, U' = \{1\}$ and $V' = \{2\}$.}
• The verifier time is $t_{v_{\text{mset}}}^{\text{F}} = t_{v_{\text{prod}}}^{f'/g'} = \mathcal{O}(\mu)$.
• The query complexity is $q_{\text{mset}}^{\text{F}} = q_{\text{prod}}^{f'/g'} = \mu + 2$.
• The round complexity and the number of proof oracles is $rc_{\text{mset}}^{\text{F}} = rc_{\text{prod}}^{f'/g'} = \mu + 1$.
• The number of field elements sent by $\mathcal{P}$ is $n_{\text{mset}}^{\text{F}} = n_{\text{prod}}^{f'/g'} = \mu$.
• The size of the proof oracles is $pl_{\text{mset}}^{\text{F}} = pl_{\text{prod}}^{f'/g'} = \mathcal{O}(2^\mu)$; the size of the witness is $\mathcal{O}(k \cdot 2^\mu)$.

### 3.5 Permutation PIOP

We describe a multivariate PIOP showing that for two multivariate polynomials $f, g \in \mathcal{F}_\mu^{(\leq d)}$, the evaluations of $g$ on the boolean hypercube is a predefined permutation $\sigma$ of $f$’s evaluations on the boolean hypercube. The permutation PIOP is a key building block of HyperPlonk for proving the wiring identity.

**Definition 3.5 (Permutation relation).** The indexed relation $R_{\text{PERM}}$ is the set of tuples

$$(i; x; w) = (\sigma; ([f]), ([g]); (f, g)),$$

where $\sigma : B_\mu \to B_\mu$ is a permutation, $f, g \in \mathcal{F}_\mu^{(\leq d)}$, and $g(x) = f(\sigma(x))$ for all $x \in B_\mu$.

**Construction.** Gabizon et. al. [46] construct a permutation argument. We adapt their scheme into a multivariate PolyIOP. The construction uses a PolyIOP instance for $R_{\text{MSET}}$. Given a tuple $(\sigma; ([f]), ([g]); (f, g))$ where $\sigma$ is the predefined permutation, the indexer generates two oracles $[[s_{\text{id}}]], [[s_{\sigma}]]$ such that $s_{\text{id}} \in \mathcal{F}_\mu^{(\leq 1)}$ maps each $x \in B_\mu$ to $[x] := \sum_{i=1}^{\mu} x_i \cdot 2^{i-1} \in \mathbb{F}$, and $s_{\sigma} \in \mathcal{F}_\mu^{(\leq 1)}$ maps each $x \in B_\mu$ to $[\sigma(x)]$. The PolyIOP is the following:

- Run a Multiset Check PolyIOP for
  $$(([s_{\text{id}}]), ([f]), ([s_{\sigma}]), ([g])); (s_{\text{id}}, f, s_{\sigma}, g)) \in R_{\text{MSET}}^2.$$

**Theorem 3.6.** The PIOP for $R_{\text{PERM}}$ is perfectly complete and has knowledge error $\delta_{\text{perm}}^{d, \mu} := \delta_{\text{mset},2}^{d, \mu} = \mathcal{O}\left((2^\mu + d\mu)/|\mathbb{F}|\right)$.

**Proof.** Completeness. For any $(\sigma; ([f]), ([g]); (f, g)) \in R_{\text{PERM}}$, it holds that the multiset $\{([x], f(x))\}_{x \in B_\mu}$ is identical to the multiset $\{([\sigma(x)], g(x))\}_{x \in B_\mu}$. Thus

$$(([s_{\text{id}}]), ([f]), ([s_{\sigma}]), ([g])); (s_{\text{id}}, f, s_{\sigma}, g)) \in R_{\text{MSET}}^2$$

and completeness follows from the completeness of the PolyIOP for $R_{\text{MSET}}^2$.

**Knowledge soundness.** By Lemma 2.3, it is sufficient to argue the soundness error of the protocol. The PolyIOP has soundness error $\delta_{\text{mset},2}^{d, \mu}$ as the permutation relation holds if and only if the above multiset check relation holds.

**Complexity.** The complexity of the PIOP for $R_{\text{PERM}}$ with respect to $f, g \in \mathcal{F}_\mu^{(\leq d)}$ is identical to the complexity of the PIOP for $R_{\text{MSET}}^2$ with respect to $(s_{\text{id}}, f, s_{\sigma}, g)$.

---

6 Here we further require $|\mathbb{F}| \geq 2^\mu$ so that $[x]$ never overflow.
3.6 Another permutation PIOP for small fields

We describe a different multivariate PIOP for \( \mathcal{R}_{\text{PERM}} \). This PIOP directly reduces to \( \mathcal{R}_{\text{SUM}} \) and has soundness \( O(\mu^2/|F|) \) instead of \( O(2^\mu/|F|) \). This enables using polynomial-sized fields such as in [23]. The downside is that the PIOP has quasi-linear (rather than linear) prover time. This comes from splitting up the permutation into \( \mu \) multi-linear polynomials. We emphasize that to obtain linear-time provers, the PIOPs used in Section 4 and Section 5 are the version in Section 3.5, rather than in this section.

For simplicity, we only describe the construction for multi-linear polynomials \( f, g \). It can be easily extended to higher degree polynomials by adding an additional ZeroCheck and proving equivalence between a multi-variate and a multi-linear polynomial on the boolean hypercube.

**Construction.** Our core idea is similar to the protocol in \( \mathcal{R}_{\text{BATCH}} \). Given a permutation \( \sigma : B_\mu \to B_\mu \), we reduce the equality check \( f(b') = g(\sigma(b')) \) for a given point \( b' \in B_\mu \) to a sum-check via multilinear extension. We then use a zero-check over all \( b' \) to prove the equality for all \( b' \in B_\mu \), which in turn can be reduced to another sumcheck. The resulting sumcheck is over a polynomial with \( 2\mu \) variables and individual degree \( \mu+1 \), and naively running the protocol takes time quadratic in \( 2^\mu \). Fortunately, we can utilize that \( \sigma \) is a permutation on \( B_\mu \) and reduce the prover computation to \( O(2^\mu \log^2(\mu)) \).

We begin by defining a multi-linear version of the permutation \( \sigma \). \( \tilde{\sigma} = (\sigma(1)(X), \ldots, \sigma(\mu)(X)) : \mathbb{F}^\mu \to \mathbb{F}^\mu \), such that for all \( i \in [\mu] \), \( \sigma_i \) is the multi-linear extension of the \( i \)th binary digit of \( \sigma \). Note that \( \sigma_i(x) \in \{0, 1\} \) for all \( x \in B_\mu \) and \( i \in [\mu] \). We now rewrite the permutation relation as an \( \mathcal{R}_{\text{ZERO}} \) relation for a polynomial equality equation.

\[
f(\tilde{\sigma}(x)) - g(x) = 0 \forall x \in B_\mu.
\]

We can now expand \( f(\tilde{\sigma}(x)) - g(x) \) to its multilinear extension form just as in the protocol for \( \mathcal{R}_{\text{BATCH}} \). This becomes

\[
\sum_{y \in B_\mu} (f(y)eq(\tilde{\sigma}(x), y) - g(y)eq(x, y)) = 0 \forall x \in B_\mu.
\]

Next, we use the standard trick in Section 3.2 to reduce the zerocheck to another sumcheck. Namely, for a random challenge \( t \in \mathbb{F}^\mu \), we check that

\[
\sum_{x \in B_\mu} eq(t, x) \cdot \sum_{y \in B_\mu} (f(y)eq(\tilde{\sigma}(x), y) - g(y)eq(x, y)) = 0. \tag{8}
\]

This is a \( 2\mu \)-round sumcheck. Unfortunately, we cannot directly evaluate this sumcheck efficiently. To see this, consider the second round of the sumcheck after the verifier has sent a challenge \( \alpha_1 \). The prover needs to evaluate

\[
eq(\tilde{\sigma}(\alpha_1, x_2, \ldots, x_\mu), y) = eq((\sigma(1)(\alpha_1, x_2, \ldots, x_\mu), \ldots, \sigma(\mu)(\alpha_1, x_2, \ldots), x_\mu), y)
\]

for all \( (x_2, \ldots, x_\mu) \in B_{\mu-1} \) and all \( y \in B_\mu \). This takes time \( O(2^2\mu) \), i.e. quadratic in the size of the permutation.

To remedy this we take advantage of the fact that \( \sigma \) is a permutation and has an inverse permutation \( \sigma^{-1} \) such that \( \sigma(\sigma^{-1}(x)) = x \forall x \in B_\mu \). We similarly define \( \tilde{\sigma}^{-1} : \mathbb{F}^\mu \to \mathbb{F}^\mu \) as the
multi-linear version of the inverse permutation \( \sigma^{-1} \). More precisely, \( \tilde{\sigma}^{-1} = (\sigma^{-1}_1(X), \ldots, \sigma^{-1}_\mu(X)) : \mathbb{F}^\mu \rightarrow \mathbb{F}^\mu \), such that for all \( i \in [\mu] \), \( \sigma^{-1}_i \) is the multi-linear extension of the \( i \)th binary digit of \( \sigma^{-1} \).

We can then rewrite the sumcheck from Equation 8 as

\[
\sum_{x \in B_\mu} e_q(t, x) \cdot \sum_{y \in B_\mu} (f(y)e_q(x, \tilde{\sigma}^{-1}(y)) - g(y)e_q(x, y)) = 0 \tag{9}
\]

For any \( x \) and \( y \in B_\mu \), the inner sum is \( f(y) \) if \( x = \sigma^{-1}(y) \) or equivalently \( f(\sigma(x)) \) and \(-g(x)\) if \( x = y \) and 0 otherwise. Since we sum over the entire boolean hypercube for both \( x \) and \( y \) and \( \sigma \) is a permutation on \( B_\mu \), (9) is equivalent to \( \sum_{x \in B_\mu} e_q(t, x)(f(\sigma(x)) - g(x)) \)

The prover can run the sumcheck from (9) efficiently by treating \( \tilde{\sigma}^{-1} \) as a permutation in the first \( \mu \) rounds of the sumcheck, where we only consider \( x \) values within the hypercube. In the latter \( \mu \) rounds \( x \) has been replaced with verifier challenges \( \alpha_1, \ldots, \alpha_\mu \) and we can now treat \( \tilde{\sigma}^{-1} \) as a collection of \( \mu \) multi-linear functions. We present the PIOP and the corresponding prover algorithm in Algorithm 3. The verifier is identical to the \( \mathcal{R}_{\text{SUM}} \) verifier for the sumcheck equation (9).

**Algorithm 3** Permutation PIOP with better soundness.

1: procedure Perm2 Prover\((f \in \mathcal{F}_\mu^{(\leq 1)}, g \in \mathcal{F}_\mu^{(\leq 1)}, \sigma : B_\mu \rightarrow B_\mu)\)
2: \hspace{1em} \( \mathcal{V} \) sends \( \mathcal{P} \) a random vector \( t \in \mathbb{F}^\mu \).
3: Run the sumcheck for \( \mu \) rounds on the outer sum as described in Algorithm 1. Note that in the \( i \)th round, for any given value of \( x \in B_{\mu - i}, e_q((\alpha_1, \ldots, \alpha_i, x), \sigma^{-1}(y)) \) is non-zero for at most \( 2^i \) values of \( y \in B_\mu \). This is because \( \sigma^{-1} \) is a permutation on the boolean hypercube, thus the \( eq \) value is non-zero only if the last \( \mu - i \) values of \( \sigma^{-1}(y) \) are identical to \( x \). Similarly \( eq((\alpha_1, \ldots, \alpha_i, x), y) \neq 0 \) for at most \( 2^i \) values of \( y \) for any given \( x \). The prover can, therefore, evaluate all inner sumchecks in each round in time \( O(2^\mu) \). The prover runs in time \( O(\mu 2^\mu) \).
4: Run the inner sumcheck with \( x = \alpha \) as described in Algorithm 1 and treating \( \sigma^{-1}_1, \ldots, \sigma^{-1}_\mu \) as multi-linear polynomials. The prover runs in time \( O(2^\mu \log^2(\mu)) \) as the sumcheck has \( \mu \) rounds and degree \( \mu \).
5: end procedure

**Theorem 3.7.** The PERM2 PIOP described by the sumcheck for Equation (9) and the corresponding prover from Algorithm 3 for \( \mathcal{R}_{\text{PERM}} \) is perfectly complete and has knowledge error \( \delta_{\text{PERM}}^{\mu, 1, 2\mu} := \delta_{\text{SUM}}^{\mu + 1, 2\mu} = O(\mu^2/|\mathbb{F}|) \).

**Proof.** **Completeness.** For any \( (\sigma; ([f]), ([g])); (f, g) \in \mathcal{R}_{\text{PERM}} \), it holds that \( f(\sigma(x)) - g(x) = 0 \) for all \( x \in B_\mu \). Moreover, for all \( x, y \in B_\mu \), it holds that \( eq(x, \sigma^{-1}(y)) = 1 \) if \( \sigma(x) = y \) and \( eq(x, \sigma^{-1}(y)) = 0 \) otherwise; and \( eq(x, y) = 1 \) if \( x = y \) and \( eq(x, y) = 0 \) otherwise. Thus for \( h(x) = \sum_{y \in B_\mu} f(y) \cdot eq(x, \sigma^{-1}(y)) - g(y)eq(x, y) \)

\([[[h]]; h] \in \mathcal{R}_{\text{ZERO}}\)

and completeness follows from the completeness of the PolyIOP for \( \mathcal{R}_{\text{ZERO}} \).

**Knowledge soundness.** By Lemma 2.3, it is sufficient to argue the soundness error of the protocol. The permutation relation holds if and only if the above zerocheck relation holds, which reduces to a sumcheck for Equation (9). The sumcheck PolyIOP is over a virtual polynomial that has \( 2\mu \) variables and individual degree \( \mu + 1 \). Thus the soundness error is \( \delta_{\text{SUM}}^{\mu + 1, 2\mu} = O(\mu^2/|\mathbb{F}|) \). \( \square \)
Complexity. The prover complexity of the PERM2 PIOP with respect to \( f,g \in \mathcal{F}_\mu^{(\leq 1)} \) is \( O(2^\mu \log^2(\mu)) \) as the prover uses \( O(\mu 2^\mu) \) in step 3 and \( O(2^\mu \mu \log^2(\mu)) \) in step 4 of the algorithm. The verifier is simply the sumcheck verifier for a \( 2^\mu \) round sumcheck and runs in time \( O(\mu) \). The verifier queries \( 2\mu \) univariate polynomials and \( 2+\mu \) multi-linear polynomials \((f,g,\sigma^{-1})\) each at one point. The PIOP proof size consists of \( 2\mu \) univariate oracles (one per round of the sumcheck).

3.7 Lookup PIOP

This section describes a multivariate PIOP checking the table lookup relation. The PIOP builds upon the multiset check PIOP (Section 3.4) and is a key building block for HyperPlonk+ (Section 5). Our construction is inspired by a univariate PIOP for the table lookup relation called \( \text{Plookup} \) [44]. However, it is non-trivial to adapt \( \text{Plookup} \) to the multivariate setting because their scheme requires the existence of a subdomain of the polynomial that is a cyclic subgroup \( \mathbb{G} \) with a generator \( \omega \in \mathbb{G} \). Translating to the multilinear case, we need to build an efficient function \( g \) that generates the entire boolean hypercube; moreover, \( g \) has to be linear so that the degree of the polynomial does not blow up. However, such a linear function does not exist. Fortunately, we can construct a quadratic function from \( \mathbb{F}^\mu \) to \( \mathbb{F}^\mu \) that traverses \( B_\mu \). We then show how to linearize it by modifying some of the building blocks that \( \text{Plookup} \) uses. This gives an efficient \( \text{Plookup} \) protocol over the hypercube.

**Definition 3.6 (Lookup relation).** The indexed relation \( \mathcal{R}_{\text{LOOKUP}} \) is the set of tuples

\[
(i; x; w) = (t; \lfloor f \rfloor; (f, \text{addr}))
\]

where \( t \in \mathbb{F}^{2^\mu-1} \), \( f \in \mathcal{F}_\mu^{(\leq d)} \), and \( \text{addr} : B_\mu \to [1, 2^\mu) \) is a map such that \( f(x) = t_{\text{addr}(x)} \) for all \( x \in B_\mu \).

Before presenting the PIOP for \( \mathcal{R}_{\text{LOOKUP}} \), we first show how to build a quadratic function that generates the entire boolean hypercube.

**A quadratic generator in \( \mathbb{F}_{2^\mu} \).** For every \( \mu \in \mathbb{N} \), we fix a primitive polynomial \( p_\mu \in \mathbb{F}_2[X] \) where \( p_\mu := X^\mu + \sum_{s \in S} X^s + 1 \) for some set \( S \subseteq [\mu - 1] \), so that \( \mathbb{F}_2[X]/(p_\mu) \cong \mathbb{F}^\mu_2[X] \cong \mathbb{F}_{2^\mu} \). By definition of primitive polynomials, \( X \in \mathbb{F}^\mu_2[X] \) is a generator of \( \mathbb{F}^\mu_2[X] \setminus \{0\} \). This naturally defines a generator function \( g_\mu : B_\mu \to B_\mu \) as

\[
g_\mu(b_1, \ldots, b_\mu) = (b_\mu, b'_1, \ldots, b'_{\mu-1}),
\]

where \( b'_i = b_i \oplus b_\mu \) (\( i \leq 1 < \mu \)) if \( i \in S \), and \( b'_i = b_i \) otherwise. Essentially, for a polynomial \( f \in \mathbb{F}^\mu_2[X] \) with coefficients \( b \), \( g_\mu(b) \) is the coefficient vector of \( X \cdot f(X) \). Hence the following lemma is straightforward.

**Lemma 3.8.** Let \( g_\mu : B_\mu \to B_\mu \) be the generator function defined above. For every \( x \in B_\mu \setminus \{0^\mu\} \), it holds that \( \{g_\mu^{(i)}(x)\}_{i \in [2^\mu - 1]} = B_\mu \setminus \{0^\mu\} \), where \( g_\mu^{(i)}(\cdot) \) denotes \( i \) repeated application of \( g_\mu \).

Directly composing a polynomial \( f \) with the generator \( g \) will blow up the degree of the resulting polynomial; moreover, the prover needs to send the composed oracle \( f(g(\cdot)) \). Both of which affect the efficiency of the PIOP. We address the issue by describing a trick that manipulates \( f \) in a way that simulates the behavior of \( f(g(\cdot)) \) on the boolean hypercube, but without blowing up the degree.
Linearizing the generator. For a multivariate polynomial \( f \in \mathcal{F}^{(\leq d)}_\mu \), we define \( f_{\Delta \mu} \in \mathcal{F}^{(\leq d)}_\mu \)

\[
f_{\Delta \mu}(X_1, \ldots, X_\mu) := X_\mu \cdot f(1, X_1', \ldots, X_\mu') + (1 - X_\mu) \cdot f(0, X_1, \ldots, X_\mu-1)
\]

where \( X_i' := 1 - X_i \) (\( i \leq 1 < \mu \)) if \( i \in S \), and \( X_i' := X_i \) otherwise.

Lemma 3.9. For every \( \mu \in \mathbb{N} \), let \( g_\mu : B_\mu \to B_\mu \) be the generator function defined in Lemma 3.8. For every \( d \in \mathbb{N} \) and polynomial \( f \in \mathcal{F}^{(\leq d)}_\mu \), it holds that \( f_{\Delta \mu}(x) = f(g_\mu(x)) \) for every \( x \in B_\mu \). Moreover, \( f_{\Delta \mu} \) has individual degree \( d \) and one can evaluate \( f_{\Delta \mu} \) from 2 evaluations of \( f \).

Proof. By definition, \( f_{\Delta \mu} \) has individual degree \( d \) and an evaluation of \( f_{\Delta \mu} \) can be derived from 2 evaluations of \( f \). Next, we argue that \( f_{\Delta \mu}(x) = f(g_\mu(x)) \) for every \( x \in B_\mu \).

First, \( f_{\Delta \mu}(0^\mu) = f(g_\mu(0^\mu)) \) because \( f_{\Delta \mu}(0^\mu) = f(0^\mu) \) and \( g_\mu(0^\mu) = 0^\mu \) by definition of \( f_{\Delta \mu}, g_\mu \).

Second, for every \( x \in B_\mu \setminus \{0^\mu\} \), by definition of \( g_\mu \),

\[
f(g_\mu(x_1, \ldots, x_\mu)) = f(x_\mu, x_1', \ldots, x_\mu'),
\]

where \( x_i' = x_i \oplus x_\mu \) (\( i \leq 1 < \mu \)) for every \( i \) in the fixed set \( S \), and \( x_i' = x_i \) otherwise. We observe that \( x_i \oplus x_\mu = 1 - x_i \) when \( x_\mu = 1 \) and \( x_i \oplus x_\mu = x_i \) when \( x_\mu = 0 \), thus we can rewrite

\[
f(x_\mu, x_1', \ldots, x_\mu') = x_\mu \cdot f(1, x_1', \ldots, x_\mu') + (1 - x_\mu) \cdot f(0, x_1, \ldots, x_\mu-1)
\]

where \( x_i' = 1 - x_i \) (\( i \leq 1 < \mu \)) for every \( i \) in the fixed set \( S \), and \( x_i' = x_i \) otherwise. The last equality holds by definition of \( f_{\Delta \mu} \). In summary, \( f(g_\mu(x_1, \ldots, x_\mu)) = f_{\Delta \mu}(x_1, \ldots, x_\mu) \) for every \( B_\mu \) and the lemma holds.

Construction. Now we are ready to present the PIOP for \( \mathcal{R}_{\text{LOOKUP}} \), which is an adaptation of Plookup [44] in the multivariate setting. The PIOP invokes a protocol for \( \mathcal{R}^2_{\text{MSET}} \). We introduce a notation that embeds a vector to the hypercube while still preserving the vector order with respect to the generator function. For a vector \( t \in \mathbb{F}^{2^\mu-1} \), we denote by \( t \leftarrow \text{emb}(t) \in \mathcal{F}^{(\leq 1)}_\mu \) the multilinear polynomial such that \( t(0^\mu) = 0 \) and \( t(g_\mu(t(1, 0^{\mu-1}))) = t_i \) for every \( i \in [2^\mu - 1] \). By Lemma 3.8, \( t \) is well-defined and embeds the entire vector \( t \) onto \( B_\mu \setminus \{0^\mu\} \).

For an index \( t \in \mathbb{F}^{2^\mu-1} \), the indexer generates an oracle \([t]\) where \( t \leftarrow \text{emb}(t) \). For a tuple \((t; [f]; (f, \text{addr}))\) where \( f(B_\mu) \subseteq t(B_\mu) \setminus \{0\} \), let \((a_1, \ldots, a_{2^\mu-1})\) be the vector where \( a_i \in \mathbb{N} \) is the number of appearance of \( t_i \) in \( f(B_\mu) \). Note that \( \sum_{i=1}^{2^\mu-1} a_i = 2^\mu \). Denote by \( h \in \mathbb{F}^{2^\mu+1} \) the vector

\[
h := (\underbrace{t_1, \ldots, t_1}_{1+a_1}, \underbrace{t_2, \ldots, t_{i-1}}_{1+a_i}, \underbrace{t_i, \ldots, t_i}_{1+a_i}, \underbrace{t_{i+1}, \ldots, t_{2^{\mu-2}}}_{1+a_{2^\mu-1}}, \underbrace{t_{2^{\mu-1}}, \ldots, t_{2^{\mu-1}}}_{1+a_{2^\mu-1}}).
\]

We present the protocol below:

- \( \mathcal{P} \) sends \( \mathcal{V} \) oracles \([h]\), where \( h \leftarrow \text{emb}(h) \in \mathcal{F}^{(\leq 1)}_{\mu+1} \).

- Define \( g_1 := \text{merge}(f, t) \in \mathcal{F}^{(\leq d)}_{\mu+1} \) and \( g_2 := \text{merge}(f, t_{\Delta \mu}) \in \mathcal{F}^{(\leq d)}_{\mu+1} \), where \text{merge} is defined in equation (7). Run a multiset check PIOP (Section 3.4) for

\[
([g_1], [g_2], [h], [h_{\Delta \mu+1}]; (f, t, h)) \in \mathcal{R}^2_{\text{MSET}}.
\]
• $\mathcal{V}$ queries $h(0^{\mu+1})$ and checks that the answer equals 0.

**Theorem 3.10.** The PIOP for $\mathcal{R}_{\text{LOOKUP}}$ is perfectly complete and has knowledge error $\delta_{\text{lookup}}^{d,\mu} := \delta_{\text{mset},2}^{d,\mu+1} = O((2^\mu + d\mu)/|F|)$.

**Proof.** **Completeness.** Denote by $n := 2^\mu$. For any $(t; [f]; (f, \text{addr})) \in \mathcal{R}_{\text{LOOKUP}}$, let $h \in \mathbb{F}^{2n-1}$ be the vector defined in the construction. Gabizon and Williamson [44] observed that

$$\{[f_i, f_i]\}_{i \in [n]} \cup \{[t_i, t_i \mod (n-1)+1]\}_{i \in [n]} = \{[h_i, h_i \mod (2n-1)+1]\}_{i \in [2n-1]},$$

equivalently, by definition of $t, h$ and by Lemma 3.9, the following two multisets of tuples are equal

$$\{[f(x), f(x)]\}_{x \in B_\mu} \cup \{[t(x), t\Delta_\mu(x)]\}_{x \in B_\mu \setminus \{0^\mu\}} = \{[h(x), h\Delta_{\mu+1}(x)]\}_{x \in B_{\mu+1} \setminus \{0^{\mu+1}\}},$$

By adding element $[0, 0] = [t(0^\mu), t\Delta_\mu(0^\mu)] = [h(0^{\mu+1}), h\Delta_{\mu+1}(0^{\mu+1})]$ on both sides, we have

$$\{[f(x), f(x)]\}_{x \in B_\mu} \cup \{[t(x), t\Delta_\mu(x)]\}_{x \in B_\mu} = \{[h(x), h\Delta_{\mu+1}(x)]\}_{x \in B_{\mu+1}}.$$

Hence the verifier accepts in the multiset check by completeness of the PIOP for $\mathcal{R}^2_{\text{mset}}$.

**Knowledge soundness.** By Lemma 2.3, to argue knowledge soundness, it is sufficient to argue the soundness error of the protocol. Fix $n := 2^\mu$, for any $(t; [f]) \notin \mathcal{L}(\mathcal{R}_{\text{LOOKUP}})$, denote by $f \in \mathbb{F}^n$ the evaluations of $f$ on $B_\mu$. Gabizon et. al. [44] showed that for any $h \in \mathbb{F}^{2n-1}$, it holds that

$$\{[f_i, f_i]\}_{i \in [n]} \cup \{[t_i, t_i \mod (n-1)+1]\}_{i \in [n]} \neq \{[h_i, h_i \mod (2n-1)+1]\}_{i \in [2n-1]},$$

since $t(0^\mu) = 0$ and $\mathcal{V}$ checks that $h(0^{\mu+1}) = 0$, with a similar argument as in the completeness proof, we have

$$\{[f(x), f(x)]\}_{x \in B_\mu} \cup \{[t(x), t\Delta_\mu(x)]\}_{x \in B_\mu} \neq \{[h(x), h\Delta_{\mu+1}(x)]\}_{x \in B_{\mu+1}}$$

and the multiset check relation does not hold. Therefore, the probability that $\mathcal{V}$ accepts is at most $\delta_{\text{mset},2}^{d,\mu+1}$ as claimed. \hfill $\square$

**Complexity.** Let $f, F := (g_1, g_2, h, h\Delta_{\mu+1}) \in \mathcal{F}^{(\leq \mu)} \times \mathcal{F}^{(\leq \mu+1)}$ be the polynomials defined in the construction. We analyze the complexity of the PIOP for $\mathcal{R}_{\text{LOOKUP}}$ with respect to $f \in \mathcal{F}^{(\leq \mu)}$.

- The prover time is $t_{\text{lookup}}^f = t_{\text{mset}}^F = O(d \log^2 d \cdot 2^{\mu}) \mathbb{F}$-ops.
- The verifier time is $t_{\text{lookup}}^v = t_{\text{mset}}^v = O(\mu)$.
- The query complexity is $q_{\text{lookup}}^f = 1 + q_{\text{mset}}^F = \mu + 3$.
- The round complexity and the number of proof oracles is $r_{\text{lookup}}^f = 1 + r_{\text{mset}}^F = \mu + 2$.
- The number of field elements sent by $\mathcal{P}$ is $n_{\text{lookup}}^f = n_{\text{mset}}^F = \mu$.
- The size of the proof oracles is $p_{\text{lookup}}^f = 2^{\mu+1} + p_{\text{mset}}^F = O(2^{\mu})$ where $2^{\mu+1}$ is the oracle size of $h$. The size of the witness is $O(2^\mu)$. 

29
3.8 Batch openings

This section describes a batching protocol proving the correctness of multiple multivariate polynomial evaluations. Essentially, the protocol reduces multiple oracle queries to different polynomials into a singe query to a multivariate oracle. The batching protocol is helpful for HyperPlonk to enable efficient batch evaluation openings. In particular, the SNARK prover only needs to compute a single multilinear PCS evaluation proof, even if there are multiple PCS evaluations.

We note that Thaler [73, §4.5.2] shows how to batch two evaluations of a single multilinear polynomial. The algorithm can be generalized for multiple evaluations of different multilinear polynomials. However, the prover time complexity is $O(k^2 \mu \cdot 2^\mu)$ where $k$ is the number of evaluations, and $\mu$ is the number of variables. In comparison, our algorithm achieves complexity $O(k \cdot 2^\mu)$ which is $k\mu$-factor faster. Note that $O(k \cdot 2^\mu)$ is already optimal as the prover needs to take $O(k \cdot 2^\mu)$ time to evaluate $\{f_i(z_i)\}_{i \in [k]}$ before batching.

**Definition 3.7** (BatchEval relation). The relation $R^k_{\text{BATCH}}$ is the set of all tuples $(x; w) = ((z_i)_{i \in [k]}, (y_i)_{i \in [k]}, ([f_i])_{i \in [k]}; (f_i)_{i \in [k]})$ where $z_i \in \mathbb{F}^n$, $y_i \in \mathbb{F}$, $f_i \in \mathbb{F}_2^{(\leq d)}$ and $f_i(z_i) = y_i$ for all $i \in [k]$.

**Remark 3.1.** The polynomials $\{f_i\}_{i \in [k]}$ are not necessarily distinct. E.g., to evaluate a single polynomial $f$ at $k$ distinct points, we can set $f_1 = f_2 = \cdots = f_k = f$.

**Remark 3.2.** The polynomials $\{f_i\}_{i \in [k]}$ are all $\mu$-variate. This is without loss of generality. E.g., suppose one of the evaluated polynomial $f_j'$ has only $\mu - 1$ variables, we can define $f_j(Y, X) = Y \cdot f_j'(X) + (1 - Y) \cdot f_j(X)$ which is essentially $f_j'$ but with $\mu$ variables. The same trick easily extends to $f_j'$ with arbitrary $\mu' < \mu$ variables.

**Construction.** For ease of exposition, we consider the case where $f_1, \ldots, f_k$ are multilinear. We emphasize that the same techniques can be extended for multi-variate polynomials.

Assume w.l.o.g that $k = 2^\ell$ is a power of 2. We observe that $R^k_{\text{BATCH}}$ is essentially a ZeroCheck relation over the set $Z := \{z_i\}_{i \in [k]} \subseteq \mathbb{F}^n$, that is, for every $i \in [k], f_i(z_i) - y_i = 0$. Nonetheless, $Z$ is outside the boolean hypercube, and we cannot directly reuse the ZeroCheck PIOP.

The key idea is to interpret each zero constraint as a sumcheck via multilinear extension, so that we can work on the boolean hypercube later. In particular, for every $i \in [k]$, we want to constrain $f_i(z_i) - y_i = 0$. Since $f_i$ is multilinear, by definition of multilinear extension, this is equivalent to constraining that

$$c_i := \left( \sum_{b \in B_\mu} f_i(b) \cdot eq(b, z_i) \right) - y_i = 0. \quad (10)$$

Note that equation (10) holds for every $i \in [k]$ if and only if the polynomial

$$\sum_{i \in [k]} eq(Z, \langle i \rangle) \cdot c_i$$

is identically zero, where $\langle i \rangle$ is $\ell$-bit representation of $i - 1$. By Lemma 2.2, it is sufficient to check that for a random vector $t \in \mathbb{F}_2^\ell$, it holds that

$$\sum_{i \in [k]} eq(t, \langle i \rangle) \cdot c_i = \sum_{i \in [k]} eq(t, \langle i \rangle) \cdot \left[ \left( \sum_{b \in B_\mu} f_i(b) \cdot eq(b, z_i) \right) - y_i \right] = 0. \quad (11)$$
Next, we arithmetize equation (11) and make it an algebraic formula. For every \((i, b) \in [k] \times B_\mu\), we set value \(g_{i, b} := eq(t, \langle i \rangle) \cdot f_i(b)\), and define an MLE \(\tilde{g}\) for \((g_{i, b})_{i \in [k], b \in B_\mu}\) such that \(\tilde{g}((i), b) = g_{i, b} \forall (i, b) \in [k] \times B_\mu\); similarly, we define an MLE \(\tilde{eq}\) for \((eq(b, z_i))_{i \in [k], b \in B_\mu}\) where \(\tilde{eq}((i), b) = eq(b, z_i) \forall (i, b) \in [k] \times B_\mu\). Let \(s := \sum_{i \in [k]} eq(t, \langle i \rangle) \cdot y_i\), then equation (11) can be rewritten as

\[
\sum_{i \in [k], b \in B_\mu} \tilde{g}((i), b) \cdot \tilde{eq}((i), b) = s .
\]

This is equivalent to prove a sumcheck claim for the degree-2 polynomial \(g^* := \tilde{g}(Y, X) \cdot \tilde{eq}(Y, X)\) over set \(B_{t+\mu}\). Hence we obtain the following PIOP protocol in Algorithm 4. Note that \(g^* = \tilde{g} \cdot \tilde{eq}\) is only with degree 2. Thus we can run a classic sumcheck without sending any univariate oracles.

**Algorithm 4** Batch evaluation of multi-linear polynomials

1: procedure **BATCHEVAL**([\(f_i \in F_{\mu}^{(\leq 1)}\), \(z_i \in F^\mu\), \(y_i \in F\)_{i=1}^k])
2: \(V\) sends \(P\) a random vector \(t \in \mathbb{F}_t\).
3: Define sum \(s := \sum_{i \in [k]} eq(t, \langle i \rangle) \cdot y_i\).
4: Let \(\tilde{g}\) be the MLE for \((g_{i, b})_{i \in [k], b \in B_\mu}\) where

\[
g_{i, b} := eq(t, \langle i \rangle) \cdot f_i(b) .
\]

5: Let \(\tilde{eq}\) be the MLE for \((eq(b, z_i))_{i \in [k], b \in B_\mu}\) such that \(\tilde{eq}((i), b) = eq(b, z_i)\).
6: \(P\) and \(V\) run a SumCheck PIOP for \((s, [g^*]; g^*) \in R_{SUM}\), where \(g^* := \tilde{g} \cdot \tilde{eq}\).
7: Let \((a_1, a_2) \in \mathbb{F}^{t+\mu}\) be the sumcheck challenge vector. \(P\) answers the oracle query \(\tilde{g}(a_1, a_2)\).
8: \(V\) evaluates \(\tilde{eq}(a_1, a_2)\) herself, and checks that

\[
\tilde{g}(a_1, a_2) \cdot \tilde{eq}(a_1, a_2)
\]

is consistent with the last message of the sumcheck.
9: end procedure

**Remark 3.3.** If the SNARK is using a homomorphic commitment scheme, to answer query \(\tilde{g}(a_1, a_2)\) the prover only needs to provide a single PCS opening proof for a \(\mu\)-ivariate polynomial

\[
g'(X) := \tilde{g}(a_1, X) = \sum_{i \in [k]} eq((i), a_1) \cdot eq(t, \langle i \rangle) \cdot f_i(X)
\]
on point \(a_2\). The verifier can evaluate \(\{eq((i), a_1) \cdot eq(t, \langle i \rangle)\}_{i \in [k]}\) in time \(O(k)\), and homomorphically compute \(g'\)’s commitment from the commitments to \(\{f_i\}_{i \in [k]}\), and checks the opening proof against \(g'\)’s commitment. Finally, the verifier checks that \(g'(a_2)\) matches the claimed evaluation \(\tilde{g}(a_1, a_2)\).

**Analysis.** The PIOP for \(R_{BATCH}\) is complete and knowledge-sound given the completeness and knowledge-soundness of the sumcheck PIOP.

Next, we analyze the complexity of the protocol: The prover time is \(O(k \cdot 2^\mu)\) as it runs a sumcheck PIOP for a polynomial \(g^* := \tilde{g} \cdot \tilde{eq}\) of degree 2 and \(\mu + \log k\) variables, where \(\tilde{g}\) and
\(\bar{eq}\) can both be constructed in time \(O(k \cdot 2^\mu)\). Note that this is already optimal as the prover anyway needs to take \(O(k \cdot 2^\mu)\) time to evaluate \(\{f_i(z_i)\}_{i \in [k]}\) before batching. The verifier takes time \(O(\mu + \log k)\) in the sumcheck; the sum \(s\) can be computed in time \(O(k)\); the evaluation \(\bar{eq}(a_1, a_2) = \sum_{i \in [k]} eq(a_1, \langle i \rangle) \cdot eq(\langle i \rangle, a_2)\) can be derived from \(a_1\) and the \(k\) evaluations \(\{eq(\langle i \rangle, a_2) = eq(a_2, z_i)\}_{i \in [k]}\) where each evaluation \(eq(a_2, z_i)\) takes time \(O(\mu)\). In summary, the verifier time is \(O(k\mu)\).

3.8.1 A more efficient batching scheme in a special setting

Sometimes one only needs to open a single multilinear polynomial at multiple points, where each point is in the boolean hypercube. In this setting, we provide a more efficient algorithm with complexity \(O(2^\mu)\) which is \(k\) times faster than Algorithm 4. We also note that the technique can be used to construct an efficient Commit-and-Prove SNARK scheme from multilinear commitments.

Recall the sumcheck equation (11) in the general batch opening scheme, when there is only one polynomial \(f\) and assume for simplicity that \(y_i = 0 \forall i \in [k]\)\footnote{The algorithm can be easily extended when \(y_i\) are non-zero.}, we can rewrite it as

\[
\sum_{i \in [k]} eq(t, \langle i \rangle) \cdot \left(\sum_{b \in B_\mu} f(b) \cdot eq(b, z_i)\right) = \sum_{b \in B_\mu} f(b) \left(\sum_{i \in [k]} eq(t, \langle i \rangle) \cdot eq(b, z_i)\right).
\]

Denote by \(d_i = eq(t, \langle i \rangle)\). The above is essentially a sumcheck for polynomial \(f \cdot \bar{eq}^*\) on set \(B_\mu\), where

\[
\bar{eq}^* (X) := \sum_{i \in [k]} d_i \cdot eq(X, z_i).
\]

Thus we can reduce the batching argument to a PCS opening on polynomial \(f\).

In the sumcheck protocol, in each round \(\mu - i + 1 \in [\mu]\), the prover needs to evaluate a degree-2 polynomial \(r_i(X)\) on point \(x_i \in \{0, 1, 2\}\), where

\[
r_i(X) := \sum_{b \in B_{i-1}} f(b, X, \alpha) \cdot \bar{eq}^* (b, X, \alpha) \tag{12}
\]

and \(\alpha = (\alpha_{i+1}, \ldots, \alpha_{\mu})\) are the round challenges. Note that the evaluation \(f(b, x_i, \alpha)\) is easy to obtain by maintaining a table \(f(B_{i-1}, \{0, 1, 2\}, \alpha)\) as in Algorithm 1. Next we argue that the evaluation \(r_i(x_i)\) can be computed in time \(O(k)\) given the evaluations \(f(B_{i-1}, \{0, 1, 2\}, \alpha)\). Since there are \(\mu\) rounds and the complexity for maintaining the table is \(O(2^\mu)\), the total complexity is \(O(2^\mu + k\mu)\).

We observe that in equation (12), since \(\{z_i\}_{i \in [k]}\) are in the boolean hypercube, and

\[
\bar{eq}^* (b, X, \alpha) = \sum_{j \in [k]} d_j \cdot eq((b, X, \alpha), z_j)
\]

\[
= \sum_{j \in [k]} d_j \cdot eq(b, z_j[1..i - 1]) \cdot eq(X, z_j[i]) \cdot eq(\alpha, z_j[i + 1..]),
\]

by definition of \(eq\), there are at most \(k\) choices of \(b\) where \(\bar{eq}^* (b, X, \alpha)\) is non-zero. In particular, the \(\ell\)-th (\(1 \leq \ell \leq k\)) such vector is \(c_\ell := z_j[1..i - 1]\) such that

\[
\bar{eq}^* (c_\ell, X, \alpha) = \sum_{j \in [k]} d_j \cdot eq(c_\ell[1..i - 1], z_j[1..i - 1]) \cdot eq(X, z_j[i]) \cdot eq(\alpha, z_j[i + 1..]).
\]
we note that for each $j \in [k]$, the value $eq(\alpha, z_j[i + 1..])$ can be maintained dynamically; the value $eq(X, z_j[i])$ can be computed in time $O(1)$. Moreover, $eq(z_{i}[1..i - 1], z_{j}[1..i - 1])$ equals 1 if $z_{i}[1..i - 1] = z_{j}[1..i - 1]$ and equals 0 otherwise. In summary, all non-zero values $\{eq^\ell(c_{\ell}, X, \alpha)\}_{\ell \in [k]}$ can be computed in a batch in time $O(k)$. Therefore for each $x_i \in \{0, 1, 2\}$, one can evaluate $r_i(x_i)$ from evaluations $\{f(c_{\ell}, x_i, \alpha)\}_{\ell \in [k]}$ in time $O(k)$, by evaluating $\{eq^\ell(c_{\ell}, x_i, \alpha)\}_{\ell \in [k]}$ first and computing the inner product between $(eq^\ell(c_{\ell}, x_i, \alpha))_{\ell \in [k]}$ and $(f(c_{\ell}, x_i, \alpha))_{\ell \in [k]}$.

**Applications to Commit-and-Prove SNARKs.** Our batching scheme is helpful for building Commit-and-Prove SNARKs (CP-SNARKs) from multilinear commitments. In the setting of CP-SNARKs, given two commitments $C_f, C_g$ that commit to vectors $f, g \in \mathbb{F}^m, g \in \mathbb{F}^m (m \leq n)$, and given two sets $I_f \subseteq [n], I_g \subseteq [m]$, one needs to prove that the values of $f(I_f)$ is consistent with $g(I_g)$. This problem can be solved using a variant of our special batching scheme with complexity $O(n)$.

For simplicity suppose that $n = m$,\(^8\) and we assume w.l.o.g that $n = 2^\mu$. The idea is to view $f, g$ as the evaluations of polynomials $f, g \in \mathbb{F}^{\mu,1}$ on the boolean hypercube $B_\mu$. Then the commitments $C_f, C_g$ can be instantiated with multilinear commitments to polynomials $f, g$ respectively. The relation that $f(I_f) = g(I_g)$ is a slightly more general version of the batching relation: let $k = |I_f| = |I_g|$, it is equivalent to prove that $f(z_i) = g(u_i)$ for all $i \in [k]$, where $z_i, u_i \in B_\mu$ map to the $i$-th index of set $I_f, I_g$ respectively.

Similar to equation (11), we can define a sumcheck relation

$$
\sum_{i \in [k]} eq(t, \langle i \rangle) \cdot \left[ \left( \sum_{b \in B_\mu} f(b) \cdot eq(b, z_i) \right) - \left( \sum_{b \in B_\mu} g(b) \cdot eq(b, u_i) \right) \right]
\quad = \sum_{b \in B_\mu} f(b) \left( \sum_{i \in [k]} eq(t, \langle i \rangle) \cdot eq(b, z_i) \right) - \sum_{b \in B_\mu} g(b) \left( \sum_{i \in [k]} eq(t, \langle i \rangle) \cdot eq(b, u_i) \right) = 0,
$$

which is essentially a sumcheck for the degree-2 polynomial $h := f \cdot e\tilde{a}_f - g \cdot e\tilde{a}_g$ on set $B_\mu$, where

$$
eq \tilde{a}_f(X) := \sum_{i \in [k]} eq(t, \langle i \rangle) \cdot eq(X, z_i), \quad e\tilde{a}_g(X) := \sum_{i \in [k]} eq(t, \langle i \rangle) \cdot eq(X, u_i).
$$

We can use the same sumcheck algorithm underlying the special batching scheme. The complexity is $O(2^\mu)$. The CP-SNARK proving is then reduced to two PCS openings, one for commitment $C_f$ and one for $C_g$.

### 4 HyperPlonk: Plonk on the boolean hypercube

Equipped with the building blocks in Section 3, we now describe the Polynomial IOP for HyperPlonk. In Section 4.1, we introduce $\mathcal{R}_{\text{PLONK}}$ — an indexed relation on the boolean hypercube that generalizes the vanilla Plonk constraint system \cite{plonk}. We present a Polynomial IOP protocol for $\mathcal{R}_{\text{PLONK}}$ and analyze its security and efficiency in Section 4.2.

---

\(^8\)the same technique applies for $n \neq m$
4.1 Constraint systems

**Notation.** For any \( m \in \mathbb{Z} \) and \( i \in [0, 2^m) \), we use \( \langle i \rangle_m = v \in B_m \) to denote the \( m \)-bit binary representation of \( i \), that is, \( i = \sum_{j=1}^{m} v_j \cdot 2^{j-1} \).

**Definition 4.1** (HyperPlonk indexed relation). Fix public parameters \( g_p := (F, \ell, n, \ell_w, \ell_q, f) \) where \( F \) is the field, \( \ell = 2^\nu \) is the public input length, \( n = 2^\mu \) is the number of constraints, \( \ell_w = 2^\nu q, \ell_q = 2^\nu q \) are the number of witnesses and selectors per constraint\(^9\), and \( f : \mathbb{F}^{\ell+\ell_w} \rightarrow \mathbb{F} \) is an algebraic map with degree \( d \). The indexed relation \( R_{\text{PLONK}} \) is the set of all tuples

\[
(i; x; w) = ((q, \sigma); (p, [[w]]); w),
\]

where \( \sigma : B_{\mu+\nu_w} \rightarrow B_{\mu+\nu_w} \) is a permutation, \( q \in F_{\mu+\nu_q}, p \in F_{\mu+\nu_q}, w \in F_{\mu+\nu_q} \), such that

- the wiring identity is satisfied, that is, \( (\sigma; ([w]), [[w]]); w) \in R_{\text{PERM}} \) (Definition 3.5);
- the gate identity is satisfied, that is, \( (\langle \hat{f}\rangle; \hat{f}) \in R_{\text{ZERO}} \) (Definition 3.2), where the virtual polynomial \( \hat{f} \in F^{(\leq d)}_\mu \) is defined as

\[
\hat{f}(X) := f(q(0)_{\nu_q}, X), \ldots, q(\ell_q - 1)_{\nu_q}, X), w(0)_{\nu_w}, X), \ldots, w(\ell - 1)_{\nu_w}, X));
\]

- the public input is consistent with the witness, that is, the public input polynomial \( p \in F_{\nu}^{(\leq 1)} \) is identical to \( w(0^{\mu+\nu_w-\nu}, X) \in F^{(\leq 1)}_\nu \).

\( R_{\text{PLONK}} \) is general enough to capture many computational models. In the introduction, we reviewed how \( R_{\text{PLONK}} \) captures simple arithmetic circuits. \( R_{\text{PLONK}} \) can be used to capture higher degree circuits with higher arity and more complex gates, including state machine computations.

**State machines.** \( R_{\text{PLONK}} \) can model state machine computations, as shown by Gabizon and Williamson [45]. A state machine execution with \( n - 1 \) steps starts with an initial state \( \text{state}_0 \in F^k \) where \( k \) is the width of the state vector. In each step \( i \in [0, n-1) \), given input the previous state \( \text{state}_i \) and an online input \( \text{inp}_i \in F \), the state machine executes a transition function \( f \) and outputs \( \text{state}_{i+1} \in F^w \). Let \( \mathcal{T} := (\text{state}_0, \ldots, \text{state}_{n-1}) \) be the execution trace and define \( \text{inp}_{n-1} := \perp \), we say that \( \mathcal{T} \) is valid for input \( (\text{inp}_0, \ldots, \text{inp}_{n-1}) \) if and only if (i) \( \text{state}_{n-1}[0] = 0^k \), and (ii) \( \text{state}_{i+1} = f(\text{state}_i, \text{inp}_i) \) for all \( i \in [0, n-1) \).

We build a HyperPlonk indexed relation that captures the state machine computation. W.l.o.g we assume that \( n = 2^\mu \) for some \( \mu \in \mathbb{N} \).\(^{10}\) Let \( \nu_w \) be the minimal integer such that \( 2^\nu w > 2k \). We also assume that there is a low-depth algebraic predicate \( f_s \) that captures the transition function \( f \), that is, \( f_s(\text{state}', \text{state}, \text{inp}) = 0 \) if and only if \( \text{state}' = f(\text{state}, \text{inp}) \). For each \( i \in [0, n) \):

- the online input at the \( i \)-th step is \( \text{inp}_i := w(0)_{\nu_w}, \langle i \rangle_\mu) \);
- the input state of step \( i \) is \( \text{state}_{\text{inp}, i} := \left[ w(1)_{\nu_w}, \langle i \rangle_\mu) , \ldots, w(\ell)_{\nu_w}, \langle i \rangle_\mu) \right] \);
- the output state of step \( i \) is \( \text{state}_{\text{out}, i} := \left[ w(\ell + 1)_{\nu_w}, \langle i \rangle_\mu) , \ldots, w(2\ell)_{\nu_w}, \langle i \rangle_\mu) \right] \);
- the selector for step \( i \) is \( q_i := q(\langle i \rangle_\mu) \);

\(^9\)We can pad zeroes if the actual number is not a power of two.

\(^{10}\)We can pad with dummy states if the number of steps is not a power of two.
the transition and output correctness are jointly captured by a high-degree algebraic map \( f' \),
\[
f'(\text{inp}_i, \text{state}_{\text{in},i}, \text{state}_{\text{out},i}; q_i) := (1 - q_i) \cdot f_\ast(\text{state}_{\text{out},i}, \text{state}_{\text{in},i}, \text{inp}_i) + q_i \cdot \text{state}_{\text{in},i}[0].
\]
For all \( i \in [0, n - 1) \), we set \( q_i = 0 \) so that \( \text{state}_{i+1} = f_i(\text{state}_i, \text{inp}_i) \) if and only if
\[
f'(\text{inp}_i, \text{state}_{\text{in},i}, \text{state}_{\text{out},i}; q_i) = f_i(\text{state}_{\text{out},i}, \text{state}_{\text{in},i}, \text{inp}_i) = 0;
\]
we set \( q_{n-1} = 1 \) so that \( \text{state}_{\text{in},n-1}[0] = 0 \) if and only if
\[
f'(\text{inp}_{n-1}, \text{state}_{\text{in},n-1}, \text{state}_{\text{out},n-1}; q_{n-1}) = \text{state}_{\text{in},n-1}[0] = 0.
\]

Note that we also need to enforce equality between the \( i \)-th input state and the \((i - 1)\)-th output state for all \( i \in [n - 1] \). We achieve it by fixing a permutation \( \sigma \) and constraining that the witness assignment is invariant after applying the permutation.

**Remark 4.1.** We can halve the size of the witness and remove the permutation check by using the polynomial shifting technique in Section 3.7. Specifically, we can remove output state columns \( \text{state}_{\text{out},i} \) and replace it with \( \text{state}_{\text{in},i+1} \) for every \( i \in [0, n) \).

### 4.2 The PolyIOP protocol

In this Section, we present a multivariate PIOP for \( \mathcal{R}_{\text{PLONK}} \) that removes expensive FFTs.

**Construction.** Intuitively, the PIOP for \( \mathcal{R}_{\text{PLONK}} \) builds on a zero-check PIOP (Section 3.2) for custom algebraic gates and a permutation-check PIOP (Section 3.5) for copy constraints; consistency between the public input and the online witness is achieved via a random evaluation check between the public input polynomial and the witness polynomial.

Let \( \mathcal{G}_p := (\mathbb{F}, \ell, n, \ell_w, \ell_q, f) \) be the public parameters and let \( d := \deg(f) \). For a tuple \( (i; z; w) = ((q, \sigma); (p, \lceil |w| \rceil); w) \), we describe the protocol in Figure 2.

**Theorem 4.1.** Let \( \mathcal{G}_p := (\mathbb{F}, \ell, n, \ell_w, \ell_q, f) \) be the public parameters where \( \ell_w, \ell_q = O(1) \) are some constants. Let \( d := \deg(f) \). The construction in Figure 2 is a multivariate PolyIOP for relation \( \mathcal{R}_{\text{PLONK}} \) (Definition 4.1) with soundness error \( O\left(\frac{2^\mu + \mu}{|\mathbb{F}|}\right) \) and the following complexity:

- the prover time is \( t_{\mathcal{G}_p}^{\text{plonk}} = O(nd \log^2 d) \);
- the verifier time is \( t_{\mathcal{G}_p}^{\text{plonk}} = O(\mu + \ell) \);
- the query complexity is \( q_{\mathcal{G}_p}^{\text{plonk}} = 2\mu + 4 + \log \ell_w \), that is, \( 2\mu + \log \ell_w \) univariate oracle queries, 3 multilinear oracle queries, and 1 query to the virtual polynomial \( \tilde{f} \);
- the round complexity and the number of proof oracles is \( r_{\mathcal{G}_p}^{\text{plonk}} = 2\mu + 1 + \nu_w \);
- the number of field elements sent by the prover is \( n_{\mathcal{G}_p}^{\text{plonk}} = 2\mu \);
- the size of the proof oracles is \( p_{\mathcal{G}_p}^{\text{plonk}} = O(n) \); the size of the witness is \( n \ell_w \).

35
Indexer. \( I(q, \sigma) \) calls the permutation PIOP indexer \( ([s_{\text{id}}], [s_{\sigma}]) \leftarrow I_{\text{perm}}(\sigma) \). The oracle output is \( ([q], [s_{\text{id}}], [s_{\sigma}]) \), where \( q \in \mathcal{F}_{\mu + \nu q}, s_{\text{id}}, s_{\sigma} \in \mathcal{F}_{\mu + \nu w} \).

The protocol. \( P(gp, i, p, w) \) and \( V(gp, p, [q], [s_{\text{id}}], [s_{\sigma}]) \) run the following protocol.

1. \( P \) sends \( V \) the witness oracle \([w]\) where \( w \in \mathcal{F}_{\mu + \nu w} \).
2. \( P \) and \( V \) run a PIOP for the gate identity, which is a zero-check PIOP (Section 3.2) for \( ([\tilde{f}]; \tilde{f}) \in \mathcal{R}_{\text{ZERO}} \) where \( \tilde{f} \in \mathcal{F}_{\mu + \nu w}^{(\leq d)} \) is as defined in Equation 13.
3. \( P \) and \( V \) run a PIOP for the wiring identity, which is a permutation PIOP (Section 3.5) for \( (\sigma; ([w]), ([w])); (w, w) \) \( \in \mathcal{R}_{\text{PERM}} \).
4. \( V \) checks the consistency between witness and public input. It samples \( r \leftarrow \mathbb{F}^\nu \), queries \([w] \) on input \((0^{\mu + \nu w - \nu}, r)\), and checks \( p(r) \equiv w((0)^{\mu + \nu w - \nu}, r) \).

Figure 2: PIOP for \( \mathcal{R}_{\text{PLONK}} \).

Remark 4.2. Two separate sumcheck PIOPs are underlying the HyperPlonk PIOP. We can batch the two sumchecks into one by random linear combination. The optimized protocol has round complexity \( \mu + 1 + \log \ell_w \), and the number of field elements sent by the prover is \( \mu \). The query complexity \( \mu + 3 + \log \ell_w \), that is, \( \mu + \log \ell_w \) univariate queries, 2 multilinear queries, and 1 queries to the virtual polynomial \( \tilde{f} \).

Remark 4.3. The prover’s memory consumption is linear to the number of constraints. For space-bounded provers, we can split the proving work to multiple parallel parties or apply the techniques from [24] to obtain a space-efficient prover with quasilinear proving time. We leave concrete specifications of space-efficient HyperPlonk provers as future work.

Lemma 4.2. The PIOP in Figure 2 is perfectly complete.

Proof. For any \( ((q, \sigma); (p, [w])); w \) \( \in \mathcal{R}_{\text{PLONK}} \), by Definition 4.1, it holds that

- \( ([\tilde{f}]; \tilde{f}) \in \mathcal{R}_{\text{ZERO}} \), thus \( V \) passes the check in Step 2 as the ZeroCheck PIOP is complete;
- \( (\sigma; ([w]), ([w])); w \) \( \in \mathcal{R}_{\text{PERM}} \), thus \( V \) passes the check in Step 3 as the permutation PIOP is complete;
- the public input polynomial \( p \in \mathcal{F}_\nu^{(\leq 1)} \) is identical to \( w(0^{\mu + \nu w - \nu}, X) \in \mathcal{F}_\nu^{(\leq 1)} \), thus their evaluations are always the same, and \( V \) passes the check in Step 4.

In summary, the lemma holds as desired.

Lemma 4.3. Let \( gp := (\mathbb{F}, \ell = 2^\nu, n = 2^\mu, \ell_w = 2^{\nu w}, \ell_q, f) \) be the public parameters and let \( d := \deg(f) \) The PIOP in Figure 2 has soundness error

\[
\delta_{\text{plonk}}^{gp} := \max \left\{ \delta_{\text{zero}}^{d, \mu + \nu w}, \delta_{\text{perm}}^{1, \mu + \nu w}, \frac{\nu}{|\mathbb{F}|} \right\}.
\]

36
Proof. For any \(((q, \sigma); (p, \{w\})) \notin L(R_{\text{PLONK}})\), that is, \(((q, \sigma); (p, \{w\}); w) \notin R_{\text{PLONK}}\), at least one of the following conditions holds:

- \(\langle \tilde{f} \rangle; \tilde{f} \rangle \notin R_{\text{ZERO}}\);
- \((\sigma; \{[w]\}, \{[w]\}); w) \notin R_{\text{PERM}}\);
- \(p(X) \neq w(0^\nu, X)\);

In the first condition, the probability that \(V\) passes the ZeroCheck in Step 2 is at most \(\delta_{\text{zero}}\); in the second condition, the probability that \(V\) passes the permutation check in Step 3 is at most \(\delta_{\text{perm}}\); in the last condition, by Lemma 2.2, \(V\) passes the evaluation check in Step 4 with probability at most \(\nu/|F|\). In summary, for any \(((q, \sigma); (p, \{w\}); w) \notin R_{\text{PLONK}}\), the probability that \(V\) accepts is at most \(\max\{\delta_{\text{zero}}, \delta_{\text{perm}}, \nu/|F|\}\) as claimed. 

Zero knowledge. We refer to Appendix A for the zero-knowledge version of the HyperPlonk PIOP.

5 HyperPlonk+: HyperPlonk with Lookup Gates

This section illustrates how to integrate lookup gates into the HyperPlonk constraint system. Then we present and analyze a Polynomial IOP protocol for the extended relation.

5.1 Constraint systems

The HyperPlonk+ indexed relation \(R_{\text{PLONK}+}\) is built on \(R_{\text{PLONK}}\) (Definition 4.1). The difference is that \(R_{\text{PLONK}+}\) further enables a set of non-algebraic constraints enforcing that some function over the witness values belongs to a preprocessed table. We illustrate via a simple example. Suppose we capture a fan-in-2 circuit with \(n\) addition/multiplication gates using relation \(R_{\text{PLONK}}\). We need to further constrain that for a subset of gates, the sum of two input wires should be in the range \([0, \ldots, B]\). What we can do is to set up a preprocessed table \(\text{table} = \{0, 1, \ldots, B\}\) and a selector \(q_{lk} \in \mathbb{F}^n\) so that for every \(i \in [n]\), \(q_{lk}(i) = 1\) if the \(i\)-th gate has a range-check, and \(q_{lk}(i) = 0\) otherwise. Then we prove a lookup relation that for all \(i \in [n]\), the value \(q_{lk}(i) \cdot (w_1(i) + w_2(i))\) is in \(\text{table}\), where \(w_1(i), w_2(i)\) are the first and the second input wire of gate \(i\).

We generalize the idea above and enable enforcing arbitrary algebraic functions (over the selectors and witnesses) to be in the table. Namely, the index further setups an algebraic functions \(f_{lk}\). Each constraint is of the form

\[f_{lk}(q_{lk}(\langle 0 \rangle, \langle i \rangle), \ldots, q_{lk}(\langle \ell_{lk} - 1 \rangle, \langle i \rangle), w(\langle 0 \rangle, \langle i \rangle), \ldots, w(\langle \ell_w - 1 \rangle, \langle i \rangle)) \in \text{table}\]

where \(\ell_{lk}\) is the number of selectors, \(\ell_w\) is the number of witness wires and \(\langle i \rangle\) is the binary representation of \(i\). Note that the constraint in the previous paragraph is a special case where \(f_{lk} = q_{lk}(i) \cdot (w_1(i) + w_2(i))\). We formally define the relation below.

**Definition 5.1** (HyperPlonk+ indexed relation). Let \(gp_1 := (F, \ell, n, \ell_w, \ell_q, f)\) be the public parameters for relation \(R_{\text{PLONK}}\) (Definition 4.1). Let \(gp_2 := (\ell_{lk}, f_{lk})\) be the additional public parameters
where \( \ell_k = 2^{\nu k} \) is the number of lookup selectors and \( f_k : \mathbb{F}^{\ell_k + \ell_w} \rightarrow \mathbb{F} \) is an algebraic map. The indexed relation \( \mathcal{R}_{\text{PLONK}^+} \) is the set of all triples

\[
(i; x; w) = ((i_1, i_2); (p, [[w]]); w)
\]

where \( i_2 := (\text{table} \in \mathbb{F}^{n-1}, q_{ik} \in \mathcal{F}_{\mu + \nu_k}^{(\leq)} ) \) such that

- \((i_1; x; w) \in \mathcal{R}_{\text{PLONK}} \);
- there exists \( \text{addr} : B_k \rightarrow [1, 2^{\nu}] \) such that \((\text{table}; [[g]]; (g, \text{addr})) \in \mathcal{R}_{\text{LOOKUP}} \) (Definition 3.6), where \( g \in \mathcal{F}_{\mu}^{(\leq \deg(f_k))} \) is defined as

\[
g(X) := f_k(q_{ik}(0)_{\nu_k}, X), \ldots, q_{ik}(\ell_k - 1)_{\nu_k}, X), w((0)_{\nu_w}, X), \ldots, w((\ell_w - 1)_{\nu_w}, X)) \quad (14)
\]

Remark 5.1 (Supporting vector lookups). We can generalize \( \mathcal{R}_{\text{PLONK}^+} \) to support vector lookups where each “element” in the table is a vector rather than a single field element. Let \( k \in \mathbb{N} \) be the length of the vector. The lookup table is \( \text{table} \in \mathbb{F}^{k \times (n - 1)} \); the lookup function \( f_k : \mathbb{F}^{2^{\nu k} + 2^{\nu w}} \rightarrow \mathbb{F}^k \) is an algebraic map that outputs \( k \) field elements.

Remark 5.2 (Supporting multiple tables). We can generalize \( \mathcal{R}_{\text{PLONK}^+} \) to support multiple lookup tables. In particular, the index \( i_2 \) can specify \( k > 1 \) lookup tables \( \text{table}_1, \ldots, \text{table}_k \) and \( k \) lookup functions \( f^{(1)}_k, \ldots, f^{(k)}_k \); and we require that all of the \( k \) lookup relations hold.

5.2 The PolyIOP protocol

Construction. The PIOP for \( \mathcal{R}_{\text{PLONK}^+} \) is a combination of the PIOP for \( \mathcal{R}_{\text{PLONK}} \) and the PIOP for a lookup relation (Section 3.7). Let \( \text{gp} := (\text{gp}_1, \text{gp}_2) \) be the public parameters where \( \text{gp}_1 := (\mathbb{F}, \ell, \mu, \ell_w, \ell_q, f) \) and \( \text{gp}_2 := (\ell_k, f_k) \). We denote \( d_k := \deg(f_k) \). For a tuple \((i; x; w) = ((i_1, i_2); (p, [[w]]); w) \) where \( i_2 := (\text{table} \in \mathbb{F}^{n-1}, q_{ik} \in \mathcal{F}_{\mu + \nu_k}^{(\leq)}) \) we describe the protocol in Figure 3.

Indexer. \( I(i_1, i_2 = (\text{table}, q_{ik})) \) calls the HyperPlonk PIOP indexer \( \text{vp}_{\text{plonk}} \leftarrow I_{\text{plonk}}(i_1) \), and calls the Lookup PIOP indexer \( \text{vp}_l \leftarrow I_{\text{lookup}}(\text{table}) \). The oracle output is \( \text{vp} := ([[g_{ik}]), \text{vp}_l, \text{vp}_{\text{plonk}}) \).

The protocol. \( \mathcal{P}(\text{gp}, i, p, w) \) and \( \mathcal{V}(\text{gp}, p, \text{vp}) \) run the following protocol.

1. \( \mathcal{P} \) sends \( \mathcal{V} \) the witness oracle \([w]\) where \( w \in \mathcal{F}_{\mu + \nu_w}^{(\leq)} \).
2. Run a HyperPlonk PIOP (Section 4.2) for \((i_1; x; w) \in \mathcal{R}_{\text{PLONK}} \).
3. Run a lookup PIOP (Section 3.7) for \((\text{table}; [[g]]) \in \mathcal{L}(\mathcal{R}_{\text{LOOKUP}}) \) where \( g \in \mathcal{F}_{\mu}^{(\leq d_k)} \) is as defined in Equation 14.

Figure 3: PIOP for \( \mathcal{R}_{\text{PLONK}^+} \).

Theorem 5.1. Let \( \text{gp} := (\text{gp}_1, \text{gp}_2) \) be the public parameters, where \( \text{gp}_1 := (\mathbb{F}, \ell, \mu, \ell_w, \ell_q, f) \) and \( \ell_w, \ell_q = O(1) \) are some constants; \( \text{gp}_2 := (\ell_k, f_k) \) and \( \ell_k = O(1) \) is some constant. Let \( d' := \max(\deg(f), \deg(f_k)) \) and let \( g \) be the polynomial defined in Equation 14. The construction in Figure 3 is a multivariate PolyIOP for relation \( \mathcal{R}_{\text{PLONK}^+} \) with soundness error \( O\left(\frac{2^\mu + d'^\mu}{|\mathbb{F}|}\right) \) and the following complexity:
• The prover time is $tp_{\text{plonk}^+}^{\text{gp}} = tp_{\text{plonk}}^{\text{gp}} + tp_{\text{ikup}}^{\vartheta} = \mathcal{O}(nd' \log^2 d')$ $\mathbb{F}$-ops.

• The verifier time is $tv_{\text{plonk}^+}^{\text{gp}} := tv_{\text{plonk}}^{\text{gp}} + tv_{\text{ikup}}^{\vartheta} = \mathcal{O}(\mu + \ell)$ $\mathbb{F}$-ops.

• The query complexity is $q_{\text{plonk}^+}^{\text{gp}} = q_{\text{plonk}}^{\text{gp}} + q_{\text{ikup}}^{\vartheta} = 3\mu + 7 + \log \ell_w$, that is, $3\mu + \log \ell_w$ univariate oracle queries, 5 multilinear oracle queries, 1 query to the virtual polynomial $\tilde{f}$, and 1 query to the virtual polynomial $g$ defined in Equation 14.

• The round complexity and the number of proof oracles is $rc_{\text{plonk}^+}^{\text{gp}} = rc_{\text{plonk}}^{\text{gp}} + rc_{\text{ikup}}^{\vartheta} = 3\mu + 3 + \log \ell_w$.

• The number of field elements sent by $P$ is $3\mu$.

• The size of the proof oracles is $\mathcal{O}(n)$; the size of the witness is $n\ell_w$.

**Remark 5.3.** Similar to Remark 4.2, there are 3 separate sumcheck PIOPs underlying the HyperPlonk$^+$ PIOP. By random linear combination, we can batch the 3 sumchecks into a single one. The optimized protocol has query complexity $\mu + 7 + \log \ell_w$, round complexity $\mu + 3 + \log \ell_w$, and the number of field elements sent by the prover is $\mu$.

**Remark 5.4.** We emphasize that the PolyIOP for $\mathcal{R}_{\text{PLONK}^+}$ naturally works for the more general versions of $\mathcal{R}_{\text{PLONK}^+}$ that involve vector lookups (Remark 5.1) or multiple tables (Remark 5.2). Because we can transform the problem of building PIOPs for the more general relations to the problem of building PIOPs for $\mathcal{R}_{\text{PLONK}^+}$ by applying the randomization and domain separation techniques in Section 4 of [44].

**Lemma 5.2.** The PIOP in Figure 3 is perfectly complete.

**Proof.** For any $((i_1, \text{table}, q_{lk}); (p, [[w]]); w) \in \mathcal{R}_{\text{PLONK}^+}$, by Definition 5.1, it holds that

• $(i_1; x; w) \in \mathcal{R}_{\text{PLONK}}$, thus $V$ passes the check in Step 2 as the HyperPlonk PIOP is complete;

• $(\text{table}; [[g]]) \in \mathcal{L}(\mathcal{R}_{\text{LOOKUP}})$, thus $V$ passes the check in Step 3 as the lookup PIOP is complete.

In summary, the lemma holds as desired.

**Lemma 5.3.** Let $\text{gp} := (\text{gp}_1, \text{gp}_2)$ be the public parameters. Let $n = 2^\mu \in \text{gp}_1$ denote the number of constraints. Let $f_k \in \text{gp}_2$ be the lookup gate map and set $d_k := \deg(f_k)$. The PIOP in Figure 3 has soundness error

$$\delta_{\text{plonk}^+}^{\text{gp}} := \max \left\{ \delta_{\text{plonk}}^{\text{gp}_1}, \delta_{\text{ikup}}^{d_k, \mu} \right\}.$$  

**Proof.** For any $((i_1, \text{table}, q_{lk}); (p, [[w]])) \notin \mathcal{L}(\mathcal{R}_{\text{PLONK}^+})$, that is, $((i_1, \text{table}, q_{lk}); (p, [[w]]); w) \notin \mathcal{R}_{\text{PLONK}}$, at least one of the following conditions holds:

• $(i_1; x; w) \notin \mathcal{R}_{\text{PLONK}}$;

• $(\text{table}; [[g]]) \notin \mathcal{L}(\mathcal{R}_{\text{LOOKUP}})$, where $g \in \mathcal{F}_\mu^{(\leq d_k)}$ is as defined in Equation 14.

For the first case, the probability that $V$ accepts in the HyperPlonk PIOP is at most $\delta_{\text{plonk}}^{\text{gp}_1}$; for the second case, the probability that $V$ passes the lookup check is at most $\delta_{\text{ikup}}^{d_k, \mu}$. Thus for every instance not in $\mathcal{L}(\mathcal{R}_{\text{PLONK}^+})$, the probability that $V$ accepts is at most $\max(\delta_{\text{plonk}}^{\text{gp}_1}, \delta_{\text{ikup}}^{d_k, \mu})$. 

39
Zero knowledge. We refer to Appendix A for the zero-knowledge version of the HyperPlonk+ PIOP.

6 Instantiation and evaluation

6.1 Implementation

We implement HyperPlonk as a library using about 5600 lines of RUST. Figure 4 highlights the building blocks contributing to our HyperPlonk code base. Our backend is built on top of the Arkworks [4]. Specifically, we adopted the finite field, elliptic curve, and polynomial libraries from this project. We then build our PIOP libraries, including our core zero and permutation checks, and use merlin transcript [40] to turn it into a non-interactive protocol. We also implement a multilinear KZG commitment scheme variant that is compatible with our batch-evaluation PIOP.

Our implementation is highly modular: one may switch between different elliptic curves, other multilinear polynomial commitment schemes and various circuit frontends within our framework.

The current version of our code base has a few limitations, which do not affect the benchmarks reported in this section. Firstly, it is built for benchmarking purposes with mock circuits, but we aim to support Halo2 and Jellyfish arithmetization as frontends. Secondly, we are not yet supporting lookup tables and thus HyperPlonk+.

![Figure 4: Stack of libraries comprising HyperPlonk. The components in grey we implemented ourselves. The arithmetization frontends have not yet been linked to the implementation.](image)

6.2 Evaluation

We benchmark HyperPlonk on an AWS EC2 instance running Ubuntu 20.04. The server has 64 cores (AMD EPYC 7R13 at 2.65GHz) and 128 GB of RAM. The hyperplonk benchmarks were run using a rust implementation available online\(^\text{11}\). We utilize a multi-linear KZG commitment built using curve BLS12-381. If not otherwise indicated, we use the same custom gate as the Jellyfish library. The gate has 5 inputs, 13 selectors, and degree 5.

Proof size and verification time. Our implementation is modeled after the unrolled and optimized Hyperplonk scheme described in Appendix C. The proof size is 176 µ + 1168 bytes. One

\(^{11}\text{https://github.com/EspressoSystems/hyperplonk}\)
Caveat is that we do not yet commit to the univariate polynomials in each round of the sum check. This slightly increases the proof size. The verification time for \( \mu = 20 \) is less than 20ms on a consumer-grade laptop.

**Cost breakdown.** We present a cost breakdown of HyperPlonk’s prover cost. The breakdown is measured on a consumer-grade laptop\(^\text{12}\). As we see in Figure 5a, the majority of the computation is spent on committing and (batch) opening the commitment; the actual time spent on the information-theoretic PIOPs (Perm Check and Circuit Check) is about 30%. The batch opening does not yet take advantage of the fact that many evaluation points and polynomials are identical. This could reduce the complexity of the resulting zero-check.

Figure 5b gives another breakdown. It shows that the majority of the time (61%) is spent on multi-linear evaluations. This includes the operations performed within the sumcheck protocol. The rest of the time is spent on elliptic curve multi-exponentiations. Batching zero-checks and improving the batch-opening implementation could further reduce the number of MLE operations. We note that both multi-exponentiations and sumchecks are highly parallelizable and hardware-friendly, thus we expect further performance improvement on special-purpose hardware (e.g. GPUs).

It is also worth noting that HyperPlonk never requires the explicit multiplication of polynomials. This enables high-degree custom gates for HyperPlonk.

\[
\begin{array}{c}
\text{Batch Open} \hspace{1cm} \text{Perm Check} \\
40.6\% \hspace{0.5cm} 14.2\%
\end{array}
\]

\[
\begin{array}{c}
\text{Circuit Check} \\
17.3\%
\end{array}
\]

\[
\begin{array}{c}
\text{Commit witness} \\
25.9\%
\end{array}
\]

\[
\begin{array}{c}
\text{Rest} \\
2.0\%
\end{array}
\]

(a) In terms of building blocks (b) In terms of computations

Figure 5: Cost breakdown for vanilla \( \mathcal{R}_{\text{PLONK}} \) with \( 2^{20} \) constraints.

### 6.3 MultiThreading performance

A key advantage of HyperPlonk is that it does not rely on FFT algorithms that are less parallelizable. Indeed, in Figure 6a we observe an almost linear improvement when num of threads is small. We also observe that with low parallelization, the prover’s run time is linear in the number of gates. For example, increase from a single thread to two threads, the prover time is reduced by 45% on average. In contrast, from 32 to 64 threads, there is almost no additional speedup. We assume that this is implementation dependent.

\(^{12}\)2021 Apple MacBook Pro with M1 chip
6.4 High degree gates

It has been shown in VeriZexe [81] that custom gates, even at degree 5, allow for significant improvement of circuit size and prover time. For example, one may perform an elliptic curve group addition with just two gates; while a naive version may require 10+ gates. The better expressibility of high-degree gates enables VeriZexe to improve 9x of prover time over the previous state-of-the-art [25].

However, in a univariate Plonk system, such as [44, 66], high-degree custom gates increase the size of the required FFTs as well as the number of group operations. This limits their utility as they get larger. In comparison, in HyperPlonk, high-degree only affects the number of field operations. Our benchmark result in Figure 6b validates this observation and shows that the prover time from a degree 2 gate to a degree 32 gate only increases by 30%. These more expressive gates can significantly reduce the number of gates in the circuit which more than offsets the added cost.

6.5 Comparisons

We compare our scheme with both Jellyfish Plonk\footnote{https://github.com/EspressoSystems/jellyfish/tree/hyperplonk-bench}, and Spartan\footnote{https://github.com/zhenfeizhang/ark-spartan} [68]. Jellyfish is a highly optimized implementation of Plonk with lookup arguments. It is the state-of-the-art plonk prove system that uses Arkworks as the backend. Spartan is a multilinear ZKP system. Spartan’s statements are written in Rank-1-Constraint-System (\texttt{R1CS}), which is simpler but less expressive.

Hyperplonk, Plonk, and Spartan are polynomial IOPs that can be combined with various polynomial commitments. The commitment has a large impact on the performance of the proof system. For sake of comparison, we ensure that all 3 systems use the same elliptic curve and the same implementation. Concretely we use the Arkworks BLS12-381 implementation. Hyperplonk uses the multi-linear KZG commitment, Jellyfish the univariate KZG commitment, and Spartan uses an inner product argument [20, 27] -based polynomial commitment. We refer to the Spartan fork as Ark-Spartan to highlight the use of the Arkworks BLS12-381 backend.
Comparison by application. We have presented data points for a few typical applications in Table 4. The proof systems are evaluated using mock circuits. The circuit sizes for both the $\mathcal{R}_{\text{PLONK}^+}$ (using the Jellyfish custom gate) and for the $\mathcal{R}_{\text{RICS}}$ arithmetization are taken from references and demonstrate the advantages of (Hyper)Plonk. For example, a proof of knowledge of exponent for a 256-bits elliptic curve group element requires 3315 $\mathcal{R}_{\text{RICS}}$ constraints [63], while it reduces to 783 for $\mathcal{R}_{\text{PLONK}^+}$ [81]. Note that our HyperPlonk implementation does not yet support lookup, but we estimate that the slowdown will only be minor and offset by further optimizations.

<table>
<thead>
<tr>
<th>Application</th>
<th>$\mathcal{R}_{\text{RICS}}$</th>
<th>Ark-Spartan</th>
<th>$\mathcal{R}_{\text{PLONK}^+}$</th>
<th>Jellyfish</th>
<th>HyperPlonk</th>
</tr>
</thead>
<tbody>
<tr>
<td>3-to-1 Rescue Hash</td>
<td>288 [1]</td>
<td>422 ms</td>
<td>144 [71]</td>
<td>40 ms</td>
<td>88 ms</td>
</tr>
<tr>
<td>PoK of Exponent</td>
<td>3315 [63]</td>
<td>902 ms</td>
<td>783 [63]</td>
<td>64 ms</td>
<td>105 ms</td>
</tr>
<tr>
<td>ZCash circuit</td>
<td>$2^{17}$ [55]</td>
<td>8.3 s</td>
<td>$2^{15}$ [42]</td>
<td>0.8 s</td>
<td>0.6 s</td>
</tr>
<tr>
<td>Zexe’s recursive circuit</td>
<td>$2^{22}$ [81]</td>
<td>6 min</td>
<td>$2^{17}$ [81]</td>
<td>13.1 s</td>
<td>5.1 s</td>
</tr>
<tr>
<td>Rollup of 50 private tx</td>
<td>$2^{25}$</td>
<td>39 min$^b$</td>
<td>$2^{20}$ [71]</td>
<td>110 s</td>
<td>38.2 s</td>
</tr>
<tr>
<td>zkEVM circuit$^a$</td>
<td>N/A</td>
<td>N/A</td>
<td>$2^{47}$</td>
<td>1 hour$^{b,c}$</td>
<td>25 min$^{b,c}$</td>
</tr>
</tbody>
</table>

Table 4: Prover runtime of Hyperplonk vs. Spartan[68] and the Jellyfish Plonk implementation for popular applications. Column 2 shows the number of $\mathcal{R}_{\text{RICS}}$ constraints for each application and column 4 shows the corresponding number of constraints in HyperPlonk+/Ultraplonk. We emphasize that the Zexe and the Rollup applications are using the BW6-761 curve because they need to use two-chain curves. The rest of the applications are using the BLS12-381 curve.

Comparison by circuit size. We also compare HyperPlonk with Jellyfish and Ark-Spartan. We run both the vanilla-gate version of HyperPlonk that supports just additions and multiplication gates as well as the degree 5 Jellyfish gate. Note that $n$ Jellyfish gates are significantly more expressive than $n$ vanilla or R1CS gates. We measure both the single-threaded performance in Table 5 and the multi-threaded performance in Table 6. Our benchmark shows that multi-threaded HyperPlonk outperforms Jellyfish starting from $2^{14}$ constraints; the advantage grows when circuit size increases. This is mainly because FFTs scale worse than multi-exponentiations.

HyperPlonk also has slightly better performance than Spartan. The difference is more pronounced in the multi-threaded benchmark which is likely because the Ark-Spartan implementation does not take full advantage of parallelism. We stress again that plonk+ is more expressive than $\mathcal{R}_{\text{RICS}}$, and thus a fair comparison should be over the same application rather than the same size of constraints. Table 4 shows that HyperPlonk is $5 \sim 60x$ faster than Spartan in those applications.

7 Orion+: a linear-time multilinear PCS with constant proof size

Recently, Xie et al. [80] introduced a highly efficient multilinear polynomial commitment scheme called Orion. The prover time is strictly linear, that is, $O(2^\mu)$ field operations and hashes where $\mu$ is the number of variables. For $\mu = 27$, it takes only 115 seconds to commit to a polynomial and
One can view the evaluation of \( f \) as a tensor product, that is, a linear function whose image is a linear code (Definition 2.1). Golovnev et al. [50] later further simplify the scheme. Let \( \mathbf{F} \) be a multilinear polynomial where \( f_b \in \mathbb{F} \) is the coefficient of \( X_b := X_1^{b_1} \cdots X_n^{b_n} \) for every \( b \in B_n \). Denote by \( n = 2^\mu \), \( k = 2^\nu < 2^\mu \) and \( m = n/k \), one can view the evaluation of \( f \) as a tensor product, that is,

\[
f(\mathbf{X}) = \langle \mathbf{w}, t_0 \otimes t_1 \rangle
\]

where \( \mathbf{w} = (f_0, \ldots, f_{n-1}) \), \( t_0 = (X_0, X_1, \ldots, X_{k-1}) \) and \( t_1 = (X_0, X_k, \ldots, X_{(m-1)k}) \). Here \( \langle i \rangle \) denotes the \( \mu \)-bit binary representation of \( i \). Let \( E : \mathbb{F}^m \rightarrow \mathbb{F}^M \) be a linear encoding scheme, that is, a linear function whose image is a linear code (Definition 2.1). Golovnev et al. [50, §4.2] construct a PCS scheme as follows:

- **Commitment:** To commit a multilinear polynomial \( f \) with coefficients \( \mathbf{w} \in \mathbb{F}^n \), the prover \( \mathcal{P} \) interprets \( \mathbf{w} \) as a \( k \times m \) matrix, namely \( \mathbf{w} \in \mathbb{F}^{k \times m} \), encodes \( \mathbf{w} \)'s rows, and obtains matrix \( W \in \mathbb{F}^{k \times M} \) such that \( W[i, :] = E(\mathbf{w}[i, :]) \) for every \( i \in [k] \). Then \( \mathcal{P} \) computes a Merkle tree commitment for each column of \( W \) and builds another Merkle tree \( T \) on top of the column commitments. The polynomial commitment \( C_f \) is the Merkle root of \( T \).

<table>
<thead>
<tr>
<th></th>
<th>( 2^{10} )</th>
<th>( 2^{11} )</th>
<th>( 2^{12} )</th>
<th>( 2^{13} )</th>
<th>( 2^{14} )</th>
<th>( 2^{15} )</th>
<th>( 2^{16} )</th>
<th>( 2^{17} )</th>
<th>( 2^{18} )</th>
<th>( 2^{19} )</th>
<th>( 2^{20} )</th>
</tr>
</thead>
<tbody>
<tr>
<td>HP (Vanilla Gate)</td>
<td>0.22</td>
<td>0.4</td>
<td>0.7</td>
<td>1.2</td>
<td>2.2</td>
<td>4.1</td>
<td>7.8</td>
<td>14.6</td>
<td>28</td>
<td>53</td>
<td>103</td>
</tr>
<tr>
<td>HP (Jellyfish Gate)</td>
<td>0.33</td>
<td>0.6</td>
<td>1.1</td>
<td>1.9</td>
<td>3.6</td>
<td>6.8</td>
<td>13</td>
<td>24.5</td>
<td>49.5</td>
<td>95</td>
<td>185</td>
</tr>
<tr>
<td>Jellyfish Plonk</td>
<td>0.49</td>
<td>0.9</td>
<td>1.5</td>
<td>2.8</td>
<td>5.5</td>
<td>10.5</td>
<td>19.4</td>
<td>37.9</td>
<td>74</td>
<td>143</td>
<td>284</td>
</tr>
<tr>
<td>Ark-Spartan</td>
<td>0.95</td>
<td>1.6</td>
<td>2.5</td>
<td>4.4</td>
<td>6.4</td>
<td>12.1</td>
<td>20.8</td>
<td>38.7</td>
<td>69.2</td>
<td>135</td>
<td>223</td>
</tr>
</tbody>
</table>

Table 5: Single-thread prover’s performance (in seconds) for varying number of constraints under different schemes.

<table>
<thead>
<tr>
<th></th>
<th>( 2^{10} )</th>
<th>( 2^{11} )</th>
<th>( 2^{12} )</th>
<th>( 2^{13} )</th>
<th>( 2^{14} )</th>
<th>( 2^{15} )</th>
<th>( 2^{16} )</th>
<th>( 2^{17} )</th>
<th>( 2^{18} )</th>
<th>( 2^{19} )</th>
<th>( 2^{20} )</th>
</tr>
</thead>
<tbody>
<tr>
<td>HP (Vanilla Gate)</td>
<td>0.07</td>
<td>0.1</td>
<td>0.14</td>
<td>0.2</td>
<td>0.3</td>
<td>0.5</td>
<td>0.8</td>
<td>1.4</td>
<td>2.5</td>
<td>5.1</td>
<td>9.6</td>
</tr>
<tr>
<td>HP (Jellyfish Gate)</td>
<td>0.1</td>
<td>0.13</td>
<td>0.18</td>
<td>0.27</td>
<td>0.4</td>
<td>0.67</td>
<td>1.2</td>
<td>2</td>
<td>3.7</td>
<td>7.3</td>
<td>13.5</td>
</tr>
<tr>
<td>Jellyfish Plonk</td>
<td>0.07</td>
<td>0.1</td>
<td>0.15</td>
<td>0.25</td>
<td>0.46</td>
<td>0.78</td>
<td>1.4</td>
<td>2.7</td>
<td>5.5</td>
<td>10.8</td>
<td>22</td>
</tr>
<tr>
<td>Ark-Spartan</td>
<td>0.51</td>
<td>0.72</td>
<td>0.9</td>
<td>1.4</td>
<td>1.9</td>
<td>3.1</td>
<td>4.7</td>
<td>8.3</td>
<td>13.7</td>
<td>27</td>
<td>44</td>
</tr>
</tbody>
</table>

Table 6: 64-thread prover’s performance (in seconds) for varying number of constraints under different schemes.
• **Evaluation proof:** To prove that \( f(z) = y \) for some point \( z \in \mathbb{F}^n \) and value \( y \in \mathbb{F} \), the prover \( P \) translates \( z \) to vectors \( t_0 \in \mathbb{F}^k \) and \( t_1 \in \mathbb{F}^m \) as above and proves that \( \langle w, t_0 \otimes t_1 \rangle = y \) (where \( w \in \mathbb{F}^{k \times m} \) is the message encoded and committed in \( C_f \)). To do so, \( P \) does two things:

  - **Proximity check:** The prover shows that the matrix \( W \in \mathbb{F}^{k \times M} \) committed by \( C_f \) is close to \( k \) codewords. Specifically, the verifier sends a random vector \( r \in \mathbb{F}^k \), the prover replies with a vector \( y_r := r \cdot w \in \mathbb{F}^m \) which is the linear combination of \( w \)'s rows according to \( r \). The verifier checks that the encoding of \( y_r \), namely \( E(y_r) \in \mathbb{F}^M \), is close to \( r \cdot W \), the linear combination of \( W \)'s rows. This implies that the \( k \) rows of \( W \) are all close to codewords [50, §4.2].

  - **Consistency check:** The prover shows that \( \langle w, t_0 \otimes t_1 \rangle = y \) where \( w \in \mathbb{F}^{k \times m} \) is the \( k \) error-decoded messages from \( W \in \mathbb{F} \) committed in \( C_f \). The scheme is similar to the proximity check except that we replace the random vector \( r \) with \( t_0 \). After receiving the linearly combined vector \( y_0 \in \mathbb{F}^m \), the verifier further checks that \( \langle y_0, t_1 \rangle = y \).

We describe the concrete PCS evaluation protocol below.

**Protocol 1** (PCS evaluation [50]): The goal is to check that \( \langle w, t_0 \otimes t_1 \rangle = y \) (where \( w \in \mathbb{F}^{k \times m} \) is the message encoded and committed in \( C_f \)).

1. \( V \) sends a random vector \( r \in \mathbb{F}^k \).
2. \( P \) sends vector \( y_r, y_0 \in \mathbb{F}^m \) where

\[
y_r = \sum_{i=1}^{k} r_i \cdot w[i,:], \text{ and } y_0 = \sum_{i=1}^{k} t_{0,i} \cdot w[i,:],
\]

where \( w \in \mathbb{F}^{k \times m} \) is the message matrix being encoded and committed.
3. \( V \) sends \( P \) a random subset \( I \subseteq [M] \) with size \( |I| = \Theta(\lambda) \).
4. \( P \) opens the entire columns \( \{W[:,j]\}_{j \in I} \) using Merkle proofs, where \( W \in \mathbb{F}^{k \times M} \) is the row-wise encoded matrix. That is, \( P \) outputs the column commitment \( h_j \) for every column \( j \in I \), and provide the Merkle proof for \( h_j \) w.r.t. to Merkle root \( C_f \).
5. \( V \) checks that (i) the Merkle openings are correct w.r.t. \( C_f \), and (ii) for all \( j \in I \), it holds that

\[
E(y_r)_j = \langle r, W[:,j] \rangle \text{ and } E(y_0)_j = \langle t_0, W[:,j] \rangle.
\]

6. \( V \) checks that \( \langle y_0, t_1 \rangle = y \).

Note that by sampling a subset \( I \) with size \( \Theta(\lambda) \) and checking that \( r \cdot W, t_0 \cdot W \) are consistent with the encodings \( E(y_r), E(y_0) \) on set \( I \), the verifier is confident that \( r \cdot W, t_0 \cdot W \) are indeed close to the encodings \( E(y_r), E(y_0) \) with high probability. By setting \( k = \sqrt{n} \), the prover takes \( O(n) \) \( \mathbb{F} \)-ops and hashes; the verifier time and proof size are both \( O(\sqrt{n}) \). **Orion** describes an elegant code-switching scheme that reduces the proof size and verifier time down to \( O(\log^2(n)) \). However, the concrete proof size is still large. Next, we describe a scheme that has much smaller proof.

**Linear-time PCS with small proofs.** Similar to **Orion** (and more generally, the proof composition technique [21, 22, 50]), instead of letting the verifier check the correctness of \( y_r, y_0 \) and the openings of the columns \( W[:,j] \forall j \in I \), the prover can compute another (succinct) outer proof validating the correctness of \( y_r, y_0, W[:,j] \). However, we need to minimize the outer proof’s circuit complexity, which is non-trivial. **Orion** builds an efficient SNARK circuit that removes all of the
hashing gadgets, with the tradeoff of larger proof size. We describe a variant of their scheme that minimizes the proof size without significantly increasing the circuit complexity.

Specifically, after receiving challenge vector \( r \in \mathbb{F}^k \), \( \mathcal{P} \) instead sends \( \mathcal{V} \) commitments \( C_r, C_0 \) to the messages \( y_r, y_0 \); after receiving \( \mathcal{V} \)'s random subset \( I \subset [M] \), \( \mathcal{P} \) computes a SNARK proof for the following statement:

Statement 1 (PCS Eval verification):

- **Witness**: \( y_r, y_0 \in \mathbb{F}^m, \{W[:,j]\}_{j \in I} \).
- **Circuit statements**:
  - \( C_r, C_0 \) are the commitments to \( y_r, y_0 \) respectively.
  - For all \( j \in I \), it holds that
    - \( h_j = H(W[:,j]) \) where \( H \) is a fast hashing scheme;
    - \( E(y_r)_j = \langle r, W[:,j] \rangle \) and \( E(y_0)_j = \langle t_0, W[:,j] \rangle \).
- \( \langle y_0, t_1 \rangle = y \).
- **Public output**: \( \{h_j\}_{j \in I} \), and \( C_r, C_0 \).

Besides the SNARK proof, the prover also provides the openings of \( \{h_j\}_{j \in I} \) with respect to the commitments \( C_r \). Intuitively, the new protocol is “equivalent” to Protocol 1, because the SNARK witness \( \{W[:,j]\}_{j \in I} \) and \( y_r, y_0 \) are identical to those committed in \( C_r, C_0 \) by the binding property of the commitments; and the SNARK does all of the verifier checks. Unfortunately, the scheme has the following drawbacks:

- Instantiating the commitments with Merkle trees leads to a large overhead on the proof size.
  In particular, the proof contains \( |I| \) Merkle proofs, each with length \( O(\log n) \). For 128-bit security, we need to set \( |I| = 1568 \), and the proof size is at least 1 MBs for \( \mu = 20 \).
- The random subset \( I \) varies for different evaluation instances. It is non-trivial to efficiently lookup the witness \( \{E(y_r)_j, E(y_0)_j\}_{j \in I} \) in the circuit if the set \( I \) is dynamic (i.e. we need an efficient random access gadget).
- The circuit complexity is huge. In particular, the circuit is dominated by the commitments to \( y_r, y_0 \) and the hash commitments to \( \{W[:,j]\}_{j \in I} \). This leads to \( 2m + k|I| \) hash gadgets in the circuit. Note that we can’t use algebraic hash functions like Rescue [1] or Poseidon [51], which are circuit-friendly, but have slow running times. For \( \mu = 26, k = m = \sqrt{n} \) and 128-bit security (where \( |I| = 1568 \)), this leads to 13 million hash gadgets where each hash takes hundreds to thousands of constraints, which is unaffordable.

We resolve the above issues via the following observations.

First, a large portion of the multilinear PCS evaluation proof is Merkle opening paths. We can shrink the proof size by replacing Merkle trees with multilinear PCS that enable efficient batch openings (Section 3.8). Specifically, in the committing phase, after computing the hashes of \( W \)'s columns, instead of building another Merkle tree \( T \) of size \( M = O(n/k) \) and set the Merkle root as the commitment, the prover can commit to the column hashes using a multilinear PCS (e.g. KZG). Though the KZG committing is more expensive, the problem size has been reduced to \( O(n/k) \), thus for sufficiently large \( k \), the committing complexity is still approximately \( O(n) \) \( \mathbb{F} \)-ops.

A great advantage is that the batch opening proof for \( \{h_j\}_{j \in I} \) consists of only \( O(\log n) \) group/field elements, with good constant. Even better, when instantiating the outer proof with HyperPlonk(+),
the openings can be batched with those in the outer SNARK and thus incur almost no extra cost in proof size.

Second, with Plookup, we can efficiently simulate random access in arrays in the SNARK circuit. For example, to extract witness \( \{Y_{r,j} = E(y_r)_{j} \}_{j \in I} \), we can build an (online) table \( T \) where each element of the table is a pair \( (i, E(y_r)_i) \) \( (1 \leq i \leq M) \). Then for every \( j \in I \), we build a lookup gate checking that \( (j, Y_{r,j}) \) is in the table \( T \), thus guarantee that \( Y_{r,j} \) is identical to \( E(y_r)_j \). The circuit description is now independent of the random set \( I \) and we only need to preprocess the circuit once in the setup phase.

Third, with the help of Commit-and-Prove-SNARKs (CP-SNARK) \([31, 32, 3]\), there is no need to check the consistency between commitments \( C_r, C_0 \) and \( y_r, y_0 \) in the circuit. Instead, we can commit \( (y_r, y_0) \) to a multilinear commitment \( C \), and build a CP-SNARK proof showing that the vector underlying \( C \) is identical to the witness vector \( (y_r, y_0) \) in the circuit. We further observe that \( C \) can be a part of the witness polynomials, which further removes the need of an additional CP-SNARK proof.

After applying previous optimizations, the proof size is dominated by the \(|I| \) field elements \( \{h_j\}_{j \in I} \). We can altogether remove them by applying the CP-SNARK trick again. In particular, since \( \{h_j\}_{j \in I} \) are both committed in the polynomial commitment \( C_f \) and the SNARK witness commitment, it is sufficient to construct a CP-SNARK proving that they are consistent in the two commitments with respect to set \( I \). We refer to Section 3.8 for constructing CP-SNARK proofs from multilinear commitments.

Since the bulk of verification work is delegated to the prover, there is no need to set \( k = \sqrt{n} \). Instead, we can set an appropriate \( k = \Theta(\lambda / \log n) \) to minimize the outer circuit size. In particular, the circuit is dominated by 2 linear encodings (of length \( n/k \)) and \(|I| \) hashes (of length \( k \)). If we use vanilla HyperPlonk+ as the outer SNARK scheme and use Reinforced Concrete [7] as the hashing scheme that has a similar running time to SHA-256, for \( \mu = 30, k = 64 \) and 128-bit security (where \(|I| = 1568 \)), the circuit complexity is only \( \approx 2^{26} \) constraints. And we can expect the running time of the outer proof to be \( O_\lambda(n) \).

The resulting multilinear polynomial commitment scheme is shown in Figure 7.

**Remark 7.1 (CP-SNARKs instantiation.).** We can use the algorithm in Section 3.8.1 to instantiate the CP-SNARK in Figure 7 from any multilinear PIOP-based SNARKs with minimal overhead. First, we can split the witness polynomial into two parts: one includes the vector \( (y_r, y_0) \) while the other includes the rest. The witness polynomial commitment to \( (y_r, y_0) \) is essentially the commitment \( C_{py} \) in Figure 7, so that we don’t need to additionally commit to \( (y_r, y_0) \) and provide a proof. We emphasize that \( C_{py} \) is sent before the prover receives the challenge set \( I \), which is essential for knowledge soundness.

Second, the CP-proof generation between the multilinear commitment \( C_f \) and the SNARK witness polynomial commitment (w.r.t. set \( I \)) consists of a sumcheck with \( O(\log m) \) rounds and 2 PCS openings (one for \( C_f \) and one for the witness polynomial). If we instantiate the SNARK with HyperPlonk+, we can batch the proving of the CP-proof and the SNARK proof so that the CP-proof adds no extra cost to the proof size beyond the original SNARK proof.
Building blocks: A CP-SNARK scheme OSNARK; an (extractable) polynomial commitment scheme PC; a hash commitment scheme HCom; and a linear encoding scheme $E$ with minimum distance $\delta$.

Setup$(1^\lambda, \mu^*) \rightarrow \text{gp}$: Given security parameter $\lambda$, upper bound $\mu^*$ on the number of variables, set $m^*$ so that the running time of OSNARK (and PC) is $O_\lambda(2^{\mu^*})$ for circuit size (and degree) $m^*$. Run $\text{gp}_o \leftarrow \text{OSNARK.Setup}(1^\lambda, m^*)$, $\text{gp}_{pc} \leftarrow \text{PC.Setup}(1^\lambda, m^*)$, run the indexing phase of OSNARK for the circuit statement in Figure 8 and obtain $(v_p, pp_o)$. Output $\text{gp} := (\text{gp}_o, \text{gp}_{pc}, v_p, pp_o)$.

Commit$(\text{gp}; f) \rightarrow C_f$: Given polynomial $f \in F^{(\leq 1)}_{\mu}$ with coefficients $w \in \mathbb{F}^{k \times m}$, set $m = n/k$ so that the running time of OSNARK (and PC) is $O_\lambda(2^\mu)$ for circuit size (and degree) $m$. Interpret $w$ as a $k \times m$ matrix (i.e. $w \in \mathbb{F}^{k \times m}$):

- Compute matrix $W \in \mathbb{F}^{k \times m}$ such that $W[i,:] = E(w[i,:]) \forall i \in [k]$. Here $E : \mathbb{F}^m \rightarrow \mathbb{F}^{m}$ is the linear encoding.
- For each column $j \in [m]$, compute hash commitment $h_j \leftarrow \text{HCom}(W[:,j])$, where $W[:,j] \in \mathbb{F}^{k}$ is the $j$-th column of $W$.
- Let $p_h$ be the polynomial that interpolates vector $(h_j)_{j \in [m]}$. Output commitment $C_f \leftarrow \text{PC.Commit}(\text{gp}_{pc}, p_h)$.

Open$(\text{gp}, C_f, f)$: Given polynomial $f \in F^{(\leq 1)}_{\mu}$ with coefficients $w \in \mathbb{F}^{k \times m}$, run the committing algorithm and check if the output is consistent with $C_f$.

Eval$(\text{gp}; C_f, z, y; f)$: Given public parameter $\text{gp}$, point $z \in \mathbb{F}^\mu$ and commitment $C_f$ to polynomial $f \in F^{(\leq 1)}_{\mu}$ with coefficients $w \in \mathbb{F}^{k \times m}$, transform $z$ to vectors $t_0 \in \mathbb{F}^k$ and $t_1 \in \mathbb{F}^m$ as in Equation (15) such that $f(z) = \langle w, t_0 \otimes t_1 \rangle$. The prover $P$ and the verifier $V$ run the following protocol:

1. $V$ sends $P$ a random vector $r \in \mathbb{F}^k$.
2. Define vectors
   $$y_r = \sum_{i=1}^{k} r_i \cdot w[i,:], \quad y_0 = \sum_{i=1}^{k} t_{0,i} \cdot w[i,:].$$

   Let $p_r$ be the polynomial that interpolates $(y_r, y_0)$. $P$ sends $V$ commitment $C_{py} \leftarrow \text{PC.Commit}(\text{gp}_{pc}, p_r)$.
3. $V$ sends a random subset $I \subseteq [m]$ with size $t := \frac{-\lambda}{\log(1-\delta)}$.
4. $P$ sends $V$, a CP-SNARK proof $\pi_o$ showing that
   - the statement in Figure 8 holds true;
   - the SNARK witness $(y_r, y_0)$ is identical to the vector committed in $C_{py}$;
   - the SNARK witness $(h_j)_{j \in I}$ is consistent with that in the polynomial commitment $C_f$ w.r.t. set $I$.
5. $V$ checks $\pi_o$ with public input $(\alpha, r, y, z)$, and commitments $C_{py}, C_f$.

Figure 7: The multilinear polynomial commitment scheme.
Evaluation check with probability more than \( \epsilon \). E

Even if the linear code \( W \) is not efficiently decodable. For any adversary \( A \) that can pass the PCS evaluation check with probability more than \( \epsilon \), the extractor \( E^A \) works as follows:

1. Run \( A \) and obtain commitment \( C_f \), point \( z \in \mathbb{F}^\mu \), and evaluation \( y \in \mathbb{F} \). Run the extractor of the PCS and the hash function to recover the matrix \( W' \in \mathbb{F}^{k \times M} \) underlying \( C_f \). Abort if the extraction fails.

Public input:
- challenge vector \( r \in \mathbb{F}^k \);
- random subset \( I \subseteq [M] \);
- evaluation point \( z \in \mathbb{F}^\mu \) and claimed evaluation \( y \in \mathbb{F} \).

Circuit statements:
- \( t_0, t_1 \) is the correct transformation from \( z \) as in Equation (15).
- \( Y_r = E(y_r) \) and \( Y_0 = E(y_0) \).
- For \( i = 1 \ldots |I| \), let \( j_i \in I \) be the \( i \)-th element in \( I \), it holds that
  - \( Y_{r,i} = Y_{r,j_i} \), that is, \( (j_i, Y_{r,i}) \) is in the table \( \{(k, Y_{r,k})\}_{k \in [M]} \), and
  - \( Y_{0,i} = Y_{0,j_i} \), that is, \( (j_i, Y_{0,i}) \) is in the table \( \{(k, Y_{0,k})\}_{k \in [M]} \).
- For \( i = 1 \ldots |I| \), it holds that
  - \( h_i = \text{HCom}(W'[:; i]) \) where \( \text{HCom} : \mathbb{F}^k \rightarrow \mathbb{F} \) is the hash commitment scheme;
  - \( Y'_{r,i} = \langle r, W'[:; i] \rangle \) and \( Y'_{0,i} = \langle t_0, W'[:; i] \rangle \).
- \( \langle y_0, t_1 \rangle = y \).

Figure 8: The outer SNARK circuit statement. The circuit configuration is independent of the random set \( I \).

**Theorem 7.1.** The multilinear polynomial commitment scheme in Figure 7 is correct and binding. The PCS evaluation protocol is knowledge-sound.

**Proof. Correctness and binding.** Correctness holds obviously by inspection of the protocol. We prove the binding property by contradiction. Suppose an adversary finds a commitment \( C_f \) and two polynomials \( f_1, f_2 \) with different coefficients \( w_1, w_2 \in \mathbb{F}^{k \times m} \) such that \( C_f \) can open to both \( w_1 \) and \( w_2 \). There are two cases:

1. \( C_f \) can open to two different vectors of column hash commitments \( h_1, h_2 \in \mathbb{F}^M \), which contradicts the binding property of the PCS PC.

2. \( C_f \) binds to a single vector \( h \in \mathbb{F}^M \), but encoding \( w_1, w_2 \) lead to two different encoded matrices \( W_1, W_2 \in \mathbb{F}^{k \times M} \). This contradicts the collision resistance of the hash function.

In summary, the binding property holds.

**Knowledge soundness.** We use a similar technique as in [50] that enables extracting polynomials even if the linear code \( E \) is not efficiently decodable. For any adversary \( A \) that can pass the PCS evaluation check with probability more than \( \epsilon \), the extractor \( E^A \) works as follows:

1. Run \( A \) and obtain commitment \( C_f \), point \( z \in \mathbb{F}^\mu \), and evaluation \( y \in \mathbb{F} \). Run the extractors of the PCS and the hash function to recover the matrix \( W' \in \mathbb{F}^{k \times M} \) underlying \( C_f \). Abort if the extraction fails.
2. Set \( S \leftarrow \emptyset \), repeat the following procedures until \(|S| \geq k\) or the number of \( r \) being sampled is more than \( 8k/\epsilon \):

- Sample and send \( A \) a random vector \( r \sim \mathbb{F}^k \).
- Obtain the PCS commitments \( C_{p_y} \). Use the PCS extractor to extract the vector \((y_r, y_0) \in \mathbb{F}^{2m}\). Abort and rerun with another \( r \) if the extraction fails.
- Sample and send \( A \) a random subset \( I \subseteq [M] \).
- Obtain the CP-SNARK proof \( \pi_o \). Add the pair \((r, y_r)\) into set \( S \) if the proof correctly verifies.

3. If \(|S| \geq k\) and the random vectors \( \{r\} \) in \( S \) are linearly independent, run the Gaussian elimination algorithm to extract the witness \( w \) from \( S = \{(r, y_r)\} \), otherwise abort.

The extractor runs in polynomial time as Step 2 runs in polynomial time, and the extractor executes Step 2 for at most \( 8k/\epsilon \) times. Next, we argue that the extractor’s success probability is non-negligible. Since \( A \) succeeds with probability at least \( \epsilon \), with probability at least \( \epsilon/2 \) over the choice of \((C_f, z, y)\), the adversary passes the PCS evaluation protocol \( \text{Eval}(gp; C_f, z, y) \) with probability at least \( \epsilon/2 \). We denote by \( B \) the event that the above happens.

Conditioned on event \( B \), we first argue that with high probability, \( E \) can add \( k \) pairs to \( S \), and the \( r \)'s in \( S \) are linearly-independent. Note that for each run of PCS evaluation (with a freshly sampled vector \( r \)), the probability that the extractor adds a pair to \( S \) is at least \( \epsilon/2 - \text{negl}(\lambda) \geq \epsilon/4 \). This is because \( A \) passes the checks with probability at least \( \epsilon/2 \), and thus with probability at least \( \epsilon/2 - \text{negl}(\lambda) \), \( A \) passes all the checks, and the PCS extractor succeeds. Therefore, by Chernoff bound, the probability that \( E \) adds \( k \) pairs to \( S \) within \( 8k/\epsilon \) runs of Step 2 is at least \( 1 - \text{exp}(-k/8) \).

Moreover, as noted by Lemma 2 of [50], the random vectors \( \{r\} \) in set \( S \) are linearly independent with overwhelming probability.

Next, still conditioned on event \( B \), we argue that with probability \( 1 - \text{negl}(\lambda) \), there exists a coefficient matrix \( w \in \mathbb{F}^{k \times m} \) that is consistent with the commitment \( C_f \), such that \((w, t_0 \otimes t_1) = y \) (i.e. the evaluation is correct) and \( y_r = \sum_{i=1}^{k} r_i \cdot w_i \) for every pair \((r, y_r)\) in set \( S \). Let \( W' \in \mathbb{F}^{k \times M} \) be the matrix extracted by \( E \) at Step 1, note that \( W' \) commits to \( C_f \). Consider each run of PCS evaluation where the extractor adds a pair \((r, y_r)\) to \( S \). Since \( C_f, C_{p_y} \) are binding, and the SNARK proofs verify, it holds that w.h.p over the choice of \( I \), \( E(y_r) \) is close to \( \sum_{i=1}^{k} r_i \cdot W'_i \). By Lemma 1 in [50], w.h.p. over the choice of \( r \), it also holds that \( W'_i \) is close to a codeword for all \( i \in [k] \).

Therefore, there exists a matrix \( w \in \mathbb{F}^{k \times m} \) such that (i) \( W'_i \) is close to \( E(w_i) \) for all \( i \in [k] \), and (ii) \( y_r = \sum_{i=1}^{k} r_i \cdot w_i \). Moreover, by the uniqueness of encoding, \( w \) is identical for every challenge vector \( r \) in set \( S \). Similarly, we can argue that \( y_0 = \sum_{i=1}^{k} t_{0,i} \cdot w_i \) and thus \((w, t_0 \otimes t_1) = y \).

Given the above, we conclude that with high probability, it holds that (i) \( E \) adds \( k \) pairs to \( S \) where the \( r \)'s in \( S \) are linearly independent; and (ii) there exists \( w \in \mathbb{F}^{k \times m} \) that is consistent with \( C_f \) and \((w, t_0 \otimes t_1) = y \) and \( y_r = \sum_{i=1}^{k} r_i \cdot w_i \) for every pair \((r, y_r)\) in set \( S \). In summary, conditioned on event \( B \), the extractor can extract the coefficient matrix \( w \) via Gaussian elimination with high probability, which completes the proof.

**Theorem 7.2.** When instantiating the outer SNARK with HyperPlonk+, the multilinear PCS in Figure 7 has committing and evaluation opening complexity \( O_\lambda(n) \); the proof size and verifier time is \( O_\lambda(\log n) \).
Proof. Fix \( k = \Theta(\lambda / \log n) \) and let \( m = n / k \). The committing algorithm takes \( O(n) \mathbb{F} \)-ops to encode the coefficients \( w \in \mathbb{F}^{k \times m} \) to \( W \in \mathbb{F}^{k \times M} \), \( O(n) \) hashes to compute the column commitments, and an \( O(m) \)-sized MSM to commit to the vector of column commitments. The total complexity is \( O_\lambda(n) \).

The evaluation proving mainly consists of the following steps:

- compute a HyperPlonk+ SNARK proof for the statement in Figure 8;
- compute a CP-SNARK proof between the commitment \( C_f \) and the SNARK witness polynomial commitment with respect to a set \( I \).

By the linear algorithm specified in Section 3.8.1, the CP-SNARK proof generation is dominated by a multi-group-exponentiations with size \( s \), where \( s \) is the circuit size; similarly, the HyperPlonk+ SNARK proving is also dominated by a few multi-group-exponentiations with size \( s \). Next, we prove that the outer circuit complexity is \( s = O(m) \), hence the evaluation opening complexity is also \( O_\lambda(n) \).

Lemma 7.3. The number of constraints in the circuit in Figure 8 is \( O(m) + |H| \cdot O(k\lambda) \), where \( |H| \) is the number of constraints for a hash instance.

Proof. The circuit for computing \( t_0, t_1 \) from \( z \) takes \( O(k) \) and \( O(m) \) constraints, respectively; the circuit for encoding \( y_r, y_0 \) takes \( O(m) \) constraints; the extraction of \( Y'_r, Y'_0 \) from \( \{ E(y_j) \} \) takes \( 2|I| \) lookup gates; the computation of \( Y'_r, Y'_0 \) takes \( O(k|I|) \) constraints; the computation of \( y \) takes \( O(m) \) constraints; the computation of hashes \( \{ h_i \} \) takes \( k|I| = O(k\lambda) \) hash gadgets as \( |I| = \Theta(\lambda) \), thus the number of constraints is \( O(m) + |H| \cdot O(k\lambda) \). \( \square \)

The evaluation verifier checks the CP-SNARK proof \( \pi_o \) which takes time \( O_\lambda(\log n) \).

The evaluation proof consists of a single CP-SNARK proof \( \pi_o \). As noted by Remark 7.1, the proof size is no more than that of a single HyperPlonk+ proof for circuit size \( s = O(m) \). In summary, the proof size is \( O_\lambda(\log n) \). \( \square \)

Remark 7.2. We stress that the CP-SNARK proving time (between \( C_f \) and the SNARK witness) for set \( I \) is independent of the size of \( I \), as the complexity of the special batching algorithm in Section 3.8.1 is independent of the number of evaluations. This is highly important because \( |I| \) can be as large as thousands in practice.

Remark 7.3. If we instantiate the linear code with the generalized Spielman code proposed in [80], and instantiate the outer SNARK with vanilla HyperPlonk+, for 128-bit security and \( \mu = 30 \), the outer circuit size is approximately \( 2^{26} \), thus the proof is less than 10 KBs.

Remark 7.4. In practice, to minimize the outer circuit complexity, we choose \( k \) such that \( 2 \cdot \ell(n/k) = k|I| \cdot |H| \), where \( \ell(n/k) \) is the circuit size for encoding a message with length \( n/k \). Note that \( |I| = 1568 \) for 128-bit security and \( |I| = 980 \) for 80-bit security.

Remark 7.5. In contrast with Orion, Orion+ requires using a pairing-friendly field. We leave the construction of linear-time PCS with succinct proofs/verifier that supports arbitrary fields as future work.
Conclusions and open problems

We presented a SNARK with a fast prover that is an adaption of Plonk to the boolean hypercube. We also present Orion+, a significantly improved multi-linear commitment scheme with short proofs and fully-linear prover time. There are several open questions:

- Higher degree shifts: We show how to build a generator for the boolean hypercube that enables a next function that shifts points in the hypercube by 1. This is critical for building a lookup protocol. In some versions of the Halo 2 arithmetization, the proof system accesses machine states from more than the previous step. To implement this, we need higher degree shifts. This can be done by composing the next function multiple times, but this has an exponential blow-up in verifier time. Implementing higher degree next functions efficiently remains an open problem.

- Better codes and hash functions for Orion+: The utility of Orion+ for smaller polynomials is limited by the use of recursion. Only for large multilinear polynomials (approximately with more than 24 variables), does the recursive circuit size become less than the original polynomial size. The keys to improving this are linear codes with better distance and more efficient and circuit-friendly hash functions.

- Automatic custom gate design: HyperPlonk+ supports high-degree custom gates efficiently. Currently, designing suitable custom gates for specific applications is a task left to the circuit designer. It remains an open problem to have a more principled approach that automates and optimizes the design of the custom gates for any given application.

- Linear-time permutation argument for small fields: In Section 3.6 we present a permutation argument with good soundness even for non-exponential fields. The argument unfortunately only has quasi-linear prover time. An important open question is whether there exists a permutation argument or a multi-set argument with linear prover time and optimal soundness.

Acknowledgments. We want to thank Ben Fisch and Alex Oezdemir for helpful discussions and Tiancheng Xie for answering many questions regarding Orion and Virgo. We want to thank Alessandro Chiesa for bringing the permutation check soundness question as well a connection to PCPs to our attention. Finally, we thank Srinath Setty for suggesting improvements to our evaluation section. This work was partially funded by NSF, DARPA, the Simons Foundation, UBRI, and NTT Research. Opinions, findings, and conclusions or recommendations expressed in this material are those of the authors and do not necessarily reflect the views of DARPA.

References


A Zero Knowledge PIOPs and zk-SNARKs

In this Section, we describe a compiler that transforms a class of sumcheck-based multivariate PolyIOPs into ones that are zero knowledge. The general framework consists of two parts. The first part is to mask the oracle polynomials so that their oracle query answers do not reveal the information of the original polynomial; moreover, we require that the masking do not change evaluations over the boolean hypercube, thus the correctness of PIOPs still holds. The second part is making the underlying sumcheck PIOPs zero knowledge. For this we reuse the ZK sumchecks described in [79].

We note that in contrast with univariate PIOPs, there is a subtlety in compiling multivariate PIOPs: the zero-knowledge property is hard to achieve if the set of query points is highly structural. E.g., suppose $f$ is 2-variate and there are 4 query points $(r_1, r_2), (r_1, r_1), (r_2, r_1), (r_2, r_2)$. Though all of the 4 points are distinct, each dimension has at least 2 points that share the same value. This makes the adversary much easier to cancel out the masking randomness and obtain a correlation between the evaluations of $f$ on the 4 points. We resolve the issue by restricting the set of query points to be less structured. In particular, we require that there is at least one dimension where each point has a distinct value. We also slightly modify the underlying sumcheck protocols to satisfy the restriction while the soundness is not affected.

The Section is organized as follows. We define zero knowledge PIOPs in Section A.1. In Section A.2, we describe a scheme masking the multivariate polynomials. Section A.3 reviews the ZK sumchecks in [79]. We describe the ZK compiler for PIOPs in Section A.4 and explain how to obtain a zk-SNARK from a zk-PIOP and a PCS in Section A.5.

A.1 Definition

We follow [35] and define the (honest verifier) zero-knowledge property of PIOPs. Since the provers in sumcheck PIOPs also send field elements, we slightly adapt the definition in [35].

**Definition A.1.** A PIOP $\langle P, V \rangle$ has perfect zero-knowledge with query bound $t$ and query checker $C$ if there is a PPT simulator $S$ such that for every field $F$, index $i$, instance $x$, witness $w$, and every $(t, C)$-admissible verifier $V^*$, the following transcripts are identically distributed:

$$\text{View}(P(F, i; x, w), V^*) \approx S^V(F, i; x).$$

Here the view consists of $V^*$’s randomness, the non-oracle messages sent by $P$, and the list of answers to $V^*$’s oracle queries. A verifier is $(t, C)$-admissible if it makes no more than $t$ queries, and each query is accepted by the checker $C$. We say that $\langle P, V \rangle$ is honest-verifier-zero-knowledge (HVZK) if there is a simulator for $V$.

A.2 Polynomial masking

**Definition A.2.** A randomized algorithm $\text{msk}$ is a $(t, C, \mu)$-masking if

1. for every $d \in \mathbb{N}$ and every polynomial $f \in F_\mu^{(\leq d)}$, the masked polynomial $f^* \leftarrow \text{msk}(f, t, C)$ does not change evaluations over the boolean hypercube $B_\mu$;

2. for every $d \in \mathbb{N}$ and every polynomials $f \in F_\mu^{(\leq d)}$, and every list of queries $q := (q_1, \ldots, q_t)$ that is accepted by the checker $C$, let $f^* \leftarrow \text{msk}(f, t, C)$. It holds that $(f^*(q_1), \ldots, f^*(q_t))$ is uniformly distributed over $\mathbb{F}^t$. 

59
Lemma A.1. There is a \((t, C_\ell, \mu)\)-masking algorithm \(\text{msk}(f, t, \ell)\) for every \(\mu, t \in \mathbb{N}\) and \(\ell \in [\mu]\), where checker \(C_\ell\) accepts a list of queries \((q_1, \ldots, q_t)\) if and only if \(b_{i,\ell} \notin \{0, 1, b_{1,\ell}, \ldots, b_{t-1,\ell}\}\) for every query \(q_i := (b_{i,1}, \ldots, b_{i,\mu}) \in \mathbb{F}_\mu\) \((1 \leq i \leq t)\). For any \(f \in \mathcal{F}_\mu^{(\leq d)}\) and \(\ell \in [\mu]\), the degree of the masked polynomial \(f^* \leftarrow \text{msk}(f, t, \ell)\) is \(\max(d, t + 1)\).

Proof. Given a polynomial \(f \in \mathcal{F}_\mu^{(\leq d)}\), query bound \(t\), and checker \(C_\ell\), the algorithm does follow:

- Sample a univariate polynomial \(R(X) := c_0 + c_1X + \ldots + c_{t-1}X^{t-1}\) where \(c_0, \ldots, c_{t-1} \overset{\$}{\leftarrow} \mathbb{F}\).
- Output \(f^* := f + Z(X_\ell) \cdot R(X_\ell)\), where \(Z(X_\ell) := X_\ell \cdot (1 - X_\ell)\).

It is clear that \(f^*\) has degree \(\max(d, t + 1)\); \(f^*\) does not change \(f\)'s evaluations over \(B_\mu\) as \(Z\) evaluates to zero over \(B_\mu\). Next, we argue that \(f^* := (f^*(q_1), \ldots, f^*(q_t)) \in \mathbb{F}^t\) is uniformly random. Denote query \(q_i := (b_{i,1}, \ldots, b_{i,\mu}) (1 \leq i \leq t)\), we define \(R\) to be

\[
R := (Z(b_{1,\ell}) \cdot R(b_{1,\ell}), \ldots, Z(b_{t,\ell}) \cdot R(b_{t,\ell})).
\]

Since the queries satisfy \(b_{i,\ell} \notin \{0, 1\}\) for every \(i \in [t]\), it holds that \(z_i := Z(b_{i,\ell})\) are non-zero and thus invertible. Moreover, since \(R\) is a random univariate polynomial with degree \(t - 1\) and \(\{b_{1,\ell}, \ldots, b_{t,\ell}\}\) are distinct, it holds that \(\{R(b_{1,\ell}), \ldots, R(b_{t,\ell})\}\) are uniformly random. Therefore \(R\) is uniformly random, and thus \(f^* = f + q\) is also uniformly random where \(f := (f(q_1), \ldots, f(q_t))\). \(\square\)

A.3 Zero knowledge SumCheck

Construction. Xie et al. [79] described an efficient ZK compiler for sumchecks. For reader’s convenience, we adapt Construction 1 in [79] to a PIOP.

Zero knowledge SumCheck PIOP \((\mathcal{P}, \mathcal{V})\):

- Input: polynomial \(f \in \mathcal{F}_\mu^{(\leq d)}\) and claimed sum \(H \in \mathbb{F}\).
- \(\mathcal{P}\) samples a polynomial \(g := c_0 + g_1(x_1) + \cdots + g_\mu(x_\mu)\) where \(g_i(x_i) := c_{i,1}x_i + \cdots + c_{i,d}x_i^{d}\) and \(c_{i,1}, \ldots, c_{i,d}\) are uniformly random. \(\mathcal{P}\) sends oracle \(g\) and a claimed sum \(G := \sum_{x \in B_\mu} g(x)\).
- \(\mathcal{V}\) sends a challenge \(\rho \overset{\$}{\leftarrow} \mathbb{F}\).
- \(\mathcal{P}\) and \(\mathcal{V}\) run SumCheck PIOP (Section 3.1) over polynomial \(f + \rho g\) and claimed sum \(H + \rho G\).
- \(\mathcal{V}\) queries \(g\) and \(f\) at point \(r\) where \(r \in \mathbb{F}_\mu\) is the vector of sumcheck’s challenges. \(\mathcal{V}\) then checks that \(f(r) + \rho g(r)\) is consistent with the last message of the sumcheck.

The completeness of the ZK PIOP holds obviously, it was shown in [34] that the PIOP also preserves soundness. The zero knowledge property is proved in [79] and we state it below.

Lemma A.2 (Theorem 3 of [79]). For every field \(\mathbb{F}\), verifier \(\mathcal{V}^*\) and multivariate polynomial \(f \in \mathcal{F}_\mu^{(\leq d)}\), there is a simulator \(S_{\text{sum}}(\mathbb{F}, \mu, d, H)\) that perfectly simulates \(\mathcal{P}\)'s oracle answers except for \(f(r)\). Here \(H := \sum_{x \in B_\mu} f(x)\).
A.4 Zero knowledge compilation for SumCheck-based PIOPs

A general description to the sumcheck-based PIOPs. The multivariate PIOPs considered in this paper can all be adapted to the following format.

General sumcheck-based PIOPs:

1. Both P and V have oracle access to a public multilinear polynomial \( p_0 \in \mathcal{F}^{(\leq 1)}_{\mu_0} \).

2. For every \( i \in [k_1] \), P sends a multilinear polynomial \( p_i \in \mathcal{F}^{(\leq 1)}_{\mu_i} \), and V sends some random challenges. \( p_i \) is a function of \( p_0, \ldots, p_{i-1} \) and verifier’s previous challenges.

3. P and V sequentially run \( k_2 \) sumcheck PIOPs. The \( i \)-th (\( 1 \leq i \leq k_2 \)) sumcheck is over a polynomial \( f_i := h_i(g_1, \ldots, g_{c_i}) \in \mathcal{F}^{(\leq d_i)}_{\nu_i} \), where \( h_i \) is public information and each multilinear polynomial \( g_j \in \mathcal{F}^{(\leq 1)}_{\nu_i} \) (\( 1 \leq j \leq c_i \)) is \( g_j := v|_{X_s=b} \) for some boolean vector \( b \) and some \( v \in \{p_1, \ldots, p_{k_1}\} \), that is, \( g_j \) is a partial polynomial of \( v \) where the variables in \( S \) are set to \( b \).

4. For every \( i \in [k_2] \), V queries a random point \( r_i \in \mathbb{F}_{p_i}^{q_i} \) to the oracle \( f_i \), where \( r_i \) are the round challenges in the \( i \)-th sumcheck. V then checks that \( f_i(r_i) \) is consistent with the last message in the \( i \)-th sumcheck.

5. For every \( i \in [k_3] \), the verifier queries a point \( c_i \in \mathbb{F}_{\mu_i}^{\nu_i} \) to an oracle \( p_{j_i} \) (\( 0 \leq j_i \leq k_1 \)) and checks that the evaluation is \( y_i \). We emphasize that the evaluations \( \{y_i\}_{i \in [k_3]} \) can be efficiently and deterministically derived from \( \{c_i, j_i\}_{i \in [k_3]} \) and the public oracle \( p_0 \).

We note that the above description captures all of the multivariate PIOPs in this paper because

- for the case where P sends an oracle \( f := h(g_1, \ldots, g_c) \in \mathcal{F}^{(\leq d)}_{\mu} \) for \( d > 1 \), we can instead let P send \( g_1, \ldots, g_c \in \mathcal{F}^{(\leq 1)}_{\mu} \) as \( h \) is public information;

- for the case where P sends multiple multilinear oracles in a round, we can merge the polynomials into a single polynomial;

- the PIOPs we consider are all finally reduced to one or more sumcheck PIOPs.

Construction. We present a generic framework that transforms any (sumcheck-based) multivariate PIOPs into zero knowledge PIOPs. For a PIOP \( \langle P, V \rangle \), let \( \{p_i\}_{i \in [0,k_1]}, \{f_i\}_{i \in [k_2]} \) be the polynomials denoted in the above protocol. For every \( i \in [k_1] \), let \( t_i \in \mathbb{N} \) be the number of \( p_i \)'s partial polynomials that appear in the sumcheck polynomials \( f_1, \ldots, f_{k_2} \), and let \( t^* := \max\{t_i\}_{i \in [k_1]} \). For every \( i \in [k_1] \), we assume that there exists index \( \ell_i \in [\mu_i] \) such that for every \( p_i \)'s partial polynomial \( v|_{X_s=b} \) that appears in some sumcheck (where \( p_i \)'s variables in set \( S \) are boolean), it holds that \( \ell_i \) is not in the set \( S \). Let \( \text{msk} \) be the masking algorithm described in Lemma A.1. The compiled zero knowledge protocol \( \langle \hat{P}, \hat{V} \rangle \) works as follows.

The ZK-compiler for sumcheck-based PIOPs:

1. For every \( i \in [k_1] \), \( \hat{P} \) sends an oracle \([(p_i^*)] \) where \( p_i^* \overset{*}{\leftarrow} \text{msk}(p_i, t_i, \ell_i) \). \( \hat{V} \) sends the same challenges as \( V \) does.
2. \(\hat{P}\) and \(\hat{V}\) sequentially run \(k_2\) zero knowledge sumcheck PIOPs (Section A.3). The \(i\)-th (1 \(\leq i \leq k_2\)) sumcheck is over the polynomial \(f_i^* := h_i(g_1^*, \ldots, g_{c_i}^*) \in \mathcal{F}_{\nu_i}^{(d, t^*)}\), where \(h_i\) is the same as in \(\langle P, V \rangle\); each \(g_j^* \in \mathcal{F}_{\nu_i}^{(d, t^*)}\) (1 \(\leq j \leq c_i\)) is \(g_j^* := \nu^*_{x_s=b}\) for some boolean vector \(b\) and some \(\nu^* \in \{p_1^*, \ldots, p_{k_1}^*\}\).

3. For every \(i \in \{k_2\}, \hat{V}\) queries a random point \(r_i \in \mathbb{F}^{\nu_i}\) to the oracle \(f_i\), where \(r_i\) are the round challenges in the \(i\)-th ZK sumcheck. \(\hat{V}\) then checks that \(f_i(r_i)\) is consistent with the last message of the \(i\)-th ZK sumcheck. We emphasize a slight modification over the original PIOP \(\langle P, V \rangle\): in the \(i\)-th sumcheck, \(\hat{V}\) samples each round challenge \(r_{i,j}\) (1 \(\leq j \leq \mu_i\)) in the set \(\mathbb{F} \setminus \{0, 1, r_{1,j}, \ldots, r_{i-1,j}\}\) rather than in \(\mathbb{F}\).

4. \(\hat{V}\) simulates \(V\), i.e., for all \(i \in \{k_3\}\), queries points \(c_i\) to oracle \(p_i^*\) and checks the evaluation.

**Theorem A.3.** Given any PIOP \(\langle P, V \rangle\) for some relation over the boolean hypercube, the compiled PIOP \(\langle \hat{P}, \hat{V} \rangle\) is HVZK. Moreover, \(\langle \hat{P}, \hat{V} \rangle\) preserves perfect completeness and negligible soundness.

**Proof.** **Completeness.** Completeness holds because the sumcheck relations are over boolean hypercubes and the masked polynomials’ evaluations do not change over the boolean hypercubes by the property of \(\text{msk}\).

**Soundness.** Compared to the sumchecks in \(\langle P, V \rangle\), the following changes of the sumchecks in \(\langle \hat{P}, \hat{V} \rangle\) affect soundness error:

1. The degrees of the sumcheck polynomials are increased by a factor \(t^*\).

2. The challenge space of \(j\)-th round in the \(i\)-th (1 \(\leq i \leq k_2\)) sumcheck is \(\mathbb{F} \setminus \{0, 1, r_{1,j}, \ldots, r_{i-1,j}\}\) rather than \(\mathbb{F}\).

3. The sumcheck protocols are replaced with ZK sumchecks.

Since \(t^*\) and \(k_2\) are constants and ZK sumchecks preserves soundness [34], the compiled protocol preserves negligible soundness.

**HVZK.** We describe the simulator as follows.

The simulator \(\mathcal{S}^V(\mathbb{F}, i; \mathbf{x})\):

1. Honestly generate the public polynomial \(p_0 \in \mathcal{F}_{\nu_0}^{(1)}\).

2. Pick arbitrary polynomial \(\{\tilde{p}_i\}_{i \in [k_1]}\) conditioned on that the sumcheck relations over \(f_1, \ldots, f_{k_2}\) hold. Send \(\hat{V}\) polynomials \(\{\tilde{p}_i^*\}_{i \in [k_1]}\) where \(\tilde{p}_i^* \leftarrow \text{msk}(\tilde{p}_i, t_i, \ell_i)\), obtain from \(\hat{V}\) the challenges in the first \(k_1\) rounds.

3. Run the next \(k_2\) ZK sumcheck PIOPs using \(p_0\) and the sampled polynomials \(\{\tilde{p}_i^*\}_{i \in [k_1]}\).

4. For every \(i \in [k_2]\), answer query \(f_i^*(r_i)\) honestly using \(\{\tilde{p}_i^*\}_{i \in [k_1]}\).

5. For every \(i \in [k_3]\), answer query \(c_i\) with value \(y_i\), where \(\{y_i\}_{i \in [k_3]}\) are deterministically derived from \(\{c_i, j_i\}_{i \in [k_3]}\) and the public polynomial \(p_0\).

Next we show that \(\mathcal{S}^V(\mathbb{F}, i; \mathbf{x}) \approx \text{View}(\hat{P}(\mathbb{F}, i; \mathbf{x}; w), \hat{V})\). We set \(H_0 := \mathcal{S}^V(\mathbb{F}, i; \mathbf{x})\) and consider following hybrid games.

62
• Game $H_1$: identical to $H_0$ except that step 3 is replaced with the ZK sumcheck simulator’s output. We note that $H_1 \approx H_0$ by the ZK property of the ZK sumchecks.

• Game $H_2$: identical to $H_1$ except that the queries in step 4 are answered with random values. (Note that $f_r^*(r_i)$’s answer is a random value consistent with the last message of the $i$-th sumcheck.) We argue that $H_2 \approx H_1$: for every $i \in [k_1]$, the number of queries to oracle $\tilde{p}_i \leftarrow \text{msk}(p_i, t_i, \ell_i)$ is no more than $t_i$ and the $\ell_i$-th element in each of the query point are distinct and non-boolean, by Lemma A.1, the answers to the queries are uniformly random.

• Game $H_3$: identical to $H_2$ except that the polynomials $\{\tilde{p}_i\}_{i \in [k_1]}$ in step 2 are replaced with $\{p_i\}_{i \in [k_1]}$. Note that $H_3 \approx H_2$ as the verifier’s view does not change at all.

• Game $H_4 := \text{view}(\hat{P}(F,i; x; w), \hat{V})$: identical to $H_3$ except that the queries in step 4 are answered honestly and the ZK sumchecks are run honestly using $p_0$ and the sampled polynomials $\{p_i\}_{i \in [k_1]}$. With similar arguments (for $H_1$ and $H_2$) we have $H_4 \approx H_3$.

Given above, it holds that $S^{\hat{V}}(F,i; x) \approx \text{view}(\hat{P}(F,i; x; w), \hat{V})$ and we complete the proof. □

A.5 zk-SNARKs from PIOPs

In the ZK PIOP of Section A.4, the masked polynomials sent by the prover are with the form $f^* := f + Z(x_\ell) \cdot R(x_\ell)$ where $f \in F_\ell^{(\leq 1)}$ is multilinear and $Z(x_\ell) := x_\ell \cdot (1 - x_\ell)$ is univariate and with degree $t + 1$. It is shown in Theorem 10 of [19] that every additive and $m$-spanning PCS can be compiled into a hiding PCS with a zero-knowledge Eval protocol, where $m$-spanning means that commitments to polynomials of degree at most $m$ can already generate the commitment space $G$. Thus we can construct a hiding PCS for $f^*$ with ZK evaluations from any additive and spanning polynomial commitment schemes (e.g., KZG and FRI). In particular, one instantiation is to set the commitment of $f^*$ to be $(C_1, C_2) \in G$ where $C_1$ is the multilinear commitment to $f$ and $C_2$ is the univariate commitment to $Z(X) \cdot R(X)$, then apply the ZK transformation in [19].

By combining Theorem 2.4 and Theorem A.3 we obtain the following corollary.

Corollary A.4. Given any (non-hiding) additive and spanning polynomial commitment schemes, we can transform any (non-ZK) sumcheck-based PIOP (Section A.4) for relation $R$ to a zk-SNARK for $R$.

B The FRI-based multilinear polynomial commitment

In this Section, we construct a simple multilinear polynomial commitment scheme (PCS) from FRI [9]. Along the way, we also show how to generically transform a univariate PCS to a multilinear PCS using the tensor-product univariate PIOP from [24], which might be of independent interest. We note that Virgo [84, §3] describes another scheme constructing multilinear PCS from FRI. The main idea is to build the evaluation opening proof from a univariate sumcheck [12], which in turn uses FRI. However, the naive scheme incurs linear-time overhead for the verifier. Virgo [84, §3] resolves the issue by delegating the verifier computation to the prover. To this end, the prover needs to compute another GKR proof convincing that the linear-time verifier will accept the proof. This complicates the scheme and adds additional concrete overhead on prover time and proof size.
We refer to [9, 57] and [53] for background of FRI low-degree testing and the approach to build univariate PCS from FRI. We note that the FRI-based univariate PCS supports batch opening. The evaluation opening protocol for multiple points on multiple polynomials invokes only a single call to the FRI protocol. Below we present a generic approach to transforming any univariate PCS into a multilinear PCS.

**Generic transformation from univariate PCS to multilinear PCS.** Bootle et al. built a univariate PIOP for the tensor-product relation in Section 5 of [24]. The tensor-product relation $(\mathbf{x}, \mathbf{w}) = ((\mathbb{F}, n, z_1, \ldots, z_\mu, y), f)$ states that $f \in \mathbb{F}^n$ satisfies that $(f, \otimes_j (1, z_j)) = y$, where $\langle \cdot, \cdot \rangle$ denotes an inner product, and $\otimes$ denotes a tensor product. The PIOP naturally implies an algorithm that transforms univariate polynomial commitment schemes to multilinear polynomial commitment schemes.

- The commitment to a multilinear polynomial $\tilde{f}$ with monomial coefficients, $f$ is the commitment to a univariate polynomial $f$ with the same coefficients.
- To open $\tilde{f}$ at point $(z_1, \ldots, z_\mu)$ that evaluates $y$, the prover and the verifier runs the univariate PIOP for the relation $(\mathbf{x}, \mathbf{w}) = ((\mathbb{F}, n, z_1, \ldots, z_\mu, y), f)$, which reduces to a batch evaluation on a set of $\mu + 1$ univariate polynomials.

We provide the concrete construction below. Let $P_{w} = (\text{Setup}, \text{Commit}, \text{BatchOpen}, \text{BatchVfy})$ be a univariate PCS, we construct a multilinear PCS $P_{m}$ as follows.

- $P_{m}.\text{Setup}(1^\lambda, \mu) \rightarrow (ck, vk)$. On input security parameter $\lambda$ and the number of variables $\mu$, output $P_{w}.\text{Setup}(1^\lambda, n)$ where $n = 2^\mu$.
- $P_{m}.\text{Commit}(ck, f) \rightarrow c$. On input committer key $ck$, multilinear polynomial $\tilde{f}$ with coefficients $f \in \mathbb{F}^n$, output $P_{w}.\text{Commit}(ck, f)$ where $f$ has the same coefficients as $\tilde{f}$.
- $P_{m}.\text{Open}(ck, \tilde{f}, z, y) \rightarrow \pi$. On input committer key $ck$, multilinear polynomial $\tilde{f}$, point $z \in \mathbb{F}^\mu$ and evaluation $y \in \mathbb{F}$, the prover computes the proof as follows. Let $f_0(X) := f(X)$ be the committed univariate polynomial that has the same coefficients as $\tilde{f}$, consider the following PIOP for the tensor-product relation $(\mathbf{x}, \mathbf{w}) = ((\mathbb{F}, n, z, y), f)$:

  - The prover sends the verifier univariate polynomials $f_1, \ldots, f_\mu$ such that for all $i \in [\mu]$, $f_i(X) = g_{i-1}(X) + z_i \cdot h_{i-1}(X)$, where $g_{i-1}, h_{i-1}$ satisfies that $f_{i-1}(X) = g_{i-1}(X^2) + X \cdot h_{i-1}(X^2)$.
  - The verifier samples a random challenge $\beta \leftarrow \mathbb{F}^\times$ (where $\mathbb{F}^\times$ is a multiplicative subgroup of $\mathbb{F}$), and queries the oracles to obtain evaluations $\{a_i, b_i, c_i\}_{i \in \{0, \ldots, \mu\}}$ such that $a_i := f_i(\beta), \quad b_i := f_i(-\beta), \quad c_i := f_{i+1}(\beta^2)$.

  Note that we skip $f_{\mu+1}(\beta^2)$ and set $c_\mu := y$.

  - The verifier checks that for all $i \in \{0, \ldots, \mu\}$, $c_i = \frac{a_i + b_i}{2} + z_i \cdot \frac{a_i - b_i}{2\beta}$.

64
The opening proof \( \pi \) comprises (i) the univariate commitments to \( f_1, \ldots, f_\mu \), (ii) the evaluations \( \{a_i, b_i, c_i\}_{i \in \{0, \ldots, \mu\}} \), and (iii) the batch opening proof for polynomials \( (f_0, f_1, \ldots, f_\mu) \) at points \((\beta, -\beta, \beta^2)\), where the random challenge \( \beta \) is derived via the Fiat-Shamir transform.

\[ \text{PC}_m.\text{Vfy}(\text{vk}, c, z, y, \pi) \in \{0, 1\} \]  

On input verifier key \( \text{vk} \), commitment \( c \), point \( z \), evaluation \( y \), and proof \( \pi \), parse \( \pi \) to commitments \( \{c_1, \ldots, c_\mu\} \), evaluations \( \text{evals} \), and the batch opening proof \( \pi^* \). Derive random challenge \( \beta \) via the Fiat-Shamir transform, perform the verification check in the above PIOP, and run \( \text{PC}_u.\text{BatchVfy}(\text{vk}, (c, c_1, \ldots, c_\mu), (\beta, -\beta, \beta^2), \text{evals}, \pi^*) \).

**Efficiency.** We emphasize that when instantiated \( \text{PC}_u \) with the FRI-based PCS, the multilinear polynomial commitment scheme has approximately the same complexity as that in the univariate setting. In particular, the committing phase takes only a Merkle root computation with tree depth \( \log(n) \); the opening phase takes (i) \( \mu \) Merkle commitment computation where the \( i \)-th \((1 \leq i \leq \mu)\) Merkle tree is with size \( 2^{\mu-i} \), and (ii) a univariate PCS batch evaluation protocol that is simply a single call to the FRI protocol.

### C Unrolled and optimized Hyperplonk

In Figure 9 and Figure 10, we present an optimized and batched version of HyperPlonk. The protocol batches the zerochecks and additionally batches all evaluations using \( \mathcal{R}_{\text{BATCH}} \). Moreover, the sumcheck has complexity proportional to \( 2^\mu \) rather than \( 2^{\mu+\nu_w} \) where \( \nu_w \) is the logarithm of the number of wires.

**Proof size analysis of the compiled protocol.** We analyze the concrete proof size of the optimized PIOP. We analyze the proof size after compilation, i.e., where the prover sends commitments and performs evaluation proofs. The prover sends

1. \( \ell_w + 2 \) of \( \mu \)-variate multilinear polynomial commitments (\( \ell_w \) for the witness and 2 for the product polynomial),
2. \( \mu \) of degree \( \max(d-2, \ell_w-1) \) univariate polynomial commitments and \( 2\mu \) claimed evaluations (in the first batched sumcheck),
3. \( 8 + 2 \cdot \ell_w + \ell_q \) claimed multilinear evaluations,
4. 1 univariate evaluation of a batched univariate polynomial,
5. 1 multilinear evaluation of a batched multilinear polynomial, and
6. \( 2 \cdot (\mu + \lceil \log_2(8 + 2 \cdot \ell_w + \ell_q) \rceil) \) field elements for the sumcheck in the PIOP of \( \mathcal{R}_{\text{BATCH}} \).

For KZG-based commitments, the proof size is \( 2 + \ell_w + \mu \ G_1 \) elements and \( 4\mu + 10 + 2 \cdot \ell_w + \ell_q + 2 \lceil \log_2(8 + 2 \cdot \ell_w + \ell_q) \rceil \) field elements. For the case where \( \ell_w = \ell_q = 3 \), using BLS12-381, where \( G_1 \) elements are 48 bytes and field elements are 32 bytes, the proof size becomes 176 \( \cdot \mu + 1168 \) bytes. For \( \mu = 20 \), this is only 4688 bytes.

65
Indexer. The indexer $I$ on an input circuit $C$ calls the permutation indexer $I_{\text{perm}}(\sigma)$: $(\llbracket s_{\text{id}} \rrbracket, \llbracket s_\sigma \rrbracket) \leftarrow I_{\text{perm}}(\sigma)$ and computes the selector polynomial $q \in F_{\mu+\nu_w}^{(1)}$. Let $\ell_w = 2^{\nu_w}$ be the number of wires, denote by
\[
S_\sigma := (\llbracket s_\sigma(0,\nu_w, X) \rrbracket, \llbracket s_\sigma(1,\nu_w, X) \rrbracket, \ldots, \llbracket s_\sigma((\ell_w - 1)\nu_w, X) \rrbracket)
\]
the lists of partial polynomials of $s_\sigma \in F_{\mu+\nu_w}^{(1)}$. The indexer outputs $(\llbracket q \rrbracket, S_\sigma)$.

We note that for all $i \in [0, 2^{\mu+\nu_w})$, $s_{\text{id}} \in F_{\mu+\nu_w}^{(1)}$ evaluates to $i$ at point $(i)_{\mu+\nu_w} \in B_{\mu+\nu_w}$ (where $(i)_{\mu+\nu_w}$ denotes $\mu + \nu_w$-bit binary encoding of $i$). Since multilinear extension is unique, it holds that $s_{\text{id}}(X) = \sum_{i \in [\mu+\nu_w]} 2^{i-1} \cdot X_i$ and thus one can evaluate $s_{\text{id}}$ at any points in time $O(\mu + \nu_w)$. Hence, there is no need for the indexer to output oracle $\llbracket s_{\text{id}} \rrbracket$.

Figure 9: The indexer of the optimized PIOP for $\mathcal{R}_{\text{PLONK}}$. 
\( \mathcal{P}(gp,i,p,w) \) and \( \mathcal{V}(gp,p,[[q]],S_\sigma) \) run the following protocol.

1. \( \mathcal{P} \) sends \( \mathcal{V} \) the witness oracles \( W := ([w_0], [w_1], \ldots, [w_{\ell_w-1}]) \) where \( w_i := w(\langle i \rangle_{v_w}, X) \) is the \( i \)th \((0 \leq i < \ell_w) \) partial polynomial of the witness polynomial \( w \in \mathcal{F}_{\mu+1}^{\leq \ell_w} \).
2. \( \mathcal{V} \) sends input challenge \( r_{io} \leftarrow \mathbb{F}_q^* \), \( R^1_{\text{MSET}} \) challenge \( \gamma \) and \( R^2_{\text{MSET}} \) challenges \( \beta \).
3. \( \mathcal{P} \) computes the product polynomial \( \hat{v} \in \mathcal{F}_{\mu+1}^{\leq \ell} \) from \( W, S_\sigma, s_{id} \) and the challenges \( \beta, \gamma \) (See Section 3.3), where for all \( x \in B_\mu \),

\[
\hat{v}(0, x) = \prod_{i \in [0, \ell_w)} \frac{W_i(x) + \beta \cdot s_{id}(\langle i \rangle_{v_w}, x) + \gamma}{W_i(x) + \beta S_{\sigma,i}(x) + \gamma}.
\]

Here \( W_i, S_{\sigma,i} \) denotes the \( i \)th polynomial in \( W, S_\sigma \) respectively. \( \mathcal{P} \) then sends oracles \( [[\hat{v}(0, X)]], [[\hat{v}(1, X)]] \) to \( \mathcal{V} \).
4. Verifier sends challenges \( \alpha_1, \alpha_2 \) to batch three zerochecks, one resulting from the gate identity (see Section 4.2) and two from the product check (see Section 3.3). The two zerocheck virtual polynomials \( Q_1(X) \in \mathcal{F}_{\mu}^{\leq \ell}, Q_2(X) \in \mathcal{F}_{\mu}^{\leq \ell_w+1} \) for the product check are \( Q_1(X) := \hat{v}(1, X) - \hat{v}(X, 0) \hat{v}(X, 1) \) and

\[
Q_2(X) := \prod_{i \in [0, \ell_w)} (W_i(X) + \beta \cdot s_{id}(\langle i \rangle_{v_w}, X) + \gamma) - \hat{v}(0, X) \prod_{i \in [0, \ell_w)} (W_i(X) + \beta S_{\sigma,i}(X) + \gamma) .
\]

Note that \( \hat{v}(X, 0), \hat{v}(X, 1) \) can be simulated given the oracle accesses to \( [[\hat{v}(0, X)]], [[\hat{v}(1, X)]] \), because for all \( b \in \{0, 1\}, \hat{v}(X, b) = (1 - X_1) \cdot \hat{v}(0, X_2, \ldots, X_\mu, b) + X_1 \cdot \hat{v}(1, X_2, \ldots, X_\mu, b) \).
5. \( \mathcal{V} \) sends zerocheck challenge \( z \leftarrow \mathbb{F}_q^{2m} \)
6. \( \mathcal{P} \) and \( \mathcal{V} \) run sumcheck resulting from batched zerocheck. The sumcheck is of size \( \mu \) and has degree \( \max(\ell_w + 2) \). In each round, the prover sends an oracle to the univariate round polynomial as well as the claimed evaluation. The verifier delays querying the oracles. Similarly, in the last round, the verifier receives the claimed evaluations of all the multilinear polynomials. There are \( 8 + 2 \cdot \ell_w + \ell_q \) total evaluations:

\begin{itemize}
  \item \( 1 + \ell_w \) of \( W \) (\( \ell_w \) from the batched sumcheck, one to check the outputs)
  \item \( 3 \) of \( \hat{v}(0, X) \) and \( 4 \) of \( \hat{v}(1, X) \) from the product check
  \item \( \ell_q \) of \( q \) (one per selector)
  \item \( \ell_w \) of \( S_\sigma \) from the product check (there is no need to query \( s_{id} \) as \( \mathcal{V} \) can efficiently evaluate it).
\end{itemize}

7. \( \mathcal{V} \) uses the claimed evaluations to verify all previous protocols.
8. \( \mathcal{P} \) and \( \mathcal{V} \) run the univariate batch-opening algorithm from [19] to reduce all the round polynomial queries to one.
9. \( \mathcal{P} \) and \( \mathcal{V} \) run \( R_{\text{BATCH}} \) on all evaluations using a degree \( 2, \mu + \lfloor \log_2(8 + 2 \cdot \ell_w + \ell_q) \rfloor \) round sum-check.

In the protocol, the prover directly transmits the round polynomial using \( 2 \) field elements. The verifier can compute the third from the claimed sum.

Figure 10: Optimized PIOP for \( R_{\text{PLONK}} \).