Multi-Key Homomorphic Encryption from TFHE

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Abstract. In this paper, we propose a Multi-Key Homomorphic Encryption (MKHE) which allows homomorphic evaluation of a binary gate (with bootstrapping) on ciphertexts encrypted under different keys. We generalize a low-latency homomorphic encryption scheme of Chillotti et al. (ASIACRYPT 2016) by exploiting a key-extension approach of Brakerski and Perlman (CRYPTO 2016).

All the prior works on MKHE were too inefficient to be used in practice. Our construction improved the performance in terms of both asymptotic and concrete complexity: the length of ciphertexts and the computational costs of a binary gate grow linearly and quadratically on the number of parties, respectively. Furthermore, our scheme is fully-dynamic so that no information about the involved parties needs to be known before the computation and the resulting ciphertext can be reused in further computation with newly joined parties.

To the best of our knowledge, this is the first work to implement an MKHE scheme. Our implementation takes about 0.15 (resp. 0.72) seconds to perform the gate bootstrapping when the number of involved parties is 2 (resp. 4).

Keywords: Multi-key homomorphic encryption · Bootstrapping.

1 Introduction

Homomorphic Encryption (HE) and Multi-Party Computation (MPC) are well-studied cryptographic primitives for secure computation. We can use an HE scheme to outsource the storage and computation to a public cloud, but all data providers should agree on the same public key generated by a secret key owner. MPC performs an interactive protocol between parties to evaluate a circuit without revealing any auxiliary information beyond the computation result, but it usually suffers from a high communication and round complexity.

López-Alt et al. [25] proposed the notion of Multi-Key Homomorphic Encryption (MKHE) which is a variant of HE supporting the computation on ciphertexts encrypted under different keys. This attractive primitive can address the issues of HE and MPC, and it has many applications such as round-efficient MPC (e.g. [15, 1, 21, 30, 27]) and spooky encryption [16]. There have been several researches (e.g. [14, 27, 5, 28, 8]) on MKHE. However, all the previous works
were abstract and far from practical. In particular, the efficiency of MKHE remained an open question for years because there has been no study to implement or compare the MKHE schemes empirically.

In this paper, we present an efficient MKHE scheme based on the learning with errors (LWE) assumption [29] and its ring variant (RLWE) [26]. Our construction can be viewed as a generalization of the TFHE scheme of Chillotti et al. [10], where an LWE-based ciphertext encrypts a single bit and one can evaluate a binary gate on ciphertexts with bootstrapping using the ring GSW (RGSW) scheme [20, 17]. We modify the extension algorithm of Brakerski-Perlman [5] to generate a common bootstrapping key of involved parties from the individual evaluation keys.

This work is no worse than previous ones in terms of both functionality and efficiency. This work is comparable to previous ones in terms of both functionality and efficiency. Our construction is fully-dynamic, meaning that a new party can join the computation at anytime and a resulting ciphertext may be reused for further computation. Meanwhile, the length of ciphertext and the computational costs of a single binary gate grow linearly and quadratically on the number of involved parties, respectively (see Table 1 for a comparison). Furthermore, our scheme is simple to implement and easily compatible with existing techniques for advanced functionalities such as the threshold decryption [23, 2], circuit bootstrapping [11], and a larger plaintext space [3, 6].

Finally, we provide a proof-of-concept implementation with concrete parameter sets. For example, it took 3.4, 10.1, and 56.9 seconds to generate a bootstrapping key, and 0.15, 0.72, and 5.15 seconds for a gate bootstrapping when the number of parties is 2, 4, and 8, respectively, on a personal computer.

Overview of Our Scheme. Chillotti et al. [10] introduced a formalization of (R)LWE over the real torus $T = \mathbb{R} / (\mathbb{Z} / 2)$ and the set of cyclotomic polynomials $T = \mathbb{Z}[X] / (X^N + 1)$, and proposed a FHE scheme (called TFHE) with a low-latency bootstrapping based on the LWE and RGSW schemes. We first describe the TFHE scheme briefly. TFHE generates both LWE secret $s_i \in \mathbb{Z}^n$ and RLWE secret $z_i(X) = \sum_{0 \leq i < N} z_i X^i \in R = \mathbb{Z}[X] / (X^N + 1)$. A TFHE encryption of a single bit $m_i \in \{0, 1\}$ is a vector $\mathbf{ct}_i = (b_i, a_i) \in T_{N+1}$ satisfying $b_i + \langle a_i, s \rangle \approx \frac{1}{4} m_i$ (mod 1). One can homomorphically evaluate a bootstrapped NAND (or any other binary) gate $m = m_1 \land m_2$ on encrypted bits by evaluating a linear combination followed by a fast bootstrapping. The linear combination outputs a ciphertext $\mathbf{ct}' = (b', a')$ such that $b' + \langle a', s \rangle \approx \frac{1}{4} m$ (mod 1). The fast bootstrapping starts with an accumulation, that exploits the RGSW scheme to evaluate the decryption formula and recover the scaling factor of $\frac{1}{4}$. It requires a bootstrapping key which is a sequence of RGSW encryptions of $s_i$ encrypted by $z(X)$. Finally, we extract an LWE ciphertext $\mathbf{ct}'' = (b'', a'') \in T_{N+1}$ such that $b'' + \langle a'', z \rangle \approx \frac{1}{4} m$ (mod 1) for $z = (z_0, \ldots, z_{N-1})$ and run the key-switching process to recover the original LWE secret $s$.

We generalize TFHE to support the homomorphic computation on ciphertexts encrypted under independently generated keys. Each party independently generates the LWE secret $s_i \in \mathbb{Z}^n$ and the RLWE secret $z_i(X) \in R$. An encryp-
Both \#2 and 8 are leveled so a large constant (depending on the maximum only consider the second (main) one between two schemes described in 28). The computation. This work was extended in concurrent researches by Peikert-

Related Works. López-Alt et al. [25] firstly proposed an MKHE scheme based on the NTRU assumption. Clear and McGoldrick [14] introduced an LWE-based construction, and it was significantly simplified by Mukherjee and Wichs [27]. This scheme is single-hop for keys where the list of parties has to be known before the computation. This work was extended in concurrent researches by Peikert-Shiehian [28] and Brakerski-Perlman [5] which design multi-hop (dynamic for keys) MKHEs. Chen, Zhang and Wang [8] constructed a scheme which can encrypt a ring element compared to a single bit of prior works.

We summarize the performance of recent MKHE schemes in Table 1. We only consider the second (main) one between two schemes described in 28. Both 28 #2 and 8 are leveled so a large constant (depending on the maximum level of a circuit to be evaluated) is hidden in the $O(\cdot)$ notation. The space

\[ b + \sum_{i=1}^{k} \langle a'_i, s_i \rangle \approx \frac{1}{2} m \ (\mod 1) \]

\[ b' + \sum_{i \in [k]} \langle a'_i, s_i \rangle \approx \frac{1}{2} m \ (\mod 1) \]. The evaluation is done after arranging the entries and extending the dimension of input ciphertexts to share the same secret.

In the second step, we extract the most significant bits \( \tilde{b} = [2N \cdot b'] \) and \( \tilde{a}_i = [2N \cdot a'_i] \), and initialize the accumulator

\[ \text{ACC} = (-\frac{1}{2} X^b \cdot h(X), 0) \in T^{k+1} \]

for the testing polynomial \( h(X) = \sum_{-N/2<d<N/2} X^d \). Then, we recursively compute the Mux gate (choice function) based on the external product with an RGSW encryption of \( X^{s_{ij}} \).

The main difference between our MKHE scheme and TFHE is that we have to generate an RLWE encryption of \( X^{s_{ij}} \) with respect to the concatenated RLWE secret \( z = (1, z_1, \ldots, z_k) \in R^{k+1} \). We adapt and modify the extension algorithm in previous works [14, 27, 5] which combines a single-key RGSW encryption with auxiliary information about encryption randomness with public information of other parties and returns an RGSW encryption of the same message under the concatenation of individual secrets. Our algorithm returns a ciphertext of dimension \((k+1)\) which is almost half of the previous one, and it is based on the Common Reference String (CRS) model as in prior works. The output \( \text{ACC} \) is an RLWE encryption of \( \frac{1}{2} - \frac{1}{2} X^{b} + \sum_{i=1}^{k} \langle \tilde{a}_i, s_i \rangle \cdot h(X) \) whose constant term is approximately equal to \( \frac{1}{4} m \) as desired since

\[ \tilde{b} + \sum_{i=1}^{k} \langle \tilde{a}_i, s_i \rangle \approx N \cdot m \ (\mod 2N) \]

The computational costs of our bootstrapping pipeline mainly depends on the second accumulation step whose complexity grows quadratically with the number of involved parties.

Finally, we extract an LWE encryption from \( \text{ACC} \) and perform the multi-key-switching procedure. An LWE ciphertext \( \overline{c''} = (b'', a''_1, \ldots, a''_k) \in T^{1+kN} \) from \( \text{ACC} \) satisfies \( b'' + \sum_{i=1}^{k} \langle a''_i, z_i \rangle \approx \frac{1}{2} m \ (\mod 1) \) where \( z_i \in \mathbb{Z}^N \) is the coefficient vector of \( z_i \). The multi-key-switching takes this ciphertext as an input to generate an LWE ciphertext encrypting the same message under \( (s_1, \ldots, s_k) \) by repeating the ordinary key-switching procedure from \( z_i \) to \( s_i \).
<table>
<thead>
<tr>
<th>Scheme</th>
<th>Space Type</th>
<th>Complexity</th>
<th>Time Type</th>
<th>Complexity</th>
<th>Bootstrap</th>
</tr>
</thead>
<tbody>
<tr>
<td>CZW17 [8]</td>
<td>EvalKey</td>
<td>$O(k^3n)$</td>
<td>EvalKey</td>
<td>$O(k^4n)$</td>
<td>No</td>
</tr>
<tr>
<td></td>
<td>Ciphertext</td>
<td>$O(kn)$</td>
<td>Gen</td>
<td>$O(k^3n)$</td>
<td></td>
</tr>
<tr>
<td>PS16 #2 [28]</td>
<td>PK</td>
<td>$O(kn^4)$</td>
<td>Hom Mult</td>
<td>$O(k^{2.37}n^{2.37})$</td>
<td>No</td>
</tr>
<tr>
<td></td>
<td>Ciphertext</td>
<td>$O(k^2n^2)$</td>
<td></td>
<td>$O(k^2n^2)$</td>
<td></td>
</tr>
<tr>
<td>BP16 [5]</td>
<td>PK</td>
<td>$O(kn^3)$</td>
<td>Hom NAND</td>
<td>poly$(k, n)$</td>
<td>Yes</td>
</tr>
<tr>
<td></td>
<td>Ciphertext</td>
<td>$O(kn)$</td>
<td></td>
<td>$O(k^2n^2)$</td>
<td></td>
</tr>
<tr>
<td>This work</td>
<td>Eval Key</td>
<td>$O(k^2n^2)$</td>
<td>Eval Key</td>
<td>$O(k^2n^2)$</td>
<td>Yes</td>
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<td></td>
<td>Ciphertext</td>
<td>$O(kn)$</td>
<td>Gen</td>
<td>$O(k^2n^2)$</td>
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<tr>
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<td></td>
<td>Hom NAND</td>
<td>$O(k^2n^2)$</td>
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</tbody>
</table>

Table 1. Memory (bit-size) and computational costs (number of scalar operations) of MKHE schemes. $k$ denotes the number of parties and $n$ is the dimension of the (R)LWE assumption. PK and EVK denote the public and evaluation (or bootstrapping) keys, respectively.

and time complexity of [8] grow rapidly as the number of parties increases. Its complexity is quasi-linear on the security parameter, however, our scheme can be implemented using a smaller parameter.\(^3\)

[5] is based on an abstract bootstrapping method which evaluates a huge branching program of length $L = \text{poly}(k, n)$ representing the decryption circuit. Its memory requirement grows linearly on the number of parties but it comes from a space-time tradeoff. This idea is easily applicable to our scheme to reduce down the space complexity (see Section 4.2 for details).

2 Background

2.1 Notation

All logarithms are in base two unless otherwise indicated. We denote vectors in bold, e.g. $\mathbf{a}$, and matrices in upper-case bold, e.g. $\mathbf{A}$. We denote by $\langle \cdot, \cdot \rangle$ the usual dot product of two vectors. For a real number $r$, $\lfloor r \rfloor$ denotes the nearest integer to $r$, rounding upwards in case of a tie. We use $x \leftarrow D$ to denote the sampling $x$ according to distribution $D$. For a finite set $S$, $U(S)$ denotes the uniform distribution on $S$. For a real $\alpha > 0$, $D_\alpha$ denotes the Gaussian distribution of variance $\alpha^2$. We let $\lambda$ denote the security parameter throughout the paper: all known valid attacks against the cryptographic scheme under scope should take $\Omega(2^\lambda)$ bit operations.

\(^3\) For the reader who is familiar with previous HE schemes, we note that a gate bootstrapping of TFHE [10, 13] takes only 13ms despite its quadratic complexity with the security parameter. A single multiplication of ring-based schemes [4, 18] with a quasi-linear complexity usually takes longer.
2.2 TLWE and TRLWE

The TFHE scheme, presented for the first time in [10], is based on the TLWE (resp. TRLWE) problem, which is the torus variant of the LWE (resp. RLWE) problem. Instead of working over $\mathbb{Z}/q\mathbb{Z}$, or over the ring $\mathbb{Z}[X]/(X^N + 1) \mod q$ in the ring variant, in TFHE we work over the real Torus $\mathbb{T} = \mathbb{R} \mod 1$ and over $T = \mathbb{T}[X]/(X^N + 1)$, the set of cyclotomic polynomials over $\mathbb{T}$ for a power-of-two integer $N$. In this section and in the following one we present an overview of the TFHE scheme: for more details we refer to [13].

We denote by $R = \mathbb{Z}[X]/(X^N + 1)$ the set of cyclotomic polynomials over $\mathbb{Z}$. Then, we observe that $\mathbb{T}$ and $T$ are modules over $\mathbb{Z}$ and $R$, respectively. This means that they are groups with respect to the addition and they are provided with an external product by an integer or an integer polynomial.

A TLWE sample is a pair $(b, a) \in \mathbb{T}^{n+1}$, where $a$ is sampled uniformly over $\mathbb{T}^n$ and $b = \langle a, s \rangle + e$. The secret key $s$ and error $e$ are sampled from a key distribution $\chi$ on $\mathbb{Z}^n$ and a Gaussian with standard deviation $\alpha > 0$.

By following the same path, a TRLWE sample is a pair of polynomials $(b(X), a(X)) \in T^2$, where $a(X)$ is sampled uniformly and $b(X) = a(X) \cdot z(X) + e(X)$. The secret key $z(X)$ is an integer polynomial of degree $N$ sampled from a key distribution $\psi$ on $R$ and the error polynomial $e(X)$ is sampled from a Gaussian distribution with standard deviation $\beta$.

For $a, b \in \mathbb{R}$ (resp. $T$), we denote by $a \approx b \pmod{1}$ if $a = b + e \pmod{1}$ for a small error $e \in \mathbb{R}$ (resp. $\mathbb{R}[X]/(X^N + 1)$).

We can then define two problems for both TLWE and TRLWE:

- Decision problem: for a fixed TLWE secret $s$ (resp. TRLWE secret $z(X)$), distinguish the uniform distribution over $\mathbb{T}^{n+1}$ (resp. $T^2$) from the TLWE (resp. TRLWE) samples.
- Search problem: given arbitrarily many samples from the TLWE (resp. TRLWE) distribution, find the secret $s$ (resp. $z(X)$).

TLWE samples can be used to encrypt Torus messages. By fixing the message space as a discrete subset $\mathcal{M} \subseteq \mathbb{T}$, a message $\mu \in \mathcal{M}$ can be encrypted by adding the trivial TLWE sample $(\mu, 0)$ to a TLWE sample generated as described in previous paragraphs. Then, the corresponding ciphertext $ct$ is a pair $(b, a) \in \mathbb{T}^{n+1}$, with $b = -\langle a, s \rangle + e + \mu$. In order to decrypt, we compute the phase $\varphi_s$ of the ciphertext $ct$, which is equal to $\varphi_s(ct) = b + \langle a, s \rangle$, and we approximate it to the nearest message possible in $\mathcal{M}$ to retrieve $\mu$. By following the same footstep, we can use TRLWE samples to encrypt torus polynomial messages in $T$.

Thanks to the $\mathbb{Z}$-module structure of the torus and to the $\mathbb{R}$-module structure of $T$, the TLWE and TRLWE samples have additive homomorphic properties. The external integer homomorphic multiplication can be performed thanks to the TRGSW ciphertexts we define in the next section.
2.3 TRGSW and External Product

For a base integer $B \geq 2$ and a degree $d$, we call $g = (1/B, \ldots, 1/B^d)$ the gadget vector. For an integer $k \geq 1$, the gadget matrix is defined by

$$G_k = \mathbf{I}_k \otimes g = \begin{bmatrix} g & 0 & \ldots & 0 \\ 0 & g & \ldots & 0 \\ \vdots & \vdots & \ddots & \vdots \\ 0 & 0 & \ldots & g \end{bmatrix} \in \mathbb{T}^{dk \times k}.$$

For any $u \in \mathbb{T}^k$, we define its base decomposition by a $dk$-dimensional vector $v = G_k^{-1}(u)$ with coefficients in $\mathbb{Z} \cap (-B/2, B/2]$ which minimizes $\|v^T \cdot G_k - u^T\|_\infty$. The decomposition error $\|v^T \cdot G_k - u^T\|_\infty$ is bounded by $1/(2B^{d+1})$.

We identify an arbitrary element of $\mathbb{T}^k$ to the vector of its coefficients in $\mathbb{T}^N$, and naturally extend the base decomposition $G_k^{-1}(\cdot)$ to a function $T^k \rightarrow R^{dk}$ by applying the basic decomposition function coefficient wisely.

Then, we can define the TRGSW samples as the torus variant of RGSW samples, in the same way as we did in previous section\(^4\). For a fixed TRLWE secret $s(X)$, we define a TRGSW sample as $C = Z + \mu \cdot G_2$, where each line of the matrix $Z \in \mathbb{T}^{d \times 2}$ is a TRLWE encryption of 0, $G_2$ is the gadget matrix and the message $\mu \in R$ is an integer polynomial.

TRGSW samples are homomorphic with respect to the addition and to an internal multiplication. Furthermore, an external product, noted $\boxdot$, with TRLWE can be defined as $A \boxdot b = G_2^{-1}(b) \cdot A$, for all TRLWE samples $b$ and TRGSW samples $A$ encrypted with the same secret key. In the following sections, we define a variant of the TRGSW samples and an adapted external product. The internal product between two TRGSW samples $A$ and $B$ encrypted with the same secret key can be defined as a list of independent external products between the cipher $A$ and the lines composing the cipher $B$.

The scheme TFHE has been implemented and is publicly available at [12]. In Section 5 we present some experimental results we obtained by implementing our Multi-Key scheme on top of the TFHE library.

In the rest of the paper, in order to lighten the notations, we will abandon the ‘T’ notation in front of LWE, RLWE and RGSW.

3 Basic Schemes

In this section, we present the LWE [29] and RGSW [20, 17] schemes and describe some extended algorithms that will be used in our MKHE scheme.

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\(^4\) We define only the Ring version TRGSW, since this is the only sample we need in this paper. TGSW can be defined in the same way. For more details we refer to [13].
3.1 Multi-Key-Switching on LWE Ciphertexts

We first describe the standard LWE scheme and generalize its key-switching algorithm to the multi-key case.

**LWE.Setup**($1^λ$): It takes the security parameter as input and generates the LWE dimension $n$, key distribution $χ$, error parameter $α$, decomposition base $B_κ$ and degree $d_κ$. Return the public parameter $pp_{LWE} = (n, χ, α, B_κ, d_κ)$.

An LWE secret $s$ is sampled from the distribution $← χ$.

We use the key-switching gadget vector $g_κ = (1/B_κ, \ldots, 1/B_κ^d)$. Recall that the base decomposition algorithm with respect to $g_κ$ transforms an element $a ∈ \mathbb{R}$ into the $d_κ$-dimensional vector $g_κ^{-1}(a)$ with coefficients in $\mathbb{Z} ∩ (-B_κ/2, B_κ/2]$ which minimizes $|a - (g_κ^{-1}(a), g_κs)|$.

**LWE.Enc**($m$): This is a standard LWE encryption which takes a bit as input and generates the LWE ciphertext $ct = (b, a) ∈ \mathbb{T}^{n+1}$ for $b = \frac{1}{2}m - (a, s) + e$ (mod 1).

Note that the scaling factor is $1/4$, as in FHEW [17] or TFHE [10]. We described a symmetric encryption for simplicity, but this algorithm can be replaced by any LWE-style encryption schemes such as public key encryption [24]. The only requirement is that the output ciphertext should be a vector $ct = (b, a) ∈ \mathbb{T}^{n+1}$ satisfying $b + (a, s) ≈ \frac{1}{2}m$ (mod 1).

**LWE.KSGen**($t, s$): For given LWE secrets $t ∈ \mathbb{Z}^N$ and $s ∈ \mathbb{Z}^n$, it returns the key-switching key $K_S = \{K_{S,j}\}_{j ∈ [N]} \in (\mathbb{T}^{d_κ, \times (n+1)})^N$ from $t$ to $s$. For each $j ∈ [N]$, the $j$-th component is generated by $K_{S,j} = [b_j|A_j]$ where $A_j ← U(\mathbb{T}^{d_κ, \times n})$, $e_j ← D_κ^{d_κ}$ and $b_j = -A_j s + e_j + t_j · g_κs$ (mod 1).

We can transform an LWE ciphertext corresponding to $t$ into another LWE encryption of the same message under the secret $s$ using a key-switching key $K_S ← LWE.KSGen(t, s)$.

We consider the notion of extended LWE encryption and the multi-key-switching procedure. For $k$ LWE secrets $s_1, \ldots, s_k ∈ \mathbb{Z}^n$, an extended ciphertext $\overline{ct} = (b, a_1, \ldots, a_k) ∈ \mathbb{T}^{kn+1}$ will be called an encryption of $m ∈ \{0, 1\}$ with respect to the concatenated secret $\overline{s} = (s_1, \ldots, s_k)$ if $(\overline{ct}, (1, \overline{s})) = b + \sum_{i=1}^k (a_i, s_i) ≈ \frac{1}{2}m$ (mod 1).

**LWE.MKSwitch**($\overline{ct}, K_S$): For a given ciphertext $ct = (b, a_1, \ldots, a_k) ∈ \mathbb{T}^{kn+1}$ and a sequence of the key-switching keys $K_S = \{K_{S,i}\}_{i ∈ [k]}$, compute $(b'_i, a'_i) = \sum_{j=1}^N g_κ^{-1}(a_{i,j}) · K_{S,i} \pmod{1}$ for all $i ∈ [k]$ and let $b' = b + \sum_{i=1}^k b_i' \pmod{1}$. Return the ciphertext $\overline{ct}' = (b', a'_1, \ldots, a'_k) ∈ \mathbb{T}^{kn+1}$.

This multi-key-switching algorithm takes as the input an extended ciphertext $\overline{ct} ∈ \mathbb{T}^{kn+1}$ corresponding to $\overline{t} = (t_1, \ldots, t_k)$ and a sequence of key-switching keys from $t_i$ to $s_i$ and returns an encryption of the same message under $\overline{s} = (s_1, \ldots, s_k)$.
Security. The \( j \)-th component \( \mathbf{K}_s \) of a key-switching key \( \mathbf{K}_s = \{ \mathbf{K}_s \}_{j \in [N]} \) from \( t \in \mathbb{Z}^N \) to \( s \in \mathbb{Z}^n \) is generated by adding \( t_j \cdot g_{ks} \) to the first column of a matrix in \( \mathbb{T}^{d_{ks} \times (n+1)} \) whose rows are LWE instances under the secret \( s \).

Therefore, \( \mathbf{K}_s \leftarrow \text{LWE}_{\mathbf{K}_s \text{Gen}}(t, s) \) is computationally indistinguishable from the uniform distribution over \( \mathbb{T}^{d_{ks} \times (n+1)} \) under the LWE assumption with parameter \( (n, \chi, \beta) \) if \( s \) is sampled according to \( \chi \).

Correctness. We show that if the input \( \mathbf{ct} = (b, a_1, \ldots, a_k) \) is an LWE ciphertext encrypted by \( \bar{t} = (t_1, \ldots, t_k) \) and \( \mathbf{K}_s \)'s are key-switching keys from \( t_i \in \mathbb{Z}^N \) to \( s_i \in \mathbb{Z}^n \) for \( i \in [k] \), respectively, then the output ciphertext encrypts the same message under the concatenated secret \( \bar{s} = (s_1, \ldots, s_k) \). The correctness of this algorithm is simply shown by the following equation:

\[
\langle \mathbf{ct}', (1, \bar{s}) \rangle = b + \sum_{i=1}^{k} (b'_i + \langle a'_i, s_i \rangle)
\]

\[
\approx b + \sum_{i=1}^{k} \sum_{j=1}^{N} \langle g^{-1}_{ks}(a_{i,j}), t_{i,j} \cdot g_{ks} \rangle \approx \langle \mathbf{ct}, (1, \bar{t}) \rangle \pmod{1}.
\]

Therefore, \( \mathbf{K}_s = \{ \mathbf{K}_s \}_{i \in [k]} \) can be considered as a key-switching key from \( \bar{t} \in \mathbb{Z}^{kN} \) to \( \bar{s} \in \mathbb{Z}^{kn} \).

3.2 Extension of RGSW Ciphertexts and Generalized External Product

The GSW scheme has been widely used in the construction of cryptographic primitives due to its intrinsic characteristics. For example, the ciphertext extension procedure in recent MKHE schemes [5, 28, 8] is based on the GSW homomorphic arithmetic without evaluation key. Meanwhile, HE schemes with low-latency bootstrapping [17, 10] enjoy the compatibility of GSW with classical (R)LWE ciphertexts (e.g. external product, key-switching).

In this section, we present a variant of RGSW which supports a new ciphertext extension and a generalized external product on extended ciphertexts. It is similar to the existing techniques in previous work on MKHE, but our solution achieves a better performance by reducing the dimension of ciphertexts and the complexity of operations.

**RGSW.Setup(\( \lambda \))**: It takes as input the secret parameter \( \lambda \).

1. Set the RLWE dimension \( N \) which is a power of two.
2. Set the key distribution \( \psi \) over \( R \) and choose the error parameter \( \alpha \).
3. Set the base integer \( B \geq 2 \) and the decomposition degree \( d \).
4. Generate a random vector \( \mathbf{a} \leftarrow U(T^d) \).

Return the public parameter \( pp_{\text{PSW}} = (N, \psi, \alpha, B, d, \mathbf{a}) \).

Our scheme is based on the CRS model since it requires all participants to share a randomly generated vector \( \mathbf{a} \). The RGSW parameter should be chosen...
appropriately so that the RLWE problem with parameter \((N, \psi, \alpha)\) achieves at least \(\lambda\)-bit security level.

**RGSW.KeyGen**\( (pp)\): Sample \(z \leftarrow \psi\) and return the secret key \(SK \leftarrow z\). We write \(z = (1, z)\). Sample an error vector \(e \leftarrow D_o\) and set the public key as \(PK \leftarrow P = [b|a] \in T^{d \times 2}\) where \(b = -a \cdot z + e \mod 1\). Return \((z, P)\).

**RGSW.UniEnc**\( (\mu, z, P)\): For an input plaintext \(\mu \in R\), a secret key \(z\) and a public key \(P\), it generates and returns the ciphertexts \((C, D, F) \in (T^{d \times 2})^3\) as follows:

1. Sample \(c_1 \leftarrow U(T^d)\) and \(e_c \leftarrow D_o\). Output the ciphertext \(C = [c_0|c_1] \in T^{d \times 2}\) where \(c_0 = -z \cdot c_1 + e_c + \mu \cdot g\) (mod 1).
2. Sample a randomness \(r \leftarrow \psi\) and an error matrix \(E \leftarrow D_o\). Output the ciphertext \(D = r \cdot P + E + [0|\mu \cdot g]\) (mod 1) in \(T^{d \times 2}\).
3. Sample \(f_i \leftarrow U(T^d)\) and \(e_f \leftarrow D_o\). Output the ciphertext \(F = [f_0|f_i] \in T^{d \times 2}\) where \(f_0 = -z \cdot f_i + e_f + r \cdot g\) (mod 1).

Our RGSW scheme takes as an input the pair of secret and public keys. The third component \(F\) encrypts the randomness \(r \in R\) used in the generation of \(D\). Note that an uni-encryption \((C, D, F)\) is 1.5 times the size of ordinary RGSW ciphertexts in \(T^{2d \times 2}\).

The following algorithm transforms an uni-encryption \((C, D, F)\) encrypted by the \(i\)-th party into an RGSW ciphertext corresponding to the set of \(k\) parties based on their public key \(\{P_j\}_{j \in [k]}\).

**RGSW.Expand**\( ([C, D, F], i, \{P_j\}_{j \in [k]}\): It takes as the input a ciphertext \((C, D, F) \in (T^{d \times 2})^3\), an index \(i \in [k]\), and a sequence of public keys \(\{P_j\}_{j \in [k]}\). For each \(j \in [k]\), compute the vectors \(x_j, y_j \in R_Q^d\) by \(x_j[\ell] = (g^{-1}(b_j[\ell] - b_i[\ell]), f_i)\) and \(y_j[\ell] = (g^{-1}(b_j[\ell] - b_i[\ell]), f_i)\) for all \(\ell \in [d]\), i.e., \([x_j|y_j] = M_j F \in T^{d \times 2}\) where \(M_j \in T^{d \times d}\) is the matrix of which \(\ell\)-th row vector is \(g^{-1}(b_j[\ell] - b_i[\ell])\). Return the expanded ciphertext

\[
C = \begin{bmatrix}
c_0 & 0 & \cdots & c_1 & \cdots & 0 \\
d_0 + x_1 & d_1 & \cdots & y_1 & \cdots & 0 \\
\vdots & \vdots & \ddots & \ddots & \ddots & \vdots \\
d_0 & 0 & \cdots & d_1 & \cdots & 0 \\
\vdots & \vdots & \ddots & \ddots & \ddots & \vdots \\
d_0 + x_k & 0 & \cdots & y_k & \cdots & d_1
\end{bmatrix} \in T^{d(k+1) \times (k+1)}.
\]

**RGSW.ExtProd**\( (\tau, C)\): For an RLWE ciphertext \(\tau \in T^{k+1}\) and an RGSW ciphertext \(C \in T^{d(k+1) \times (k+1)}\), compute and return the ciphertext \(\tau' = G_{k+1}^{-1}(\tau) \cdot C\) (mod 1) in \(T^{k+1}\).
A ciphertext $\overline{c} \in T^{d(k+1) \times (k+1)}$ is called an extended RGSW encryption of $\mu \in R$ corresponding to a secret $\overline{z} = (1, z_1, \ldots, z_k) \in R^{k+1}$ if $\overline{cz} \approx \mu \cdot G_{k+1} \overline{z}$ (mod 1). Based on the public information $P_j$, our ciphertext extension algorithm transforms an uni-encryption $(C, D, F)$ corresponding to the $i$-th secret $(SK_i, PK_i) = (z_i, P_i)$ into an extended RGSW ciphertext $\overline{c}$ which encrypts the same message under the concatenated secret $\overline{z} = (1, z_1, \ldots, z_k) \in R^{k+1}$. It requires $O(kd^2)$ polynomial operations, and we need only $(2k + 1)d$ polynomials to represent $\overline{c}$ due to its sparsity.

Our scheme also provides the external product algorithm (though our scheme has no RLWE encryption) which multiplies an RGSW ciphertext to an RLWE ciphertext where both are encrypted by the same key $\overline{z}$. If $\overline{c}$ is an output of the RGSW expansion algorithm (so is sparse), then the external product takes $O(kd)$ polynomial operations.

**Security.** We claim that the distribution

$$D_0 = \{ (P, C, D, F) : (z, P) \leftarrow \text{RGSW.KeyGen}(pp^{\text{RGSW}}), (C, D, F) \leftarrow \text{RGSW.UniEnc}(\mu, z, P) \}$$

is computationally indistinguishable from the uniform distribution over $(T^{d \times 2})^4$ for any $\mu \in R$ under the RLWE assumption with parameter $(N, \psi, \alpha)$. We consider the following distributions:

$$D_1 = \{ (P, C, D, F) : P, C, F \leftarrow U(T^{d \times 2}), r \leftarrow \psi, E \leftarrow D_a^{d \times 2}, D = r \cdot P + E + [0]_{\mu} \cdot g \text{ (mod 1)} \},$$

$$D_2 = \{ (P, C, D, F) : P, C, D, F \leftarrow U(T^{d \times 2}) \}.$$

First, we can change the RLWE samples $P$, $C$, and $F$ of the secret $z$ to random matrices. Then, $D$ is changed to a uniform distribution using the RLWE assumption of secret $r$ again. Since $D_2$ is independent from $\mu$, our RGSW scheme is IND-CPA secure.

**Correctness.** We briefly show the correctness of our ciphertext expansion and external product algorithms. We refer the reader to Appendix A for a detailed noise analysis.

Let $(z_j, P_j) \leftarrow \text{RGSW.KeyGen}(pp^{\text{RGSW}})$ be independently generated RGSW keys for $j \in [k]$ and let $(C, D, F) \leftarrow \text{RGSW.UniEnc}(\mu, z_i, P_i)$ be an uni-encryption of $\mu$ under the $i$-th key. We claim that $\overline{c} \leftarrow \text{RGSW.Expand}((C, D, F), i, \{P_j\}_{j \in [k]})$ is an RGSW encryption of $\mu$ corresponding to the concatenated secret $\overline{z} = (1, z_1, \ldots, z_k)$, i.e., $\overline{cz} \approx \mu \cdot G_{k+1} \overline{z}$ (mod 1). Since $c_0 + z_i \cdot c_1 = Cz_i \approx \mu \cdot g$ (mod 1), it suffices to show that $(d_0 + x_j) + z_j \cdot d_1 + z_i \cdot y_j \approx \mu z_j \cdot g$ (mod 1) for all $j \in [k]$. It is derived from the following equations:

$$d_0 + z_j \cdot d_1 \approx \mu z_j \cdot g + r \cdot P_i z_j \approx \mu z_j \cdot g + r \cdot (b_i - b_j) \quad (\text{mod 1}),$$

$$x_j + z_i \cdot y_j = M_j Fz_i \approx r \cdot M_j g \approx r \cdot (b_j - b_i) \quad (\text{mod 1}).$$

Now let us suppose that $\overline{c} \in T^{k+1}$ is an RLWE ciphertext and $\overline{c} \in T^{d(k+1) \times (k+1)}$ is an RGSW encryption of $\mu$ with respect to the secret $\overline{z} \in R^{k+1}$, i.e., $\overline{cz} \approx \mu \cdot G_{k+1} \overline{z}$.
In this section, we explicitly describe an MKHE scheme based on the LWE and RGSW schemes. Our scheme can bootstrap a ciphertext after the evaluation of a binary gate as in TFHE [10], but it requires to pre-compute the bootstrapping key corresponding to the set of parties involved in a computation.

4 Our MKHE Scheme

4.1 Description

In this section, we explicitly describe an MKHE scheme based on the LWE and RGSW schemes. Our scheme can bootstrap a ciphertext after the evaluation of a binary gate as in TFHE [10], but it requires to pre-compute the bootstrapping key corresponding to the set of parties involved in a computation.

\( \text{MKHE.Setup}(1^\lambda): \)

- Run \( \text{LWE.Setup}(1^\lambda) \) to generate the parameter \( pp^{LWE} = (n, \chi, \alpha, B_k, d_k) \).
- Run \( \text{RGSW.Setup}(1^\lambda) \) to generate the parameter \( pp^{RGSW} = (N, \psi, \beta, B, d, a) \).
- Return the generated public parameters \( pp = (pp^{RGSW}, pp^{LWE}) \).

\( \text{MKHE.KeyGen}(pp): \)

- Run \( (z, P) \leftarrow \text{RGSW.KeyGen}(pp^{RGSW}) \) and set the public key as \( PK = P \). Write \( t = (z_0, -z_{N-1}, \ldots, -z_1) \in \mathbb{Z}^N \) for \( z(X) = z_0 + z_1 X + \ldots + z_{N-1} X^{N-1} \).
- Sample the LWE secret \( s = (s_1, \ldots, s_n) \leftarrow \psi \).
- Generate \( (C_\ell, D_\ell, F_\ell) \leftarrow \text{RGSW.UniEnc}(s_\ell, z, P) \) for \( \ell \in [n] \) and set the bootstrapping key as \( BK = \{(C_\ell, D_\ell, F_\ell)\}_{\ell \in [n]} \).
- Generate the key-switching key \( KS \leftarrow \text{LWE.ksGen}(t, s) \).
- Return the secret key \( s \). Publish the triple \( (PK, BK, KS) \) of public, bootstrapping, and key-switching keys.

We remark that for any \( a(X) = a_0 + a_1 X + \cdots + a_{N-1} X^{N-1} \in T \) and the vector of its coefficients \( a = (a_0, \ldots, a_{N-1}) \in \mathbb{T}^N \), the constant term of \( a(X) \cdot z(X) \in T \) is equal to \( (a, t) \) modulo 1.

\( \text{MKHE.Enc}(m): \) For an input bit \( m \in \{0, 1\} \), run \( \text{LWE.Enc}(m) \) and return an LWE encryption with the scaling factor 1/4. The output ciphertext \( ct = (b, a) \in \mathbb{T}^{n+1} \) satisfies \( b + (a, s) \approx \frac{1}{4}m \) (mod 1).

The dimension of a ciphertext increases after homomorphic computations. The indices of related parties should be stored together with a ciphertext for the correct decryption and homomorphic operations.

\( \text{MKHE.Dec}(\overline{c}, \{s_j\}_{j \in [k]}): \) For a ciphertext \( \overline{c} = (b, a_1, \ldots, a_k) \in \mathbb{T}^{kn+1} \) and a tuple of secrets \( (s_1, \ldots, s_k) \), return the bit \( m \in \{0, 1\} \) which minimizes \( |b + \sum_{j=1}^k (a_j, s_j) - \frac{1}{4}m| \).

\( \text{MKHE.NAND}(\overline{c}_1, \overline{c}_2, \{(PK_j, BK_j, KS_j)\}_{j \in [k]}): \) It takes as input two LWE ciphertexts \( \overline{c}_1 \in \mathbb{T}^{kn+1} \) and \( \overline{c}_2 \in \mathbb{T}^{kn+1} \), where \( [k] \) is the set of indices of the parties that are involved in either \( \overline{c}_1 \) or \( \overline{c}_2 \). For \( j \in [k] \), we denote by \( PK_j = P_j \),...
Let $\tilde{a}_i = (\tilde{a}_{i,\ell})_{\ell \in [n]}$ and $\tilde{b}_i = (\tilde{b}_{i,\ell})_{\ell \in [n]}$.

This algorithm consists of three phases. The first step expands the input LWE ciphertexts and evaluates the NAND gate homomorphically over encrypted plaintexts.

1-1. Extend $\overline{\Pi}_1$ and $\overline{\Pi}_2$ to the ciphertexts $\overline{\Pi}_1, \overline{\Pi}_2 \in T^{kn+1}$ which encrypt the same messages under the concatenated secret key $\overline{s} = (s_1, \ldots, s_k) \in \mathbb{Z}^{kn}$.

It is simply done by rearranging the components and putting zeros in the empty slots.

1-2. Compute $\overline{\Pi}' = (\overline{s}, 0, \ldots, 0) - \overline{\Pi}_1 - \overline{\Pi}_2 \pmod{1}$.

To be precise, if an input ciphertext $\overline{\Pi}_i = (b_i, a_{i,1}, \ldots, a_{i,k})$ is an encryption corresponding to a tuple $(j_1, \ldots, j_k) \in [k]^{k_1}$ of indices, then (1-1) returns $\overline{\Pi}_i' = (b_i, a'_{i,1}, \ldots, a'_{i,k})$ where $a'_{i,j} = \begin{cases} a_{i,j} & \text{if } j = j_\ell \text{ for some } \ell \in [k_i], \\ 0 & \text{otherwise}; \end{cases}$ for $j \in [k]$. It is clear from the definition that $(\overline{\Pi}_i, (1, s_{j_1}, \ldots, s_{j_k})) = (\overline{\Pi}_i', (1, \overline{s}))$ for $\overline{s} = (s_1, \ldots, s_k)$.

If $\langle \overline{\Pi}_i', (1, \overline{s}) \rangle = \frac{1}{2} m_i + e_i \pmod{1}$ for some errors $e_i \in \mathbb{R}$, then the output ciphertext satisfies that $\langle \overline{\Pi}_i, (1, \overline{s}) \rangle = \frac{1}{2} m + e' \pmod{1}$ for $m = NAND(m_1, m_2)$ and $e' = \pm \frac{1}{2} - e_1 - e_2$ which is bounded by $\frac{1}{4}$ when $|e_i| \leq \frac{1}{16}$. The next step, called homomorphic accumulator [17], is to evaluate the decryption circuit of an extended LWE ciphertext using the external product of RGSW scheme for bootstrapping.

2-1. For $i \in [k]$ and $\ell \in [n]$, run $\overline{\Pi}_{i,\ell} \leftarrow \text{RGSW}\text{.ExtProd}((\overline{C}_{i,\ell}, \overline{D}_{i,\ell}, \overline{F}_{i,\ell}), \{\overline{P}_j\}_{j \in [k]})$ to generate an RGSW encryption of $s_{i,\ell}$ under the secret $\overline{z} = (1, z_{1,1}, \ldots, z_{k}) \in R^{k+1}$. Return the shared bootstrapping key $\overline{B}K := \{\overline{C}_{i,\ell}\}_{i \in [k], \ell \in [n]}$.

2-2. Let $\overline{\Pi} = (b', a'_{1, \ell}, \ldots, a'_{k, \ell}) \in T^{kn+1}$. Compute $\overline{b} = [2N \cdot b']$ and $\overline{a}_i = [2N \cdot a'_i]$. Initialize the RLWE ciphertext as $\text{ACC} = (-\frac{1}{2} h(X) \cdot X^\overline{b}, 0) \in T^{k+1}$ where $h(X) = \sum_{\frac{N}{2} < j < \frac{3N}{2}} X^j = 1 + X + \cdots + X^{\frac{N}{2}-1} - X^{\frac{N}{2}+1} - \cdots - X^{N-1}$.

2-3. Let $\overline{a}_i = (\overline{a}_{i,\ell})_{\ell \in [n]}$ for $i \in [k]$. Compute $\text{ACC} \leftarrow \text{ACC} + \text{RGSW}\text{.ExtProd}((X^\overline{a}_i, 1) \cdot \text{ACC}, \overline{C}_{i,\ell})$

recursively for all $i \in [k]$ and $\ell \in [n]$.

2-4. Return $\text{ACC} \leftarrow (\frac{1}{2}, 0) + \text{ACC} \pmod{1}$.

In (2-1), we generate the bootstrapping key corresponding to the $k$ parties involved in this computation. The accumulator ACC is initialized in (2-2) as a trivial RLWE encryption of $-\frac{1}{2} h(X) \cdot X^\overline{b}$. The main computation is done in (2-3) based on the Mux gate [10]. In each step, it homomorphically selects one of ACC and $X^\overline{a}_i \cdot \text{ACC}$ using the encryption $\overline{C}_{i,\ell}$ of $s_{i,\ell} \in \{0, 1\}$. The output is
an RLWE ciphertext satisfying
\[ \langle \text{ACC}, z \rangle \approx -\frac{1}{8} b(X) \cdot X^{\hat{b} + \sum_{i=1}^{k} (\hat{a}_i, s_i)} \]
\[ = -\frac{1}{8} \left( \sum_{-\frac{N}{2} < j < \frac{N}{2}} X^j \right) \cdot X^{\hat{b} + \sum_{i=1}^{k} (\hat{a}_i, s_i)} \pmod{1}. \]

Since \( \hat{b} + \sum_{i=1}^{k} (\hat{a}_i, s_i) \approx (2N) \cdot \langle \overline{c}t', (1, \overline{s}) \rangle \approx N \cdot m \pmod{2N} \), the constant term of \( \langle \text{ACC}, z \rangle \) is approximately equal to either \( -\frac{1}{8} \) (if \( m = 0 \)) or \( \frac{1}{8} \) (otherwise; \( m = 1 \)), which is \( \frac{1}{8} - \frac{1}{8} = 0 \) or \( \frac{1}{8} \). Finally, the term \( \frac{1}{8} \) is cancelled out in (2-4).

In the last step, we transform \( \text{ACC} \) into an LWE ciphertext and run the multi-key-switching algorithm as follows.

3-1. For \( \text{ACC} = (c_0, c_1, \ldots, c_k) \in T^{k+1} \), let \( b'' \) be the constant term of \( c_0 \) and \( a''_i \) be the coefficient vector of \( c_i \) for \( i \in [k] \). Construct the LWE ciphertext \( \overline{c}t'' = (b'', a''_1, \ldots, a''_k) \in T^{kN+1} \).

3-2. Let \( \overline{KS} = \{\overline{KS}_i\}_{i \in [k]} \). Run the multi-key-switching algorithm and return the ciphertext \( \overline{c}t \leftarrow \text{LWE.mkSwitch}(\overline{c}t'', \overline{KS}) \).

As we noted above, \( \langle a''_i, t_i \rangle \pmod{1} \) is equal to the constant term of \( c_i z_i \) for \( i \in [k] \). Hence, (3-1) returns an LWE ciphertext \( \overline{c}t'' \) satisfying \( \langle \overline{c}t'', (1, \overline{t}) \rangle \approx \frac{1}{8} m \pmod{1} \). The next step (3-2) switches the LWE key and outputs an LWE ciphertext \( \overline{c}t \in T^{kN+1} \) such that \( \langle \overline{c}t, (1, \overline{s}) \rangle \approx \frac{1}{8} m \pmod{1} \), as desired.

**Security.** Our scheme is semantically secure under the (R)LWE assumptions described in the previous section, so the parameters \( pp^{\text{LWE}} \) and \( pp^{\text{RGSW}} \) should be chosen properly to achieve at least \( \lambda \)-bit of security level.

It also requires a circular security assumption because each party publishes uni-encryptions of \( z_1, \ldots, z_n \) encrypted by \( z \), and a key-switching key from \( t = (z_0, -z_{N-1}, \ldots, -z_1) \) to \( s \). However, our circular security assumption is exactly the same as one in TFHE [10], and it is no stronger than the circular security assumptions in *bootstrappable* HE schemes [19] such as [17, 22, 7, 9].

**Correctness.** Our scheme should satisfy the following conditions to guarantee its correctness:

- In (2-1), the quantized ciphertext \( \langle \hat{b}, \hat{a}_1, \ldots, \hat{a}_k \rangle \in \mathbb{Z}_{2N}^{k+1} \) should satisfy \( \hat{b} + \sum_{j=1}^{k} (\hat{a}_j, s_j) = N \cdot m + \hat{e} \) for some \( \hat{e} \in \mathbb{Z} \) with \( |\hat{e}| < N/2 \). This noise \( \hat{e} \) consists of two parts \( \hat{e} = 2N \cdot e' + e'' \) for \( e' = \pm \frac{1}{8} - e_1 - e_2 \) from the step (1-2) and a rounding error \( e'' = (\hat{b} - 2N \cdot b') + \sum_{j=1}^{k} (\hat{a}_j - 2N \cdot a'_j, s_j) \).
- The error \( e \in \mathbb{R} \) of an output LWE ciphertext \( \overline{c}t \) should be small enough for the correct decryption and further computations. It is the sum of the constant term of an RLWE error which is accumulated from the external products during (2-3), and the multi-key-switching error from (3-2).

We provide a rigorous noise estimation in Appendix A. We refer the reader to Section 5 for a recommended parameter set.
### Table 2. Space (number of torus elements) and time (number of scalar operations) complexity of our scheme.

<table>
<thead>
<tr>
<th>Type</th>
<th>Space</th>
<th>Type</th>
<th>Time</th>
</tr>
</thead>
<tbody>
<tr>
<td>(\mathbf{t})</td>
<td>(kn + 1)</td>
<td>BK (\mathbf{BK}) Generation</td>
<td>(O(k^2 \cdot nN \log N \cdot d^2))</td>
</tr>
<tr>
<td>(\mathbf{BK})</td>
<td>((2k + 2)k \cdot nN \cdot d)</td>
<td>Accumulator</td>
<td>(O(k^2 \cdot nN \log N \cdot d))</td>
</tr>
<tr>
<td>(\mathbf{KS})</td>
<td>(k \cdot (n + 1)N \cdot d_{ks} \cdot B_{ks})</td>
<td>Multi-key-switching</td>
<td>(O(k \cdot nN \cdot d_{ks}))</td>
</tr>
</tbody>
</table>

**Performance.** We run the expand algorithm \(k \cdot n\) times for the generation of bootstrapping key (2-1), and we recall that a single expansion algorithm takes \(O(kd^2)\) polynomial operations. The total complexity is \(O(k^2 N \log N \cdot d^2) = \tilde{O}(k^2 Nn)\) scalar operations since one polynomial operation takes \(O(N \log N)\) scalar operations. The size of bootstrapping key grows quadratically with the number of parties since it consists of \(k \cdot n\) expanded RGSW ciphertexts each of which can be represented by \((2k + 2)d\) elements in \(T\). Hence the size of \(\mathbf{BK}\) is \((2k + 2)kNnd = \tilde{O}(k^2 Nn)\) torus elements.

We stress that the bootstrapping key \(\mathbf{BK}\) depends only on the public information (public/bootstrapping key) of involved parties and so it can be used repeatedly for any computations related to the same set of parties. In addition, the bootstrapping key can be reused when a new party joins: we only need to compute one additional row from the previously generated bootstrapping keys \(\overline{\mathbf{C}}_{i,t}\) instead of generating them from the beginning again.

The accumulator (2-3) recursively evaluates the external product between extended RLWE and RGSW ciphertexts \(k \cdot n\) times. As explained in the previous section, a single external product requires \(O(kd)\) polynomial operations. Hence the total complexity is \(O(k^2 nN \log N d) = \tilde{O}(k^2 nN)\) scalar operations.

Finally, \(\mathbf{KS}\) is the concatenation of \(k\) key-switching keys and the multi-key-switching algorithm has \(k\) times the complexity of the ordinary key-switching algorithm. The time and space complexity of our MKHE scheme is summarized in Table 2.

#### 4.2 Discussion

In the previous section, we introduced an MKHE scheme which supports the evaluation of a NAND gate. However, we can simply design some variants of this basic scheme with better functionality and versatility.

**More bootstrapped gates.** In this paper, we described only the Multi-Key bootstrapped NAND gate, but any arbitrary binary bootstrapped gate (such as AND, OR, XOR, etc.) can be evaluated in the same way, as it is done in TFHE: it is sufficient to modify the initial linear combination before bootstrapping.

**Time-Space Tradeoff.** We stress again that the size of an expanded bootstrapping key \(\mathbf{BK}\) grows quadratically with the number of parties, which is larger.
Multi-Key Homomorphic Encryption from TFHE

than the linear size of a simple concatenation of individual bootstrapping keys \( \{BK_j\}_{j \in [k]} \). Brakerski and Perlman [5] suggested a method to reduce down the memory requirement by generating a temporary evaluation key in each step. This idea can be applied to our scheme to have a linearly-growing space complexity, however, we lose the reusability of a expanded bootstrapping key and the total complexity grows.

**Threshold Decryption.** HE has some attractive applications in the construction of advanced cryptographic primitives such as round-efficient MPC [15, 1, 21, 30, 27]. In particular, the distributed property of threshold HE [23, 2] makes an important role to achieve this functionality.

Since our MKHE scheme is based on the standard LWE encryption, the techniques for threshold decryption such as noise smudging (a.k.a. noise flooding) [1] can be directly applied to our scheme as follows. The noise distribution, parametrized by a constant \( \gamma > 0 \), should have a medium size which is smaller than 1 but sufficiently larger than the error of an input ciphertext to prevent the leakage of extra information beyond the decrypted value. See [1] for parameter choice and security proof.

\[
\text{MKHE.PartDec}(\overline{ct}, s_i): \text{For a ciphertext } \overline{ct} = (b, a_1, \ldots, a_k) \in \mathbb{T}^{kn+1} \text{ and the } i\text{-th secret } s_i, \text{ sample an error } e_i \leftarrow D_\gamma \text{ and return the value } p_i = \langle a_i, s_i \rangle + e_i \pmod{1}.
\]

\[
\text{MKHE.Merge}(b, \{p_j\}_{j \in [k]}): \text{For the first entry } b \text{ of an input ciphertext and the partial decryptions } \{p_j\}_{j \in [k]}, \text{ output the bit } m \in \{0, 1\} \text{ which minimizes } |b + \sum_{j=1}^{k} p_j - \frac{1}{2}m|.
\]

**Faster Evaluation of a Look-Up Table (LUT).** There have been some progresses in TFHE-type schemes to accelerate the evaluation of a LUT. For example, Chillotti et al. [11] suggested a vertical packing method for TRLWE combined with a circuit bootstrapping algorithm which gives a speed-up compared to the gate-by-gate bootstrapping, while Bonnoron et al. [3] (see also [6]) suggested a method to encrypt more than one bit in a single ciphertext. It is easy to see that these techniques are directly applicable to our MKHE scheme.

5 Experimental Results

We present a proof-of-concept implementation to convince the reader that our scheme is practical. The implementation took a few days of coding and it is based on the TFHE library [12], which takes 13ms to execute a gate bootstrapping. All experiments are performed on a Intel Core i7-4910MQ at 2.90GHz laptop, running on a single thread. Our source code is publicly available at https://github.com/ilachill/MK-TFHE.

In Table 3, we present three candidate parameter sets which guarantee both the security and correctness. We set the LWE/RLWE secret distributions \( \chi \) and \( \psi \) as the uniform distributions over the set of binary vectors in \( \mathbb{Z}^n \) and over the polynomials in \( \mathbb{R} \) with binary coefficients, respectively. We adapt the same LWE
and RLWE parameters as in the TFHE implementation which achieve 152-bit security level based on the security analysis in [13]. We show in Appendix A that the standard deviation of bootstrapping error grows linearly on the number of parties. Hence the growth of parameter with respect to the maximal number of involved parties is very slow. We control the noise by changing the decomposition degree and exponent which do not affect the security level.

We adapt a space-time trade-off technique in [17, 10] which reduces the complexity of key-switching procedure by publishing all LWE encryptions of \(a \cdot B_{ks}^i \cdot t_j\) for \(i \in [d_{ks}], j \in [N]\), and \(a \in \mathbb{Z}_{B_{ks}}\), compared to the encryptions of \(B_{ks}^i \cdot t_j\) in the scheme description. Hence our implementation of multi-key-switching is purely represented by a summation of LWE vectors. It does not make any change in asymptotic complexity.

### Table 3. Recommended parameter sets.

<table>
<thead>
<tr>
<th>Set</th>
<th>(I)</th>
<th>(II)</th>
<th>(III)</th>
</tr>
</thead>
<tbody>
<tr>
<td>(n)</td>
<td>500</td>
<td>2.43 (\cdot) (10^{-5})</td>
<td>2^2</td>
</tr>
<tr>
<td>(\alpha)</td>
<td>4 (\cdot) (10^{-5})</td>
<td>8</td>
<td>1024</td>
</tr>
<tr>
<td>(B_{ks})</td>
<td>(2^7)</td>
<td>(2^6)</td>
<td>(2^4)</td>
</tr>
<tr>
<td>(d_{ks})</td>
<td>(2^7)</td>
<td>(2^6)</td>
<td>(2^4)</td>
</tr>
<tr>
<td>(N)</td>
<td>3.29 (\cdot) (10^{-10})</td>
<td>(3.29 \cdot 10^{-10})</td>
<td>(3.29 \cdot 10^{-10})</td>
</tr>
<tr>
<td>(\beta)</td>
<td>(2^7)</td>
<td>(2^6)</td>
<td>(2^4)</td>
</tr>
</tbody>
</table>

Our experimental results are summarized in Table 4. On the left side of the table, we describe the local complexity of our scheme such as key generation timing of each party. This part is independent from \(k\). The rest of the table presents the global performance of our scheme corresponding to the multi-key operation. The parameter sets I, II and III support homomorphic computation for any number of parties up to 2, 4 and 8, respectively. A smaller parameter has a better performance but a larger parameter makes the scheme more flexible. For example, III takes twice as long compared to I in the two-party case, but...
it allows more parties to join the computation dynamically. We observe that the size of ciphertexts and bootstrapping timing grow linearly and quadratically with $k$, as expected.

References

For the decomposition base $B$ and degree $d$, let $\epsilon^2 = 1/(12B^{2d})$ be the variance of uniform distribution over the interval $(-1/2B^d, 1/2B^d)$. We denote by $V_B = \begin{cases} \frac{1}{12} (B^2 - 1) & \text{if } B \text{ is odd;} \\ \frac{1}{12} (B^2 + 2) & \text{if } B \text{ is even;} \end{cases}$ the mean square of a uniform distribution over $\mathbb{Z} \cap (-1/2B^{d}, 1/2B^{d})$. [1]
$$(-B/2, B/2]$$. We similarly define $$c_{ks}^2$$ and $$V_{ks}$$ based on the parameter $$B_{ks}$$ and $$d_{ks}$$ for the key-switching algorithm. We set the RGSW and LWE secret distributions $$\chi, \psi$$ as uniform distributions over $$\{0,1\}^N$$ and $$\{0,1\}^n$$, respectively.

The variance of a random variable $$e$$ over $$\mathbb{R}$$ is denoted by $$\text{Var}(e)$$. For a random variable $$e$$ over $$\mathbb{R}[X]/(X^N + 1)$$, it denotes the variance of a coefficient when all coefficients have the same variance. If $$e$$ is a vector of random variables, $$\text{Var}(e)$$ denotes the maximum of its entries’ variances.

We mainly compute the variance of a noise. Our average-case analysis is based on the heuristic assumption that a noise behaves like a Gaussian distribution, which has been empirically shown in the previous work (Fig. 11, [13]).

Ciphertext Expansion. Let us suppose that

$$(z_j, P_j) \leftarrow \text{RGSW.KeyGen}(pp_{\text{RGSW}}) \text{ for } j \in [k],$$

$$(C, D, F) \leftarrow \text{RGSW.UniEnc}(\mu, z_i, P_i) \text{ for } \mu \in \mathbb{R}, \text{ and}$$

$$\overline{C} \leftarrow \text{RGSW.Expand}(C, D, F, \{P_j\}_{j \in [k]}).$$

Then, we have

$$c_0 + z_i \cdot c_1 = Cz_i = \mu \cdot g + e_c \quad (\text{mod } 1),$$

$$d_0 + z_i \cdot d_1 = Dz_i = \mu z_i \cdot g + r \cdot e_i + E_i z_i \quad (\text{mod } 1),$$

and

$$(d_0 + x_j) + z_i \cdot y_j + z_j \cdot d_1 = (d_0 + z_j \cdot d_1) + (x_j + z_i \cdot y_j) \quad (\text{mod } 1)$$

$$= (0|\mu \cdot g| + r \cdot P_i + E_i)z_j + M_j Fz_i \quad (\text{mod } 1)$$

$$= \mu z_j \cdot g + r \cdot (b_i + z_j \cdot a) + E_i z_j + M_j(r \cdot g + e_f) \quad (\text{mod } 1)$$

$$= \mu z_j \cdot g + r \cdot (b_i - b_j) + r \cdot e_j + E_i z_j + r \cdot (b_j - b_i + e') + M_j e_f \quad (\text{mod } 1)$$

$$= \mu z_j \cdot g + (r \cdot (e_j + e') + E_i z_j + M_j e_f) \quad (\text{mod } 1),$$

where $$e' \in \mathbb{R}^d$$ is the error vector such that $$e'[\ell] = (g^{-1}(b_j[\ell] - b_i[\ell]), g) - (b_j[\ell] - b_i[\ell])$$ for $$\ell \in [d]$$.

A noise in the third formula has the largest variance. It is computed by

$$\text{Var}(r \cdot (e_j + e') + E_i z_j + M_j e_f) = (N/2)e^2 + (1 + N + dNv_B)\beta^2$$

since $$\text{Var}(r) = 1/2, \text{Var}(e_j) = \beta^2, \text{Var}(e') = c^2, \text{Var}(E_i z_j) = (1 + N/2)\beta^2,$$ and

$$\text{Var}(M_j e_f) = dN \cdot V_B \cdot \beta^2.$$

Rounding Error. In (2-2), we compute $$\tilde{b} = \lfloor 2N \cdot b' \rfloor \text{ and } \tilde{a}_i = \lfloor 2N \cdot a_i' \rfloor$$. We assume that each of the rounding errors behaves like a uniform random variable on the interval $$\mathbb{R} \text{ (mod } 1) = (-0.5, 0.5]$$. Therefore, the total rounding error $$\tilde{b} - \lfloor 2N \cdot b' \rfloor + \sum_{j=1}^k(\tilde{a}_j - \lfloor 2N \cdot a_j' \rfloor, s_j)$$ has the variance of $$\frac{1}{12}(1 + kn/2)$$.

External Product. We consider the external product between an (extended) RLWE and RGSW ciphertexts $$\overline{c}$$ and $$\overline{C}$$. The RGSW ciphertext will satisfy $$\overline{C}z =
\[ \mu \cdot G_{k+1} z + \sigma \] for a plaintext \( \mu \) and an error vector \( \sigma \). We denote by \( \text{VarErr}(\mathcal{C}) = \text{Var}(\mathcal{e}) \). The external product outputs an RLWE ciphertext \( \mathcal{e}' \) satisfying

\[
\langle \mathcal{e}', \mathcal{z} \rangle = G_{k+1}^{-1}(\mathcal{e}) \cdot \mathcal{C} \mathcal{z} \quad (\text{mod 1})
\]

\[
= G_{k+1}^{-1}(\mathcal{e}) \cdot (\mu \cdot G_{k+1} z + \sigma) \quad (\text{mod 1})
\]

\[
= \mu \cdot \langle \mathcal{e}, \mathcal{z} \rangle + (\mu \cdot \langle \mathcal{e}', \mathcal{z} \rangle + G_{k+1}^{-1}(\mathcal{e}) \cdot \sigma) \quad (\text{mod 1})
\]

for the decomposition error \( \mathcal{e}' = G_{k+1}^{-1}(\mathcal{e}) \cdot G_{k+1} - \mathcal{e} \). Therefore, the variance of external product error \( e_{\text{ep}} = \mu \cdot \langle \mathcal{e}', \mathcal{z} \rangle + G_{k+1}^{-1}(\mathcal{e}) \cdot \sigma \) is

\[
\text{Var}(e_{\text{ep}}) = \mu^2 \cdot \sigma^2 (1 + kN/2) + (k + 1)dN \cdot V_B \cdot \text{VarErr}(\mathcal{C})
\]

since \( \text{Var}(\mathcal{e}) = \sigma^2 \) and \( \text{Var}(G_{k+1}^{-1}(\mathcal{e})) = V_B \). Note that we do not include the error in the phase \( \langle \mathcal{e}, \mathcal{z} \rangle \) of the input RLWE ciphertext for simpler analysis of Mux gate.

**Mux Gate.** Suppose that \( \mathcal{C}_0, \mathcal{C}_1 \) are RLWE ciphertexts and \( \mathcal{C} \) is an RGSW encryption of \( \mu \in \{0, 1\} \) with error \( \mathcal{e} \). The mux gate is to compute \( \mathcal{e} = \mathcal{C}_0 + \text{RGSW.ExtProd}(\mathcal{C}_1 - \mathcal{C}_0, \mathcal{C}) \) to choose \( \mathcal{e} \cdot \mu \) homomorphically:

\[
\langle \mathcal{e}, \mathcal{z} \rangle = \langle \mathcal{C}_0, \mathcal{z} \rangle + G_{k+1}^{-1}(\mathcal{C}_1 - \mathcal{C}_0) \cdot (\mu \cdot G_{k+1} z + \sigma) \quad (\text{mod 1})
\]

\[
= (1 - \mu) \cdot \langle \mathcal{C}_0, \mathcal{z} \rangle + \mu \cdot \langle \mathcal{C}_1, \mathcal{z} \rangle + (\mu \cdot \langle \mathcal{e}', \mathcal{z} \rangle + G_{k+1}^{-1}(\mathcal{e}_1 - \mathcal{e}_0) \cdot \sigma) \quad (\text{mod 1}),
\]

for the decomposition error \( \mathcal{e}' = G_{k+1}^{-1}(\mathcal{e}_1 - \mathcal{e}_0) \cdot G_{k+1} - (\mathcal{e}_1 - \mathcal{e}_0) \). The noise has the variance of \( \mu^2 \cdot \sigma^2 (1 + kN/2) + (k + 1)dN \cdot V_B \cdot \text{VarErr}(\mathcal{C}) \), exactly the same as external product.

**Accumulation.** The initial RLWE ciphertext has no noise. All bootstrapping keys \( \mathcal{C}_{i, \ell} \) have the same variance of noise \( \text{VarErr}(\mathcal{C}_{i, \ell}) = (N/2)\sigma^2 + (1 + N + dN)B \cdot \sigma^2 \) from the expansion algorithm. We recursively evaluate the mux gate \( k \cdot n \) times and an encrypted secret \( s_{i, \ell} \) is sampled uniformly from \( \{0, 1\} \). Therefore, the output of accumulator has an error of variance

\[
\frac{1}{2} k n \cdot \sigma^2 (1 + kN/2) + (k + 1) k d n N \cdot V_B \cdot ((N/2)\sigma^2 + (1 + N + dN)B \cdot \sigma^2).
\]

**Multi-Key Switching.** Let \( \mathcal{C} = (b, a_1, \ldots, a_k) \) be an input LWE ciphertext and \( \mathcal{C}' = (b', a_1', \ldots, a_k') \) be the output of multi-key-switching algorithm. Then, we have

\[
\langle \mathcal{C}', (1, \pi) \rangle = b + \sum_{i=1}^{k} (b_i' + \langle a_i', s_i \rangle) \quad (\text{mod 1})
\]

\[
= b + \sum_{i=1}^{k} \sum_{j=1}^{n} \langle g_{k+1}^{-1}(a_{i,j}), t_{i,j} \cdot g_k + e_{i,j} \rangle \quad (\text{mod 1})
\]

\[
= \langle \mathcal{C}, (1, \mathcal{U}) \rangle + \sum_{i=1}^{k} \sum_{j=1}^{n} \left( t_{i,j} \cdot e_{i,j}' + \langle g_{k+1}^{-1}(a_{i,j}), e_{i,j} \rangle \right) \quad (\text{mod 1})
\]
for the decomposition error \( e'_{i,j} = \langle g_{ks}^{-1}(a_{i,j}), g \rangle - a_{i,j} \). As a result, the variance of a multi-key-switching error \( e_{ks} = \sum_{i=1}^{k} \sum_{j=1}^{N} (t_{i,j} \cdot e'_{i,j} + \langle g_{ks}^{-1}(a_{i,j}), e_{i,j} \rangle) \) is obtained by

\[
\text{Var}(e_{ks}) = kN \left( \frac{1}{2} \epsilon_{ks}^2 + d_{ks} V_{B_k} \alpha^2 \right). \tag{2}
\]

We note that this term does not include the error of input LWE ciphertext. If \( \langle ct', (1, \overline{s}) \rangle = \frac{1}{2}m + e \mod 1 \) for a bit \( m \in \{0, 1\} \) and an error \( e \in \mathbb{R} \), then \( ct' \) will be an encryption of the same message \( m \) with error \( e' = e + e_{ks} \).

**Multi-Key Switching (modified).** Different from the previous algorithm, the key-switching key of the \( i \)-th party consists of LWE encryptions of \( a \cdot B_{ks}^j \cdot t_{i,j} \) for \( 1 \leq j \leq N, 0 \leq \ell < d_{ks} \) and \( a \in \mathbb{Z}_{B_k} \), encrypted under the secret \( s_i \). For an input LWE ciphertext \( \overline{ct} = (b, a_1, \ldots, a_k) \), the (modified) multi-key switching algorithm computes \( g^{-1}(a_{i,j}) = (a_{i,j, \ell})_{0 \leq \ell < d_{ks}} \) for each \( 1 \leq i \leq k \) and \( 1 \leq j \leq N \), and then compute the summation of LWE encryptions of \( a_{i,j, \ell} \cdot B_{ks}^j \cdot t_{i,j} \) for \( 1 \leq i \leq k, 1 \leq j \leq N \) and \( 0 \leq \ell < d_{ks} \). Therefore, the output ciphertext \( \overline{ct}' \) satisfies that

\[
\langle \overline{ct}', (1, \overline{s}) \rangle = b + \sum_{i=1}^{k} \sum_{j=1}^{N} \sum_{\ell=0}^{d_{ks}-1} g_{ks}^{-1}(a_{i,j})[\ell] \cdot B_{ks}^j \cdot t_{i,j} + e_{i,j,a_{i,j, \ell}} \mod 1
\]

\[
= b + \sum_{i=1}^{k} \sum_{j=1}^{N} (a_{i,j} + e'_{i,j}) \cdot t_{i,j} + \sum_{i=1}^{k} \sum_{j=1}^{N} \sum_{\ell=0}^{d_{ks}-1} e_{i,j,a_{i,j, \ell}} \mod 1
\]

\[
= \langle \overline{ct}, (1, \overline{s}) \rangle + \left( \sum_{i=1}^{k} \sum_{j=1}^{N} t_{i,j} \cdot e'_{i,j} + \sum_{i=1}^{k} \sum_{j=1}^{N} \sum_{\ell=0}^{d_{ks}-1} e_{i,j,a_{i,j, \ell}} \right) \mod 1,
\]

for the decomposition error \( e'_{i,j} = \langle g_{ks}^{-1}(a_{i,j}), g \rangle - a_{i,j} \). As a result, the variance of a multi-key-switching error \( e_{ks} = \sum_{i=1}^{k} \sum_{j=1}^{N} t_{i,j} \cdot e'_{i,j} + \sum_{i=1}^{k} \sum_{j=1}^{N} \sum_{\ell=0}^{d_{ks}-1} e_{i,j,a_{i,j, \ell}} \) is obtained by

\[
\text{Var}(e_{ks}) = kN \left( \frac{1}{2} \epsilon_{ks}^2 + d_{ks} V_{B_k} \alpha^2 \right), \tag{3}
\]

which is smaller than that of standard key-switching error (2).

**Bootstrapping.** The bootstrapping noise is simply the sum of the accumulation and multi-key-switching errors so that it has the variance of (1) + (3).